

April 1988 Revised July 1999

## 74F32

## **Quad 2-Input OR Gate**

#### **General Description**

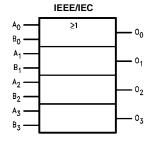
This device contains four independent gates, each of which performs the logic OR function.

## **Ordering Code:**

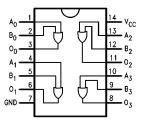
Order Number	Package Number	Package Description
74F32SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
74F32SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74F32PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

#### **Logic Symbol**



#### **Connection Diagram**



## **Unit Loading/Fan Out**

Pin Names	Description	U.L. HIGH/LOW	Input I <sub>IH</sub> /I <sub>IL</sub> Output I <sub>OH</sub> /I <sub>OL</sub>		
A <sub>n</sub> , B <sub>n</sub>	Inputs	1.0/1.0	20 μA/-0.6 mA		
O <sub>n</sub>	Outputs	50/33.3	−1 mA/20 mA		

#### **Absolute Maximum Ratings**(Note 1)

Storage Temperature  $-65^{\circ}\text{C} \text{ to } +150^{\circ}\text{C}$ 

 $\begin{array}{lll} \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to } +125^{\circ}\mbox{C} \\ \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \mbox{V}_{\mbox{CC}} \mbox{ Pin Potential to Ground Pin} & -0.5\mbox{V to } +7.0\mbox{V} \\ \end{array}$ 

Input Voltage (Note 2) -0.5V to +7.0 V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with  $V_{CC} = 0V$ )

 $\begin{array}{ll} \mbox{Standard Output} & -0.5\mbox{V to V}_{\mbox{CC}} \\ \mbox{3-STATE Output} & -0.5\mbox{V to } +5.5\mbox{V} \end{array}$ 

Current Applied to Output

in LOW State (Max) twice the rated  $I_{OL}$  (mA) ESD Last Passing Voltage (Min) 4000V

# Recommended Operating Conditions

Free Air Ambient Temperature  $0^{\circ}$ C to  $+70^{\circ}$ C Supply Voltage +4.5V to +5.5V

**Note 1:** Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

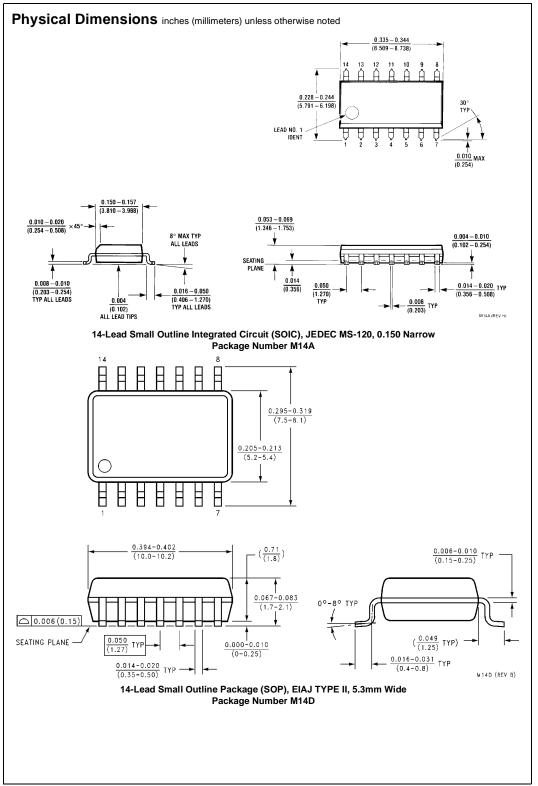
Note 2: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

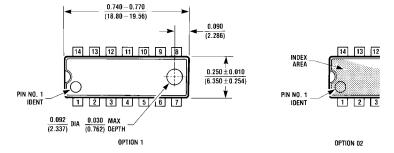
Symbol	ol Parameter		Тур	Max	Units	v <sub>cc</sub>	Conditions		
V <sub>IH</sub>	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal		
V <sub>IL</sub>	Input LOW Voltage			0.8	V		Recognized as a LOW Signal		
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	I <sub>IN</sub> = -18 mA		
V <sub>OH</sub>	Output HIGH 10% V <sub>CI</sub>	C 2.5			V	Min	I <sub>OH</sub> = -1 mA		
	Voltage 5% V <sub>CI</sub>	2.7					$I_{OH} = -1 \text{ mA}$		
V <sub>OL</sub>	Output LOW 10% V <sub>Cl</sub> Voltage	С		0.5	V	Min	I <sub>OL</sub> = 20 mA		
I <sub>IH</sub>	Input HIGH Current			5.0	μА	Max	V <sub>IN</sub> = 2.7V		
I <sub>BVI</sub>	Input HIGH Current Breakdown Test			7.0	μА	Max	V <sub>IN</sub> = 7.0V		
I <sub>CEX</sub>	Output HIGH Leakage Current			50	μА	Max	V <sub>OUT</sub> = V <sub>CC</sub>		
V <sub>ID</sub>	Input Leakage Test	4.75			V	0.0	$I_{ID} = 1.9 \mu A$ All Other Pins Grounded		
I <sub>OD</sub>	Output Leakage Circuit Current			3.75	μА	0.0	V <sub>IOD</sub> = 150 mV All Other Pins Grounded		
I <sub>IL</sub>	Input LOW Current			-0.6	mA	Max	V <sub>IN</sub> = 0.5V		
Ios	Output Short-Circuit Current	-60		-150	mA	Max	V <sub>OUT</sub> = 0V		
I <sub>CCH</sub>	Power Supply Current		6.1	9.2	mA	Max	V <sub>O</sub> = HIGH		
I <sub>CCL</sub>	Power Supply Current		10.3	15.5	mA	Max	$V_O = LOW$		

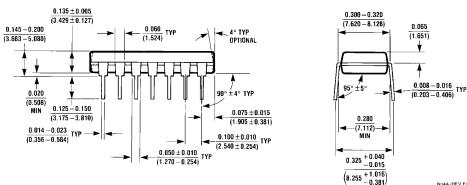
#### **AC Electrical Characteristics**

Symbol	Parameter	$T_{A} = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$			$T_A = -55$ °C to +125°C $V_{CC} = +5.0V$ $C_L = 50$ pF		$T_A = 0$ °C to +70°C $V_{CC} = +5.0V$ $C_L = 50$ pF		Units
		Min	Тур	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	3.0	4.2	5.6	3.0	7.5	3.0	6.6	no
t <sub>PHL</sub>	A <sub>n</sub> , B <sub>n</sub> to O <sub>n</sub>	3.0	4.0	5.3	2.5	7.5	3.0	6.3	ns



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)





14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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N144 (REV.F)