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- Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.
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- The contents of this catalog are applicable to the products which are purchased from our sales offices or distributors (so called "TAIYO YUDEN's official sales channel").
  - It is only applicable to the products purchased from any of TAIYO YUDEN's official sales channel.
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# **MULTILAYER CERAMIC CAPACITORS**



#### ■PARTS NUMBER

J	M	K	3	1	6	Δ	В	J	1	0	6	М	L	_	Т	Δ
1	2	3		4		(5)	(	3		7		8	9	10	11)	12

△=Blank space

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U	יע	٦a	rec	ı vo	ıtag

Code	Rated voltage[VDC]
Р	2.5
Α	4
J	6.3
L	10
E	16
Т	25
G	35
U	50
Н	100
Q	250
S	630

• • • • • • • • • • • • • • • • • • • •	

3End termination

Code	End termination
К	Plated
S	Cu Internal Electrodes

#### 4Dimension (L × W)

Туре	Dimensions (L×W)[mm]	EIA(inch)
042	0.4 × 0.2	01005
063	0.6 × 0.3	0201
105	1.0 × 0.5	0402
105	0.52 × 1.0 💥	0204
107	1.6 × 0.8	0603
107	0.8 × 1.6 💥	0306
212	2.0 × 1.25	0805
212	1.25 × 2.0 💥	0508
316	3.2 × 1.6	1206
325	3.2 × 2.5	1210
432	4.5 × 3.2	1812
N N	. (□1404) 1	_

Note: ※LW reverse type(□WK) only

#### 2Series name

Code	Series name
М	Multilayer ceramic capacitor
V	Multilayer ceramic capacitor for high frequency
W	LW reverse type multilayer capacitor

#### ⑤Dimension tolerance

Code	Type	L[mm]	W[mm]	T[mm]
Δ	ALL	Standard	Standard	Standard
	063	0.6±0.05	0.3±0.05	0.3±0.05
	105	1.0±0.10	0.5±0.10	0.5±0.10
	107	1.6+0.15/-0.05	0.8+0.15/-0.05	0.8+0.15/-0.05
				0.45±0.05
Α	212	2.0+0.15/-0.05	1.25 + 0.15 / -0.05	0.85±0.10
				1.25 + 0.15 / -0.05
	316	3.2±0.20	1.6±0.20	0.85±0.10
	310	3.2 ± 0.20	1.0 ± 0.20	1.6±0.20
	325	3.2±0.30	2.5±0.30	2.5±0.30
	063	0.6±0.09	0.3±0.09	0.3±0.09
	105	1.0+0.15/-0.05	0.5+0.15/-0.05	0.5+0.15/-0.05
	107	1.6+0.20/-0	0.8+0.20/-0	0.45±0.05
В	107	1.6 + 0.20/ = 0	0.8 + 0.20/ - 0	0.8+0.20/-0
ь				0.45±0.05
_	212	2.0+0.20/-0	1.25 + 0.20 / -0	0.85±0.10
				1.25+0.20/-0
	316	3.2±0.30	1.6±0.30	1.6±0.30
С	105	1.0+0.20/-0	0.5+0.20/-0	0.5+0.20/-0

Note: P.6 Standard external dimensions

∆= Blank space

#### **6**Temperature characteristics code

■ High dielectric type (Excluding Super low distortion multilayer ceramic capacitor (CFCAP<sup>TM</sup>))

Code	Applicable standard		Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code							
	JIS	В	-25 <b>~</b> + 85	20	±10%	±10%	K							
BJ	010	ь	25.4 1 65	20	±10%	±20%	М							
ВО	EIA	X5R	-55 <b>~</b> + 85	25	±15%	±10%	K							
	LIX	AUK	-55° + 65	25	±15%	±20%	М							
В7	EIA	X7R	-55 <b>~</b> +125	25	±15%	±10%	K							
			-557 <del>-</del> +125			±20%	М							
C6	EIA	X6S	-55~+105	25	±22%	±10%	K							
Co						±20%	М							
	EIA	X7S	-55~+125	25	±22%	±10%	K							
C/			-55 <b>~</b> +125			±20%	М							
. 5010										55   05	0.5		±10%	K
LD(※)	EIA	X5R	−55 <b>~</b> + 85	25	±15%	±20%	М							
ΔF	JIS	F	-25~+ 85	20	+30/-80%	+80/-20%	Z							
ΔF	EIA	Y5V	<b>−30~+</b> 85	25	+22/-82%	+80/-20%	Z							

Note: X.LD Low distortion high value multilayer ceramic capacitor

Δ= Blank space

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■Temperature c	ompensating typ
Codo	Applicable

Code	Appli	cable dard	Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code
						±0.1pF	В
						±0.25pF	С
CG	EIA	C0G	-55~+125	25	$0\pm30$ ppm/°C	±0.5pF	D
						±1pF	F
						±5%	J
						±0.1pF	В
СН	JIS	CH		20	0±60ppm/°C	±0.25pF	С
			-55 <b>~</b> +125			±0.5pF	D
	EIA	А СОН	-557 <b>-</b> +125	25		±1pF	F
						±5%	J
						±10%	K
CJ	JIS	CJ	-55~+125	20	0±120ppm/°C	±0.25pF	С
	EIA	C0J	35.4 1 123	25	0±120ррП/ С	±0.23pi	U
CK	JIS	CK	-55 <b>~</b> +125	20	0±250ppm/°C	±0.25pF	С
	EIA	C0J	-55° +125	25	0±230ррпі/ С	±0.25pr	L
	JIS	UJ		20		±0.25pF	С
UJ	EIA	U2J	$-55 \sim +125$	25	$-750 \pm 120 \text{ppm/}^{\circ}\text{C}$	±0.5pF	D
	EIA	023		20		±5%	J
UK	JIS	UK	-55 <b>~</b> +125	20	-750±250ppm/°C	±0.5pF	С
	EIA	U2K	-55 <b>~</b> +125	25	— /30 ± 250ppm/ C	±0.5pF	0
SL	JIS	SL	-55 <b>~</b> +125	20	+350~−1000ppm/°C	±5%	J

#### 6 Series code

(Super low distortion multilayer ceramic capacitor(CFCAP $^{\text{TM}}$ ) only)

(Gabet tett alee	or drong martinay or containing supulsition (cr. cr. ii	,, ,
Code	Series code	
SD	Standard	

#### • Medium-High Voltage Multilayer Ceramic Capacitors

Code	Series code
SD	Standard

#### 7Nominal capacitance

©	
Code (example)	Nominal capacitance
	0.F. F.
0R5	0.5pF
010	1pF
100	10pF
101	100pF
102	1,000pF
103	10,000pF
104	0.1 μ F
105	1.0 μ F
106	10 μ F
107	100 μ F

Note : R=Decimal point

#### **8**Capacitance tolerance

© Capacitance tolerance							
Code	Capacitance tolerance						
В	±0.1pF						
С	±0.25pF						
D	±0.5pF						
F	±1pF						
G	±2%						
J	±5%						
К	±10%						
М	±20%						
Z	+80/-20%						

#### Thickness

Code	Thickness[mm]
С	0.2
D	0.2
Р	0.3
Т	0.3
K	0.45
V	0.5
W	0.5
Α	0.8
D	0.85(212type or more)
F	1.15
G	1.25
L	1.6
N	1.9
Υ	2.0 max
М	2.5

#### ®Special code

19 Opoolal oodo	
Code	Special code
_	Standard

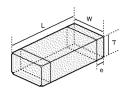
#### 11)Packaging

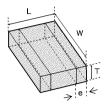
Code	Packaging						
F	$\phi$ 178mm Taping (2mm pitch)						
Т	$\phi$ 178mm Taping (4mm pitch)						
P	$\phi$ 178mm Taping (4mm pitch, 1000 pcs/reel)						
'	325 type (Thickness code M)						
W	φ 178mm Taping (1mm pitch) 042type only						

#### ①Internal code

Code	Internal code
Δ	Standard

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T (FIA)	Dimension [mm]								
Type( EIA )	L	W	T	*1	е				
□MK042(01005)	0.4±0.02	0.2±0.02	0.2±0.02	C D	0.1±0.03				
□VS042(01005)	0.4±0.02	0.2±0.02	0.2±0.02	С	0.1±0.03				
□MK063(0201)	0.6±0.03	0.3±0.03	0.3±0.03	P T	0.15±0.05				
			0.2±0.02	С					
□MK105(0402)	1.0±0.05	$0.5 \pm 0.05$	0.3±0.03	Р	$0.25 \pm 0.10$				
			0.5±0.05	٧					
□VK105(0402)	1.0±0.05	0.5±0.05	0.5±0.05	W	0.25±0.10				
□WK105(0204)※	0.52±0.05	1.0±0.05	0.3±0.05	Р	0.18±0.08				
□MK107(0603)	1.6±0.10	0.8±0.10	0.45±0.05	K	0.35±0.25				
	1.0_0.10	0.0 _ 0.10	0.8±0.10	Α	0.00 _ 0.20				
□WK107(0306)※	0.8±0.10	1.6±0.10	0.5±0.05	V	0.25±0.15				
		1.25±0.10	0.45±0.05	K					
□MK212(0805)	2.0±0.10		0.85±0.10	D	$0.5 \pm 0.25$				
			1.25±0.10	G					
□WK212(0508)※	1.25±0.15	2.0±0.15	$0.85 \pm 0.1$	D	0.3±0.2				
			0.85±0.10	D					
□MK316(1206)	3.2±0.15	1.6±0.15	1.15±0.10	F	0.5+0.35/-0.25				
□MK310(1200)	3.2 ± 0.13	1.0 ± 0.15	1.25±0.10	G	0.0+0.33/ -0.23				
			1.6±0.20	L					
			0.85±0.10	D					
			1.15±0.10	F					
□MK325(1210)	3.2±0.30	2.5±0.20	1.9±0.20	N	$0.6 \pm 0.3$				
			1.9+0.1/-0.2	Υ					
			2.5±0.20	М					
□MK432(1812)	4.5±0.40	3.2±0.30	2.5±0.20	М	0.9±0.6				

Note: ※. LW reverse type, \*1.Thickness code

#### STANDARD QUANTITY

Tuna	EIA (inch)	Dime	ension	Standard q	uantity[pcs]	
Туре	EIA (Inch)	[mm]	Code	Paper tape	Embossed tape	
042	01005	0.2	С		40000	
042 01003	01005	0.2	D	_	40000	
063	0201	0.3	Р	15000	_	
003	0201	0.3	Т	13000	_	
		0.2	С	20000	40000 4000 4000 3000 3000	
	0402	0.3	Р	15000	_	
105	0402	0.5	V			
		0.5	W	10000	_	
	0204 ※	0.30	Р		### Embossed tape #### 40000  ###########################	
	0602	0.45	K	4000	40000 4000 3000 3000 3000 2000	
107	0603	0.8	Α	4000		
	0306 ※	0.50	V	_		
		0.45	K	4000	_	
010	0805	0.85	D	4000		
212		1.25	G	_	3000	
	0508 ※	0.85	D	4000	_	
		0.85	D	4000	_	
010	1000	1.15	F			
316	1206	1.25	G	_		
		1.6	L	_		
		0.85	D			
		1.15	F		2000	
325	1210	1.9	N	_	2000	
		2.0 max	Υ			
		2.5	М	_	500(T), 1000(F	
432	1812	2.5	М	_	500	

Note : ※.LW Reverse type(□WK)

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#### **316TYPE**

[Temperature Characteristic LD : X5R] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
UMK316 LD105∏L-T		50	X5R	1 μ	±10, ±20	10	150	1.6±0.20	R
GMK316BLD475□L-T		35	X5R	4.7 μ	±10, ±20	10	150	1.6±0.30	R
TMK316BLD106□L-T		25	X5R	10 μ	±10, ±20	10	150	1.6±0.30	R

[Temperature Characteristic LD : X5R] 1.9mm thickness(N)

Ī	Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
ι	JMK325 LD105∏N-T		50		X5R	1 μ	±10, ±20	10	200	1.9±0.20	R

[Temperature Characteristic LD : X5R] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
UMK325 LD155∏M-T		- 50		X5R	1.5 μ	±10, ±20	5	150	2.5±0.20	R
UMK325 LD475∏M-T		50		X5R	4.7 μ	±10, ±20	10	200	2.5±0.20	R

# Medium-High Voltage Multilaver Ceramic Capacitors ■105TYPE

[Temperature Characteristic B7 : X7R] 0.5mm thickness(V)

Part number 1		D . I II D.	Tempe	rature	Capacitance	Capacitance	tan δ	HTLT	*3 - 3	Soldering
Part number I	Part number 2	Rated voltage [V]	characte	eristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK105 B7221 ŪV-F				X7R	220 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7331 ŪV-F				X7R	330 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7471 ŪV−F				X7R	470 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7681 ŪV-F				X7R	680 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7102 U-F		100		X7R	1000 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7152 U-F				X7R	1500 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7222 ŪV-F				X7R	2200 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7332∏V-F				X7R	3300 р	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7472 ŪV-F				X7R	4700 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R

[Temperature Characteristic CG: CG/C0G] 0.5mm thickness(V)

Destruction 1	Part number 2	Rated voltage [V]	Tempe	erature	Capacitance	Capacitance	Q (-t 1MH-)	HTLT	*3 - 7	Soldering
Part number 1	Part number 2	Rated Voltage [V]	charact	teristics	[F]	tolerance [%]	(at 1MHz) min	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK105 CG080DV-F			CG	C0G	8 p	±0.5pF	560	200	$0.5 \pm 0.05$	R
HMK105 CG090DV-F			CG	COG	9 p	±0.5pF	580	200	0.5±0.05	R
HMK105 CG100DV-F			CG	COG	10 p	±0.5pF	600	200	0.5±0.05	R
HMK105 CG120JV-F		100	CG	COG	12 p	±5%	640	200	0.5±0.05	R
HMK105 CG150JV-F		100	CG	COG	15 p	±5%	700	200	0.5±0.05	R
HMK105 CG180JV-F			CG	COG	18 p	±5%	760	200	0.5±0.05	R
HMK105 CG220JV-F			CG	C0G	22 p	±5%	840	200	$0.5 \pm 0.05$	R
HMK105 CG240JV-F			CG	COG	24 p	±5%	880	200	0.5±0.05	R

#### ●107TYPE

[Temperature Characteristic BJ : B/X5R] 0.8mm thickness(A)

Part number 1	Part number 2	Rated voltage IVII	Tempe	erature	Capacitance	Capacitance	tan δ	HTLT	*3 - 1	Soldering R:Reflow
Part number I	Part number 2	Rated Voltage [V]	charact	eristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	W:Wave
HMK107 BJ102□A-T			В	X5R*1	1000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ152□A-T			В	X5R*1	1500 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ222□A-T			В	X5R*1	2200 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ332□A-T			В	X5R*1	3300 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ472□A-T			В	X5R*1	4700 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ682□A-T		100	В	X5R*1	6800 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ103∏A-T		100	В	X5R*1	10000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ153∏A-T			В	X5R*1	15000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ223□A-T			В	X5R*1	22000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ333∏A-T			В	X5R*1	33000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ473□A-T			В	X5R*1	47000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ104□A-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	0.8±0.10	R

[Temperature Characteristic B7 : X/R ] U.8mm thickness (A)												
Part number 1	Part number 2	Rated voltage [V]	Temperature	Capacitance	Capacitance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow			
T die Hamber 1	T dre Hamber 2	Nated Voltage [V]	characteristic	[F]	tolerance [%]	[%]	Rated voltage x %	Triickiiess [iiiii]	W:Wave			
HMK107 B7102∏A-T			X7F	1000 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R			
HMK107 B7152[]A-T			X7F	1500 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7222∏A-T			X7F	2200 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7332∏A-T			X7F	3300 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7472∏A-T			X7F	4700 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7682[]A-T		100	X7F	6800 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7103[]A-T		100	X7F	10000 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7153[]A-T			X7F	15000 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7223∏A-T			X7F	22000 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7333∏A-T			X7F	33000 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7473[]A-T			X7F	47000 p	±10, ±20	3.5	200	0.8±0.10	R			
HMK107 B7104∏A-T	•		X7F	0.1 μ	±10, ±20	3.5	200	0.8±0.10	R			

【Temperature Characteristic SD : Standard 】 0.8mm thickness(A)

Tremperature on a actensic ob . Standard 7 C. Online trickness (A)												
	Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow		
					2			Rated Voltage x %		W:Wave		
Н	MK107 SD101KA-T				100 p	±10	0.1	200	$0.8 \pm 0.10$	R		
Н	MK107 SD121KA-T		100	Standard Type	120 p	±10	0.1	200	$0.8 \pm 0.10$	R		
Н	MK107 SD151KA-T		100	Standard Type	150 p	±10	0.1	200	0.8±0.10	R		
Н	MK107 SD181KA-T				180 p	±10	0.1	200	0.8±0.10	R		

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Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK107 SD221KA-T				220 p	±10	0.1	200	0.8±0.10	R
HMK107 SD271KA-T				270 p	±10	0.1	200	0.8±0.10	R
HMK107 SD331KA-T				330 p	±10	0.1	200	0.8±0.10	R
HMK107 SD391KA-T				390 р	±10	0.1	200	0.8±0.10	R
HMK107 SD471KA-T		100	Standard Type	470 p	±10	0.1	200	0.8±0.10	R
HMK107 SD561KA-T				560 p	±10	0.1	200	0.8±0.10	R
HMK107 SD681KA-T				680 p	±10	0.1	200	0.8±0.10	R
HMK107 SD821KA-T				820 p	±10	0.1	200	0.8±0.10	R
HMK107 SD102KA-T				1000 p	±10	0.1	200	0.8±0.10	R

#### ●212TYPE

[Temperature Characteristic BJ : B/X5R] 1.25mm thickness (G)

	Part number 1	Part number 2	Rated voltage [V]	Tempe	erature	Capacitance	Capacitance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
	Fart number 1	Part number 2	Nated Voltage [V]	charact	teristics	[F]	tolerance [%]	[%]	Rated voltage x %	Inickness [mm]	W:Wave
Н	MK212 BJ103[]G-T			В	X5R*1	10000 p	±10, ±20	3.5	200	1.25±0.10	R
Н	MK212 BJ153∏G-T			В	X5R*1	15000 p	±10, ±20	3.5	200	1.25±0.10	R
Н	MK212 BJ223∏G-T			В	X5R*1	22000 p	±10, ±20	3.5	200	1.25±0.10	R
Н	MK212 BJ333∏G-T		100	В	X5R*1	33000 p	±10, ±20	3.5	200	1.25±0.10	R
Н	MK212 BJ473∏G-T		100	В	X5R*1	47000 p	±10, ±20	3.5	200	1.25±0.10	R
H	MK212 BJ683∏G-T			В	X5R*1	68000 p	±10, ±20	3.5	200	1.25±0.10	R
H	MK212 BJ104[]G-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
H	MK212 BJ224[]G-T			В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
Q	MK212 BJ472[]G-T			В	X5R*1	4700 p	±10, ±20	2.5	150	1.25±0.10	R
G	MK212 BJ682∏G-T			В	X5R*1	6800 p	±10, ±20	2.5	150	1.25±0.10	R
G	MK212 BJ103∏G-T		250	В	X5R*1	10000 p	±10, ±20	2.5	150	1.25±0.10	R
Q	MK212 BJ153∏G-T			В	X5R*1	15000 p	±10, ±20	2.5	150	1.25±0.10	R
Q	MK212 BJ223[]G-T			В	X5R*1	22000 p	±10, ±20	2.5	150	1.25±0.10	R

【Temperature Characteristic BJ:B/X5R】 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
QMK212 BJ102□D-T			В	X5R*1	1000 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ152 D-T		250	В	X5R*1	1500 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ222 D-T		250	В	X5R*1	2200 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ332 D-T			В	X5R*1	3300 р	±10, ±20	2.5	150	$0.85 \pm 0.10$	R

[Temperature Characteristic B7 : X7R] 1.25mm thickness(G)

Tremperature Onaracteristi	1.20mm am		T	Capacitance	0	tan δ	HTLT		Soldering
Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	[F]	Capacitance tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow
							Nated Voltage X 70		W:Wave
HMK212 B7103∏G-T			X7R	10000 p	±10, ±20	3.5	200	$1.25 \pm 0.10$	R
HMK212 B7153[]G-T			X7R	15000 p	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7223∏G-T			X7R	22000 p	±10, ±20	3.5	200	$1.25 \pm 0.10$	R
HMK212 B7333∏G-T		100	X7R	33000 p	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7473∏G-T		100	X7R	47000 p	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7683∏G-T			X7R	68000 p	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7104∏G-T			X7R	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7224∏G-T			X7R	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
QMK212 B7472 G-T			X7R	4700 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7682∏G-T			X7R	6800 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7103 G-T		250	X7R	10000 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7153[]G-T			X7R	15000 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7223 G-T			X7R	22000 p	±10, ±20	2.5	150	1.25±0.10	R

[Temperature Characteristic B7 : X7R] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]	Tempe charact	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	
					5		2.13	Rated Voltage x %		W:Wave
QMK212 B7102[]D-T				X7R	1000 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7152[]D-T		250		X7R	1500 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7222[]D-T		230		X7R	2200 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7332[]D-T				X7R	3300 р	±10, ±20	2.5	150	$0.85 \pm 0.10$	R

[Temperature Characteristic SD : Standard ] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]	Temperature	Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Fart number 1	Fart Hulliber 2	Nated Voltage [V]	characteristics	[F]	tolerance [%]	[%]	Rated voltage x %	Inickness [mm]	W:Wave
HMK212 SD222KD-T		100		2200 p	±10	0.1	200	$0.85 \pm 0.10$	R
HMK212 SD472KD-T		100		4700 p	±10	0.1	200	$0.85 \pm 0.10$	R
QMK212 SD101KD-T				100 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD121KD-T				120 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD151KD-T				150 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD181KD-T				180 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD221KD-T			Standard Type	220 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD331KD-T		250	Standard Type	330 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD391KD-T		250		390 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD471KD-T				470 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD561KD-T				560 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD681KD-T				680 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD821KD-T				820 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD102KD-T				1000 p	±10	0.1	150	$0.85 \pm 0.10$	R

[Temperature Characteristic SD : Standard ] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK212 SD392KG-T		100	Standard Type	3900 р	±10	0.1	200	1.25±0.10	R

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#### ■316TYPE

1	Temperature	Characteristic	B.I · B/X5R	1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT	Thickness*3 [mm]	Soldering R:Reflow
			Citataci		0.1	tolerance [70]	[70]	Rated voltage x %		W:Wave
HMK316 BJ473□L-T			В	X5R*1	47000 p	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ683∏L-T			В	X5R*1	68000 p	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ104[]L-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ154[]L-T		100	В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ224[]L-T		100	В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ334□L-T			В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ474[]L-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ105[]L-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.6±0.20	R
QMK316 BJ333□L-T			В	X5R*1	33000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ473□L-T		250	В	X5R*1	47000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ683□L-T		230	В	X5R*1	68000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ104□L-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 BJ153[]L-T		630	В	X5R*1	15000 p	±10, ±20	2.5	120	1.6±0.20	R
SMK316 BJ223[]L-T		030	В	X5R*1	22000 p	±10, ±20	2.5	120	1.6±0.20	R

ı	Temperature	Characteristic	BJ · B/X5R	1.15mm thickness	(F)

5	D	Rated voltage [V]	Tempe	emperature Capacitance caracteristics [F]		Capacitance tolerance [%]	$ an\delta$	HTLT	*3 - 7	Soldering
Part number 1	Part number 2	Rated voltage [V]	charact				[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
SMK316 BJ102[F-T			В	X5R*1	1000 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ152[F-T			В	X5R*1	1500 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ222[F-T			В	X5R*1	2200 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ332∏F-T		630	В	X5R*1	3300 р	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ472∏F-T			В	X5R*1	4700 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ682∏F-T			В	X5R*1	6800 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ103∏F-T			В	X5R*1	10000 p	±10, ±20	2.5	120	1.15±0.10	R

[Temperature Characteristic B7 : X7R] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316 B7473∏L-T			X7R	47000 p	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7683∏L-T			X7R	68000 p	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7104∏L-T			X7R	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7154□L-T		100	X7R	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7224□L-T		100	X7R	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7334□L-T			X7R	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7474□L-T			X7R	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7105□L-T			X7R	1 μ	±10, ±20	3.5	200	1.6±0.20	R
QMK316 B7333 L-T			X7R	33000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7473□L-T		250	X7R	47000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7683∏L-T		230	X7R	68000 p	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7104□L-T			X7R	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 B7153[L-T		630	X7R	15000 p	±10, ±20	2.5	120	1.6±0.20	R
SMK316 B7223 L-T		030	X7R	22000 p	±10, ±20	2.5	120	1.6±0.20	R

[Temperature Characteristic B7 : X7R] 1.15mm thickness(F)

Part number 1		Part number 2	Rated voltage [V]	Temperature		Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
	Part number I	Part number 2	Rated Voltage [V]	characteristics		[F]	tolerance [%]	[%]	Rated voltage x %	I hickness [mm]	W:Wave
	SMK316 B7102[F-T				X7R	1000 p	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7152[F-T				X7R	1500 p	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7222[]F-T				X7R	2200 p	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7332∏F-T		630		X7R	3300 р	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7472[]F-T				X7R	4700 p	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7682[]F-T				X7R	6800 p	±10, ±20	2.5	120	1.15±0.10	R
	SMK316 B7103∏F-T				X7R	10000 p	±10, ±20	2.5	120	1.15±0.10	R

 $\begin{tabular}{c} \textbf{[Temperature Characteristic SD: Standard ]} & 1.6mm thickness(L) \end{tabular}$ 

	Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
	HMK316 SD223KL-T		100	Character of Tames	22000 p	±10	0.1	200	1.6±0.20	R
Q	QMK316 SD103KL-T		250	Standard Type	10000 p	±10	0.1	150	1.6±0.20	R

#### ●325TYPE

Temperature Characteristic BJ : B/X5R 2.5mm thickness(M)

Tremperature Onaracteristi	C DO . D/ NOTY 2.011111 to	IICKI 1633 (IVI)								
Part number 1	Part number 2	Rated voltage [V]		erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 BJ225∏M-T		100	В	X5R*1	2.2 μ	±10. ±20	3.5	200	$2.5 \pm 0.20$	R

[Temperature Characteristic BJ : B/X5R] 1.9mm thickness(N)

Part number 1	Part number 2	Rated voltage [V]	Tempe	erature	Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Part number 1	Part number 2	Rated voitage [v]	charact	teristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness [mm]	W:Wave
HMK325 BJ154□N-T			В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ224□N-T			В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ334∏N-T		100	В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ474□N-T		100	В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ684∏N-T			В	X5R*1	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ105∏N-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.9±0.20	R
QMK325 BJ473□N-T			В	X5R*1	47000 p	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ104□N-T		250	В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ154□N-T		230	В	X5R*1	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ224□N-T			В	X5R*1	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 BJ223∏N-T			В	X5R*1	22000 p	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ333∏N-T		630	В	X5R*1	33000 р	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ473∏N-T			В	X5R*1	47000 p	±10, ±20	2.5	120	1.9±0.20	R

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[Temperature Characteristi	c BJ : B/X5R】 1.15mm t	thickness(F)								
Part number 1	Part number 2	Rated voltage [V]	Tempe charact	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 BJ104∏F-T		100	В	Y5P*1	0.1 //	+10 +20	3.5	200	115+010	R

[Temperature Characteristic B7 : X7R] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]	Tempe charact	rature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 B7225∏M-T		100		X7R	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R

[Temperature Characteristic B7 : X7R] 1.9mm thickness(N)

<b>.</b>		n	Tempe	rature	Capacitance	Capacitance	tan δ	HTLT	+2	Soldering
Part number 1	Part number 2	Rated voltage [V]	charact	eristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK325 B7154[]N-T				X7R	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7224[]N-T				X7R	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7334∏N-T		100		X7R	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7474[]N-T		100		X7R	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7684□N-T				X7R	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7105∏N-T				X7R	1 μ	±10, ±20	3.5	200	1.9±0.20	R
QMK325 B7473[]N-T				X7R	47000 p	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7104[]N-T		250		X7R	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7154[]N-T		230		X7R	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7224[]N-T				X7R	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 B7223∏N-T				X7R	22000 p	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7333∏N-T		630		X7R	33000 p	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7473[N-T				X7R	47000 p	±10, ±20	2.5	120	1.9±0.20	R

[Temperature Characteristic B7 : X7R] 1.15mm thickness(F)

Part number 1	-	Rated voltage [V]	Tempe charact	erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
								_		W.Wave
HMK325 B7104∏F-T		100		X7R	0.1 μ	±10, ±20	3.5	200	1.15±0.10	R

#### 432TYPE

[Temperature Characteristic BJ : B/X5R] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK432 BJ474∏M-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ105∏M-T		400	В	X5R*1	1 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ155∏M-T		100	В	X5R*1	1.5 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ225∏M-T			В	X5R*1	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
QMK432 BJ104[]M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ224[]M-T		250	В	X5R*1	0.22 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ334[]M-T		230	В	X5R*1	0.33 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ474[]M-T			В	X5R*1	0.47 μ	±10, ±20	2.5	150	2.5±0.20	R
SMK432 BJ473[M-T			В	X5R*1	47000 p	±10, ±20	2.5	120	2.5±0.20	R
SMK432 BJ683[M-T		630	В	X5R*1	68000 p	±10, ±20	2.5	120	2.5±0.20	R
SMK432 BJ104[M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	120	2.5±0.20	R

[Temperature Characteristic B7 : X7R] 2.5mm thickness (M)

Part number 1	Part number 2	Rated voltage [V]	Temperature	Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Part number I	Part number 2	Rated Voltage [V]	characteristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness [mm]	W:Wave
HMK432 B7474[]M-T			X7R	0.47 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 B7105[]M-T		100	X7R	1 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 B7155[]M-T		100	X7R	1.5 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 B7225[]M-T			X7R	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
QMK432 B7104□M-T			X7R	0.1 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 B7224[]M-T		250	X7R	0.22 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 B7334□M-T		230	X7R	0.33 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 B7474[M-T			X7R	0.47 μ	±10, ±20	2.5	150	2.5±0.20	R
SMK432 B7473[M-T			X7R	47000 p	±10, ±20	2.5	120	2.5±0.20	R
SMK432 B7683∏M-T		630	X7R	68000 p	±10, ±20	2.5	120	2.5±0.20	R
SMK432 B7104[M-T			X7R	0.1 μ	±10, ±20	2.5	120	$2.5 \pm 0.20$	R
3MK432 B7104 LIM 1			Λ/Ν	0.1 μ	±10, ±20	2.5	120	2.3 ± 0.20	K

#### LW Reversal Decoupling Capacitors (LWDC<sup>TM</sup>)

● 105TYPE

[Temperature Characteristic BJ : X5R] 0.3mm thickness(P)

L remperature enaracterior	ao Bo : Atorty olonian amort	11000 (1 )								
Part number 1	Part number 2	Rated voltage [V]	Tempe	erature	Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Fart number 1	Fart number 2	Nated Voltage [V]	charact	teristics	[F]	tolerance [%]	[%]	Rated voltage x %	Inickness [mm]	W:Wave
TWK105 BJ104MP-F		25		X5R	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
EWK105 BJ224MP-F		16		X5R	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
LWK105 BJ474MP-F		10		X5R	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ104MP-F				X5R*1	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
JWK105 BJ474MP-F		6.3		X5R*1	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ105MP-F		0.3		X5R	1 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ225MP-F				X5R	2.2 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 BJ224MP-F		4		X5R	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R

[Temperature Characteristic C6 : X6S , C7 : X7S] 0.3mm thickness(P)

D. d	Part number 1 Part number 2		Temperature		Capacitance	Capacitance	$ an\delta$	HTLT	*3 - 7	Soldering
Part number 1	Part number 2	Rated voltage [V]	charact	eristics	[F]	tolerance [%]	%] [%] Ra	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
EWK105 C6104MP-F		16		X6S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
LWK105 C7104MP-F		10		X7S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
LWK105 C6224MP-F		10		X6S	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 C7104MP-F				X7S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
JWK105 C7224MP-F		6.3		X7S	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 C6474MP-F				X6S	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R

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### **Multilayer Ceramic Capacitors**

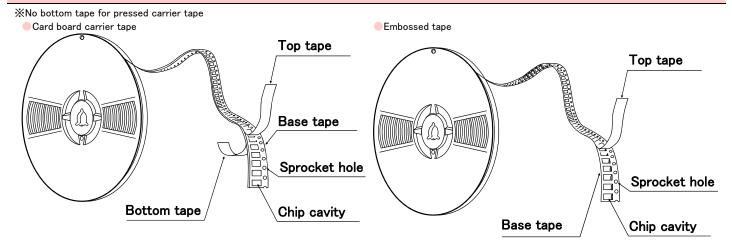
#### ■PACKAGING

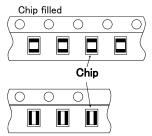
#### 1 Minimum Quantity

Taped package					
Type(EIA)	Thick	ness	Standard o	uantity [pcs]	
Type(EIA)	mm	code	Paper tape	Embossed tape	
☐MK042(01005)	0.2	C, D		40000	
□VS042(01005)	0.2	С		40000	
☐MK063(0201)	0.3	P, T	15000		
□WK105(0204) ※	0.3	Р	10000		
	0.2	С	20000		
□MK105(0402)	0.3	Р	15000	T –	
	0.5	V	10000		
□VK105(0402) ※	0.5	W	10000		
□MK107(0603)	0.45	K	4000		
□WK107(0306) ※	0.5	V	_	4000	
□MR107(0603)	0.8	Α			
□MK212(0805)	0.45	K	4000	_	
□WK212(0508) ※	0.85	D	1		
□MR212(0805)	125	G	_	3000	
	0.85	D	4000	_	
□MK316(1206)	1.15	F		2000	
□MR316(1206)	125	G	_	3000	
	1.6	L	_	2000	
	0.85	D			
	1.15	F			
☐MK325(1210)	1.9	N	7 -	2000	
□MR325(1210)	2.0max.	Υ	7		
	2.5	М		500(T), 1000(P)	
□MK432(1812)	2.5	М	_	500	

Note: X LW Reverse type.

#### ②Taping material



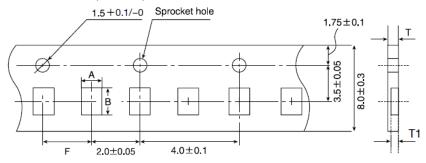


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#### 3 Representative taping dimensions

#### Paper Tape (8mm wide)

#### ● Pressed carrier tape (2mm pitch)

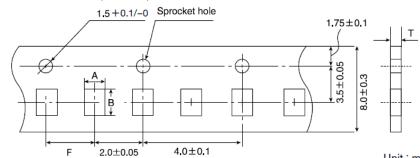


			Onit · mm					
Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness				
Type(EIA)	Α	В	F	Т	T1			
☐MK063(0201)	0.37	0.67		0.45max.	0.42max.			
□WK105(0204) ※		1.15	2.0±0.05	0.45max.	0.42max.			
☐MK105(0402) (*1 C)	0.65		2.0±0.03	0.4max.	0.3max.			
□MK105(0402) (*1 P)				0.45max.	0.42max.			

Note \*1 Thickness, C:0.2mm ,P:0.3mm. \* LW Reverse type.

Unit:mm

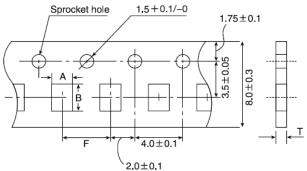
#### ●Punched carrier tape (2mm pitch)



			Unit : mm						
Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Thickness					
Type(EIA)	Α	В	F	Т					
□MK105 (0402) □VK105 (0402)	0.65	1.15	2.0±0.05	0.8max.					

Unit:mm

#### ●Punched carrier tape (4mm pitch)



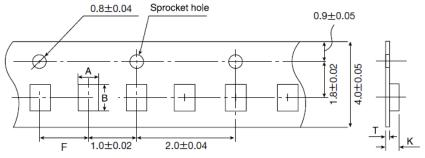
	2.0 ± 0.1	Unit	: mm		
Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	Т	
☐MK107(0603)					
□WK107(0306) ※	1.0	1.8		1.1max.	
☐MR107(0603)			40101		
☐MK212(0805)	4.05	0.4	4.0±0.1		
□WK212(0508) ※	1.65	2.4		1.1max.	
☐MK316(1206)	2.0	3.6			
				•	

Note: Taping size might be different depending on the size of the product. 💥 LW Reverse type.

Unit:mm

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#### Embossed tape (4mm wide)

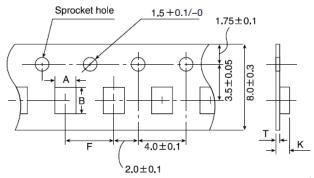


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Τ /ΓΙΔ \	Chip (	Cavity	Insertion Pitch	Tape Thickness		
Type(EIA)	Α	В	F	K	Т	
☐MK042(01005)	0.00	0.40	10+000	0.5	0.05	
□VS042(01005)	0.23	0.43	1.0±0.02	0.5max.	0.25max.	

 $\mathsf{Unit}\!:\!\mathsf{mm}$ 

#### Embossed tape (8mm wide)

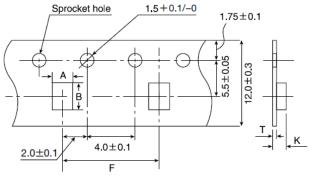


Unit: mm

Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Thickness		
Type(EIA)	Α	В	F	K	Т	
□WK107(0306) ※	1.0	1.8		1.3max.	0.25±0.1	
□MK212(0805) □MR212(0805)	1.65	2.4	4.0±0.1	3.4max.		
□MK316(1206) □MR316(1206)	2.0	3.6			0.6max.	
□MK325(1210) □MR325(1210)	2.8	3.6				

Note: \* LW Reverse type. Unit:mm

#### Embossed tape (12mm wide)



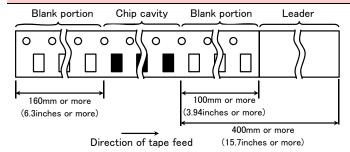
mm

Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Th	nickness
	Α	В	F	K	Т
□MK432(1812)	3.7	4.9	8.0±0.1	4.0max.	0.6max.

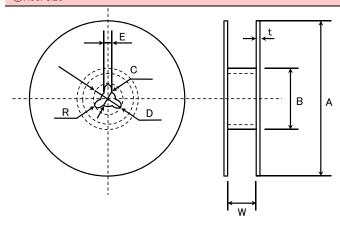
Unit:mm

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#### 4 Trailer and Leader



#### **5**Reel size



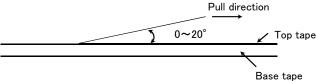
Α	В	С	D	E	R
$\phi$ 178 ± 2.0	$\phi$ 50min.	$\phi$ 13.0 $\pm$ 0.2	$\phi$ 21.0 $\pm$ 0.8	2.0±0.5	1.0

	Т	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

Unit:mm

#### $\textbf{\^{6}} \textbf{Top Tape Strength}$

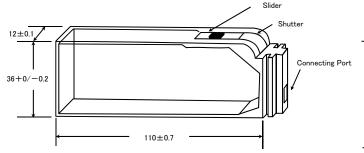
The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.

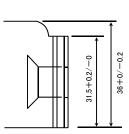


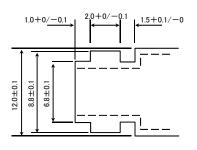
#### **7**Bulk Cassette

The exchange of individual specification is necessary.

Please contact Taiyo Yuden sales channels.







Unit:mm

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# Medium-High Voltage Multilayer Ceramic Capacitor

#### ■RELIABILITY DATA

1. Operating Tempe	rature Range			
	Temperature Compensating(Class1)	CG : −55~+125°C		
Specified Value	High Permittivity(Class2)	X7R, X7S : -55 to +125°C X5R : -55 to +85°C B : -25 to +85°C SD : -55~+125°C		
2. Storage Tempera	ture Range			
	Temperature Compensating(Class1)	CG : −55~+125°C		
Specified Value	High Permittivity(Class2)	X7R, X7S : -55 to +125°C X5R : -55 to +85°C B : -25 to +85°C SD : -55~+125°C		
0 D + 1 V II				
3. Rated Voltage	Temporatura Componentia (Classi)	100VDC(HMK)		
Specified Value	Temperature Compensating (Class1)	100VDC(HMK)		
	High Permittivity (Class2)	100VDC(HMK), 250VDC(QMK), 630VDC(SMK)		
4 Withstanding Val	age (Between terminals)			
Specified Value				
Test Methods and Remarks	No breakdown or damage  Applied voltage : Rated voltage × 2.5 (HMK), Rated voltage × 2 (QMK), Rated voltage × 1.2 (SMK)  Duration : 1 to 5sec.  Charge/discharge current : 50mA max.			
	Charge, discharge carrente . com/t max.			
5. Insulation Resista	ance			
J. Insulation Nesista	Temperature Compensating(Class1)	10000 MΩ min.		
Specified Value	High Permittivity (Class2)	100M $\Omega \cdot \mu$ F or 10G $\Omega$ , whichever is smaller.		
Test Methods and Remarks		HMK, QMK), 500V (SMK)		
6. Capacitance (To	lerance)			
Specified Value	Temperature Compensating(Class1)	0.2pF≦C≦5pF : $\pm 0.25pF$ 0.2pF≦C≤10pF : $\pm 0.5pF$ C>10pF : $\pm 5\%$ or $\pm 10\%$		
	High Permittivity (Class2)	±10%, ±20%		
Test Methods and Remarks	Measuring frequency       : 1kHz±10%         Measuring voltage       : 1±0.2Vrms         Bias application       : None			
7. Q or Dissipation	Factor			
Specified Value	Temperature Compensating(Class1)	$ \begin{array}{c} C < 30 pF : Q \ge 400 + 20C \\ C \ge 30 pF : Q \ge 1000 & (C : Nominal capacitance) \end{array} $		
	High Permittivity (Class2)	3.5%max(HMK),2.5%max(QMK, SMK)		
Test Methods and	Temperature Compensating(Class1)	Measuring frequency       : 1MHz±10%         Measuring voltage       : 0.5∼5Vrms         Bias application       : None		
Remarks	High Permittivity(Class2)	Measuring frequency: 1kHz±10%Measuring voltage: 1±0.2VrmsBias application: None		

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#### 8. Temperature Characteristic of Capacitance Temperature Compensating(Class1) CG $:0\pm30$ ppm/°C( $-55\sim+125$ °C) В : ±10%(−25 to +85°C) : ±15%(-55 to +85°C) : ±15%(-55 to +125°C X5R Specified Value High Permittivity (Class2) X7R : $\pm 22\%(-55 \text{ to } +125^{\circ}\text{C})$ X7S SD : $-(-55\sim+125^{\circ}C)$ Capacitance value at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the CG, B, X5R, X7R, X7S, SD Step Minimum operating temperature

Test Methods and Remarks

2 20°C 25°C 3 Maximum operating temperature

$$\frac{(C-C_2)}{C_2} \times 100(\%)$$

C : Capacitance value in Step 1 or Step 3

C2: Capacitance value in Step 2

Specified Value	Temperature Compensating(Class1)	Appearance : No abnormality Capacitance change : Within $\pm 5\%$ or $\pm 0.5$ pF, whichever is larger.
pecifica value	High Permittivity (Class2)	Appearance : No abnormality Capacitance change : Within±10%
est Methods and lemarks	Warp : 1mm Duration : 10sec. Test board : Glass epoxy-resin substrat Thickness : 1.6mm  Capacitance measurement shall be conducted	Warp $45\pm2$ $45\pm2$ (Unit: mm)

10. Adhesive Streng	th of Terminal Electrodes	
Specified Value	Temperature Compensating(Class1)	No terminal separation or its indication.
Specified value	High Permittivity (Class2)	No terminal separation or its indication.
Test Methods and Remarks	Applied force : 5N  Duration : 30±5sec.	Hooked jig  R=0.5  Chip Chip

11. Solderability						
Specified Value	Temperature Compensating(Class1)		A+ loos+ 05% of	At least 95% of terminal electrode is covered by new solder		
High Permittivity (Class2)	terminal electrode is covered by	new solder				
		Eutecti	c solder	Lead-free solder		
Test Methods and	Solder type	H60A d	or H63A	Sn-3.0Ag-0.5Cu		
Remarks	Solder temperature	230±5°C		245±3°C		
	Duration	4±1 sec.		sec.		

		Appearance	: No abnormality
		Capacitance change	: Within ±2.5% or ±0.25pF, whichever is larger.(HMK)
	Temperature Compensating(Class1)	Q	: Initial value
		Insulation resistance	: Initial value
		Withstanding voltage	(between terminals): No abnormality
Specified Value		Appearance	: No abnormality
		Capacitance change	: Within $\pm 15\%$ (HMK), $\pm 10\%$ (QMK, SMK)
	High Permittivity (Class2)	Dissipation facto	: Initial value
		Insulation resistance	: Initial value
		Withstanding voltage	(between terminals): No abnormality

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ting(Class1)
ung (Olass I)
condition Note3

# Test Methods and Remarks

	High Permittivity (Class2)
Preconditioning	Thermal treatment(at 150°C for 1hr) Note1
Solder temperature	270±5℃
Duration	3±0.5sec.
Preheating conditions	80 to 100°C, 2 to 5 min.
	150 to 200°C, 2 to 5min.
Recovery	24±2hrs under the standard condition Note3

			Appearance	: No abnormality		
			Capacitance change	: Within $\pm 2.5\%$ or $\pm 0.25$ pF, whichever is larger.(H		u in lawren (UMK)
	Tamanawatuwa Camanana	ation (Class 1)	Q	: Initial value		
			Insulation resistance		: Initial value : Initial value	
					s): No abnormality	
Specified Value			Withstanding voltage	(between terminal	s) : No abnormality	<u>'</u>
•			Appearance	: No abnormality		
			Capacitance change	: Within±15%(HM	K), ±10%(QMK, SN	ΛK)
	High Permittivity (Class2)		Dissipation facto	: Initial value		
			Insulation resistance	: Initial value		
			Withstanding voltage	(between termina	ls): No abnormality	/
			Class 1	Class 2		
			Jiass I	Thormal tr	eatment (at 150°C	for 1 hr)
	Preconditioning		None	Triermai tr	Note 2	ior i iir)
					Note 2	
		Step	Temperatu	re(°C)	Time (min.)	
Test Methods and		1	Minimum operatin	g temperature	30±3	
Remarks	1 cycle	2	Normal tem	perature	2 to 3	
		3	Maximum operatin	g temperature	30±3	
		4	Normal tem	perature	2 to 3	
		<u> </u>	'			
	1	5 times				
	Number of cycles	6 to 24 hrs (Standard condition) Note 3 24±2 hrs (Standard condition) Note				ì

14. Humidity (Stead	dy state)			
Specified Value	Temperature Compensating(Class1)		$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	
	High Permittivity(Class2)		Appearance Capacitance chang Dissipation factor Insulation resistance	: 7%max(HMK), 5%max(QMK, SMK).
	Class 1		1	Class 2
Test Methods and Remarks	Preconditioning	None		Thermal treatment( at 150°C for 1 hr) Note 1
	Temperature	40±2°C		40±2°C
	Humidity	90 to 95%RH		90 to 95%RH
	Duration	500+24/-	-0 hrs	500+24/-0 hrs
	Recovery	6 to 24 hrs (Standard	condition) Note 3	24±2 hrs(Standard condition)Note 3

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15. Humidity Loadin	g				
Specified Value	Temperature Compensating(Class1)  High Permittivity (Class2)		Appearance Capacitance change Q Insulation resistance	: No abnormality : Within $\pm 7.5\%$ or $\pm 0.75$ pF, whichever is larger(HMK). : $C < 30$ pF: $Q \ge 100 + 10$ C/3 $C \ge 30$ pF: $Q \ge 200$ (C:Nominal capacitance) : $500 \text{ M}\Omega$ min.	
			Appearance Capacitance change Dissipation factor Insulation resistance	: No abnormality : Within $\pm$ 15% : 7%max (HMK), 5%max (QMK, SMK). : 10M $\Omega$ $\mu$ F or 500M $\Omega$ , whichever is smaller.	
	According to JIS 5102 clause 9.9.				
		C	lass 1	Class 2	
Test Methods and Remarks	Preconditioning	None		Voltage treatment (Rated voltage are applied for 1 hour at 40°C) Note 2	
	Temperature	40±2°C		40±2°C	
	Humidity	90 to 95%RH		90 to 95%RH	
	Duration	500+24/-0 hrs		500+24/-0 hrs	
	Applied voltage	Rate	d voltage	Rated voltage	
	Charge/discharge current	50r	mA max.	50mA max.	
	Recovery	6 to 24 hrs (Stand	dard condition)Note 3	24±2 hrs (Standard condition) Note 3	

16. High Temperatu	re Loading					
Specified Value	Temperature Compensating(Class1)  High Permittivity (Class2)		Appearance Capacitance char Q Insulation resistar	: C < 30pF : Q ≥ 100 + 10C/3 C ≥ 30pF : Q ≥ 200 (C : Nominal capacitance)		
			Appearance Capacitance char Dissipation factor Insulation resistar	: 7%max(HMK), 5%max(QMK, SMK).		
Test Methods and Remarks	According to JIS 5102 clause 9.10.					
		Class	1	Class 2		
	Preconditioning	None		Voltage treatment (Twice the rated voltage shall be applied for 1 hour at 85°C, 105°C or 125°C) Note 3, 4		
	Temperature	Maximum operating temperature		Maximum operating temperature		
	Duration	1000+48/-0 hrs		1000+48/-0  hrs		
	Applied voltage	Rated voltage × 2 (HMK)		Rated voltage × 2(HMK), Rated voltage × 1.5 (QMK), Rated voltage × 1.2 (SMK)		
	Charge/discharge current	50mA m	ax.	50mA max.		
	Recovery	6 to 24hr (Standard o	ondition) Note 3	24±2 hrs(Standard condition)Note 3		

Note1 Thermal treatment : Initial value shall be measured after test sample is heat-treated at  $150 \pm 0/-10^{\circ}$ C for an hour and kept at room temperature for  $24 \pm 2$ hours.

Note2 Voltage treatment : Initial value shall be measured after test sample is voltage-treated for an hour at both the temperature and voltage specified in the test conditions, and kept at room temperature for 24±2hours.

Note3 Standard condition : Temperature: 5 to 35°C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted under the following condition.

Temperature:  $20\pm2^{\circ}$ C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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#### Precautions on the use of Multilayer Ceramic Capacitors

#### **■**PRECAUTIONS

#### 1. Circuit Design

- ◆Verification of operating environment, electrical rating and performance
- 1. A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications.

Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications.

#### Precautions

- ◆Operating Voltage (Verification of Rated voltage)
  - 1. The operating voltage for capacitors must always be their rated voltage or less.
    - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less.
    - For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.
  - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

#### 2. PCB Design

Precautions

- ◆Pattern configurations (Design of Land-patterns)
- 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
  - (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.
  - (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.
- ◆Pattern configurations (Capacitor layout on PCBs)

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

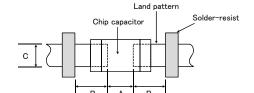
◆Pattern configurations (Design of Land-patterns)

The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

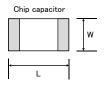
- (1) Recommended land dimensions for typical chip capacitors
- Multilayer Ceramic Capacitors : Recommended land dimensions (unit: mm)

Wave-soldering

Ту	ре	107	212	316	325
:	L	1.6	2.0	3.2	3.2
Size	W	0.8	1.25	1.6	2.5
A	4	0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5
Е	3	0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7
(	)	0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5



Land patterns for PCBs



# Technical considerations

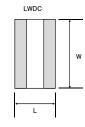
#### Reflow-soldering

1101	10 11 30	Jidoi ilig							
Ту	ре	042	063	105	107	212	316	325	432
Size	L	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
Size	W	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
-	4	0.15 to 0.25	0.20 to 0.30	0.45 to 0.55	0.8 to 1.0	0.8 to 1.2	1.8 to 2.5	1.8 to 2.5	2.5 to 3.5
E	3	0.15 to 0.20	0.20 to 0.30	0.40 to 0.50	0.6 to 0.8	0.8 to 1.2	1.0 to 1.5	1.0 to 1.5	1.5 to 1.8
(	)	0.15 to 0.30	0.25 to 0.40	0.45 to 0.55	0.6 to 0.8	0.9 to 1.6	1.2 to 2.0	1.8 to 3.2	2.3 to 3.5

 ${\bf Note:} Recommended \ land \ size \ might \ be \ different \ according \ to \ the \ allowance \ of \ the \ product.$ 

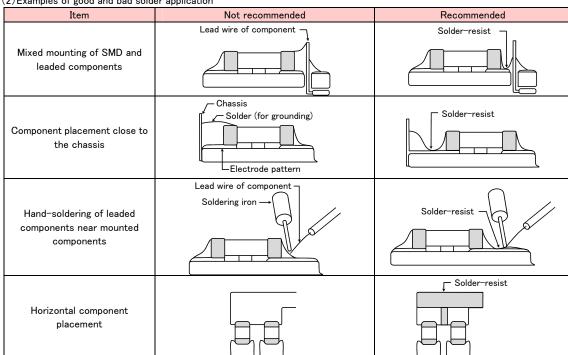
# ●LWDC: Recommended land dimensions for reflow-soldering (unit: mm)

Ту	ре	105	107	212
C:	L	0.52	0.8	1.25
Size	W	1.0	1.6	2.0
1	4	0.18 to 0.22	0.25 to 0.3	0.5 to 0.7
E	3	0.2 to 0.25	0.3 to 0.4	0.4 to 0.5
(	)	0.9 to 1.1	1.5 to 1.7	1.9 to 2.1



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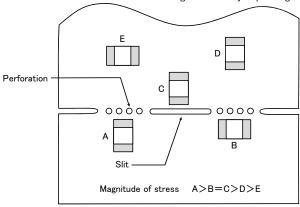
(2) Examples of good and bad solder application



- ◆Pattern configurations (Capacitor layout on PCBs)
  - 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.

Items	Not recommended	Recommended
Deflection of board		Place the product at a right angle to the direction of the anticipated mechanical stress.

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1-3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

#### 3. Mounting

◆Adjustment of mounting machine

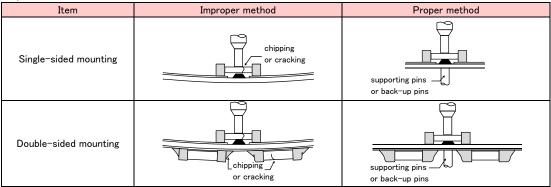
◆Selection of Adhesives

- 1. When capacitors are mounted on PCB, excessive impact load shall not be imposed on them.
- 2. Maintenance and inspection of mounting machines shall be conducted periodically.
- Precautions
- 1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.

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#### ◆Adjustment of mounting machine

- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable.
  - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.
  - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
  - (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



# Technical considerations

2. As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors.

To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.

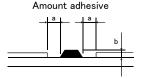
#### ◆Selection of Adhesives

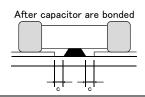
Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
  - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
  - b. The adhesive shall have sufficient strength at high temperatures.
  - c. The adhesive shall have good coating and thickness consistency.
  - d. The adhesive shall be used during its prescribed shelf life.
  - e. The adhesive shall harden rapidly.
  - f. The adhesive shall have corrosion resistance.
  - g. The adhesive shall have excellent insulation characteristics.
  - h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- (2) The recommended amount of adhesives is as follows;

[Recommended condition]

a 0.3mm min b 100 to 120 μ m	Figure	212/316 case sizes as examples
	а	0.3mm min
	b	100 to 120 $\mu$ m
c Adhesives shall not contact land	С	Adhesives shall not contact land





#### 4. Soldering

Precautions

Technical

considerations

#### ◆Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt%( in Cl equivalent) of halogenated content. Flux having a strong acidity content shall not be applied.
- (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

#### **♦**Soldering

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

Sn-Zn solder paste can adversely affect MLCC reliability.

Please contact us prior to usage of Sn-Zn solder.

#### ◆Selection of Flux

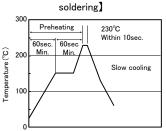
- 1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.
- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used.

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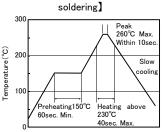
#### **♦**Soldering

- · Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating : Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 130°C
- Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.
   [Reflow soldering]

[Recommended conditions for eutectic

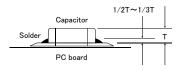


[Recommended condition for Pb-free



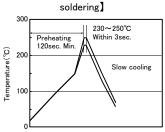
#### Caution

- $\bigcirc$  The ideal condition is to have solder mass(fillet) controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as close to recommended times as possible.

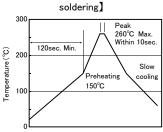


#### [Wave soldering]

[Recommended conditions for eutectic



#### [Recommended condition for Pb-free

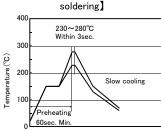


#### Caution

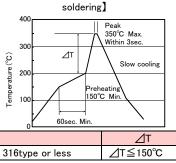
①Wave soldering must not be applied to capacitors designated as for reflow soldering only.

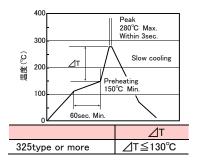
#### [Hand soldering]

[Recommended conditions for eutectic



#### [Recommended condition for Pb-free





#### Caution

- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- ②The soldering iron shall not directly touch capacitors.

#### 5. Cleaning

#### 4 WI D

#### Precautions

- 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use of the cleaning. (e.g. to remove soldering flux or other materials from the production process.)
- 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics.

# Technical considerations

- 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating, resulting in a degradation of the capacitor's electrical properties (especially insulation resistance).
- 2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors. In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the cracking of capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall be carefully checked;

Ultrasonic output: 20 W/l or less Ultrasonic frequency: 40 kHz or less Ultrasonic washing period: 5 min. or less

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# 6. Resin coating and mold 1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the capacitor's performance. 2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or destruction of capacitors. The use of such resins, molding materials etc. is not recommended.

# 7. Handling Splitting of PCB 1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board. 2. Board separation shall not be done manually, but by using the appropriate devices. Mechanical considerations Be careful not to subject capacitors to excessive mechanical shocks. (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used. (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components.

8. Storage condi	tions
Precautions	◆Storage  1. To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.  *Recommended conditions  Ambient temperature: Below 30°C  Humidity: Below 70% RH  The ambient temperature must be kept below 40°C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.  *Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.  2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits. Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for 1hour.
Technical considerations	If capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.

\*\*RCR-2335B (Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA. Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

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