

BFP540ESD

NPN Silicon RF Transistor*

- For ESD protected high gain low noise amplifier
- Excellent ESD performance typical value 1000 V (HBM)
- Outstanding *G*_{ms} = 21.5 dB Noise Figure *F* = 0.9 dB
- Gold metallization for high reliability
- SIEGET ® 45 Line
- Pb-free (RoHS compliant) package¹⁾
- Qualified according AEC Q101
- * Short term description



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

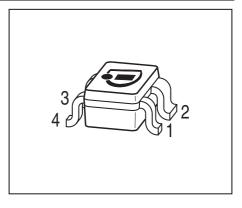
Туре	Marking	Pin Configuration				Package		
BFP540ESD	AUs	1=B	2=E	3=C	4=E	-	-	SOT343

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}		V
$T_{A} > 0^{\circ}C$		4.5	
$T_{A} \leq 0^{\circ}C$		4	
Collector-emitter voltage	V _{CES}	10	
Collector-base voltage	V _{CBO}	10	
Emitter-base voltage	V _{EBO}	1	
Collector current	I _C	80	mA
Base current	I _B	8	
Total power dissipation ²⁾	P _{tot}	250	mW
_ <i>T</i> _S ≤ 77°C			
Junction temperature	Ti	150	°C
Ambient temperature	T _A	-65 150	
Storage temperature	T _{stg}	-65 150	

¹Pb-containing package may be available upon special request

 $^2{\cal T}_S$ is measured on the collector lead at the soldering point to the pcb





Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 290	K/W

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.]
DC Characteristics	•				•
Collector-emitter breakdown voltage	V _{(BR)CEO}	4.5	5	-	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$					
Collector-emitter cutoff current	I _{CES}	-	-	10	μA
$V_{\rm CE}$ = 10 V, $V_{\rm BE}$ = 0					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 5 \rm V, I_{\rm E} = 0$					
Emitter-base cutoff current	I _{EBO}	-	-	10	μA
$V_{\rm EB} = 0.5 \rm V, I_{\rm C} = 0$					
DC current gain	h _{FE}	50	110	170	-
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 3.5 V, pulse measured					

¹For calculation of R_{thJA} please refer to Application Note Thermal Resistance



Parameter	Symbol	Values			Unit		
		min.	typ.	max.			
AC Characteristics (verified by random sampling)							
Transition frequency	f _T	21	30	-	GHz		
I _C = 50 mA, V _{CE} = 4 V, <i>f</i> = 1 GHz							
Collector-base capacitance	C _{cb}	-	0.14	0.24	pF		
$V_{\rm CB} = 2 \text{ V}, f = 1 \text{ MHz}, V_{\rm BE} = 0$,							
emitter grounded							
Collector emitter capacitance	C _{ce}	-	0.41	-			
$V_{CE} = 2 V, f = 1 MHz, V_{BE} = 0$,							
base grounded							
Emitter-base capacitance	C _{eb}	-	0.59	-			
$V_{\rm EB}$ = 0.5 V, f = 1 MHz, $V_{\rm CB}$ = 0 ,							
collector grounded							
Noise figure	F				dB		
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 2 V, f = 1.8 GHz, $Z_{\rm S}$ = $Z_{\rm Sopt}$		-	0.9	1.4			
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 2 V, f = 3 GHz, $Z_{\rm S}$ = $Z_{\rm Sopt}$		-	1.3	-			
Power gain, maximum stable ¹⁾	G _{ms}	-	21.5	-	dB		
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,							
$Z_{\rm L} = Z_{\rm Lopt}$, $f = 1.8 {\rm GHz}$							
Power gain, maximum available ¹⁾	G _{ma}	-	16	-	dB		
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,							
$Z_{\rm L} = Z_{\rm Lopt}, f = 3 {\rm GHz}$							
Transducer gain	S _{21e} ²				dB		
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω , f = 1.8GHz		16	18.5	-			
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω , f = 3GHz		-	14	-			
Third order intercept point at output ²⁾	IP ₃	-	24.5	-	dBm		
$V_{\rm CE}$ = 2 V, $I_{\rm C}$ = 20 mA, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω , f = 1.8GHz							
1dB Compression point at output	P _{-1dB}	-	11	-]		
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω , f = 1.8GHz							

Electrical Characteristics at $T_A = 25^{\circ}$ C, unless otherwise specified

 ${}^{1}G_{ma} = |S_{21e} / S_{12e}| (k - (k^{2} - 1)^{1/2}), G_{ms} = |S_{21e} / S_{12e}|$

²IP3 value depends on termination of all intermodulation frequency components.

Termination used for this measurement is 50Ω from 0.1 MHz to 6 GHz



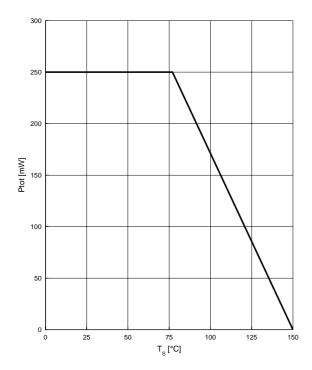
Simulation Data

For SPICE-model as well as for S-parameters including noise parameters refer to our internet website: www.infineon.com/rf.models. Please consult our website and download the latest version before actually starting your design. The simulation data have been generated and verified up to 8 GHz using typical devices. The BFP540ESD nonlinear SPICE-model reflects the typical DC- and RF-device performance with high accuracy.



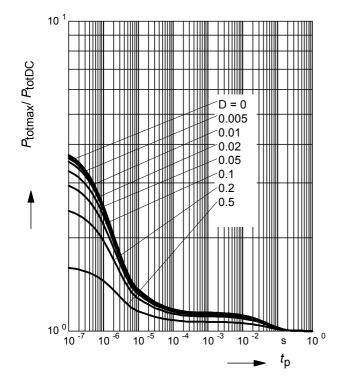
Total power dissipation $P_{tot} = f(T_S)$

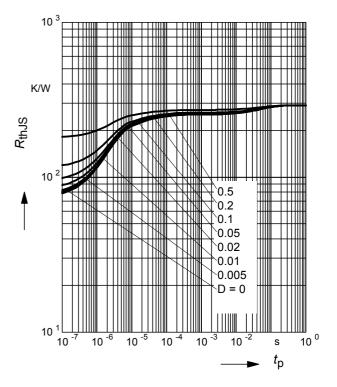
Permissible Pulse Load $R_{\text{thJS}} = f(t_p)$



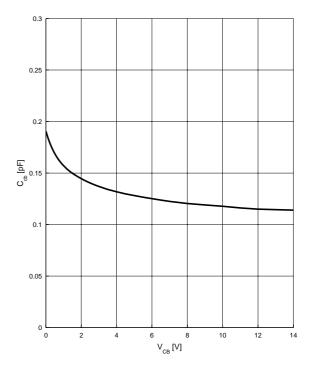
Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$





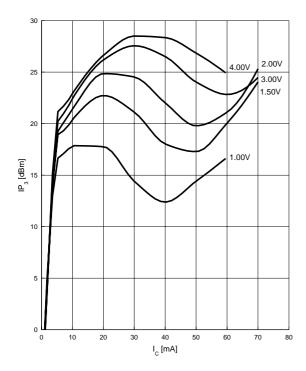
Collector-base capacitance $C_{cb} = f (V_{CB})$ f = 1 MHz



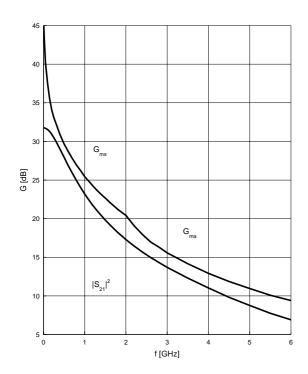


Third order Intercept Point $IP_3 = f(I_C)$

(Output, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω) $V_{\rm CE}$ = parameter, f = 900 MHz

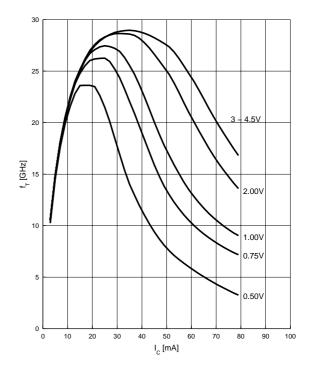


Power gain G_{ma} , $G_{ms} = f(f)$ $V_{CE} = 3 \text{ V}$, $I_C = 25 \text{ mA}$

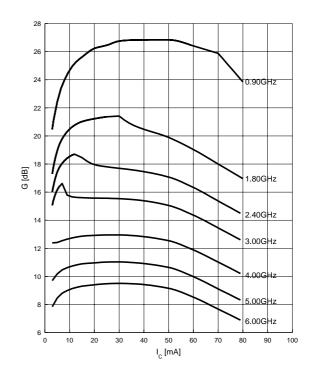


Transition frequency $f_{\rm T} = f(I_{\rm C})$

 V_{CE} = parameter in V, f = 2 GHz



Power gain G_{ma} , $G_{ms} = f(I_C)$ $V_{CE} = 3 V$ f = parameter in GHz

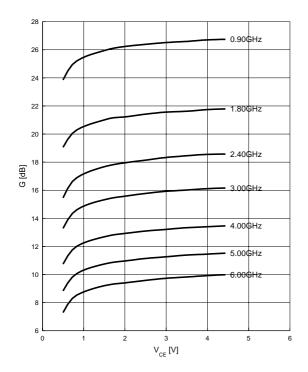


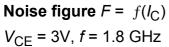


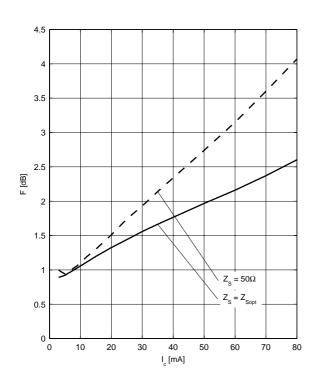
Power gain G_{ma} , $G_{ms} = f(V_{CE})$

*I*_C = 20 mA

f = parameter in GHz



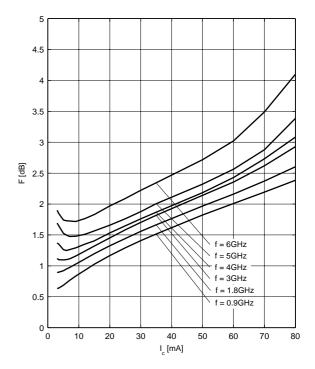




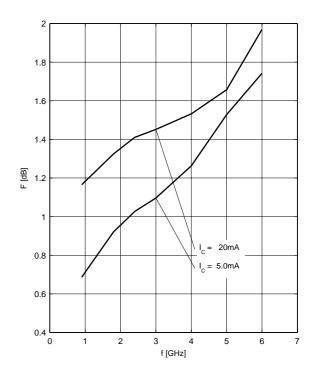
Noise figure $F = f(I_C)$

 V_{CE} = 3 V, f = parameter in GHz

 $Z_{\rm S} = Z_{\rm Sopt}$



Noise figure F = f(f) $V_{CE} = 3 V, Z_S = Z_{Sopt}$

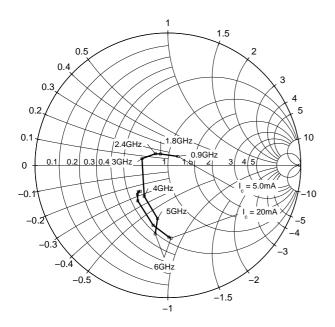




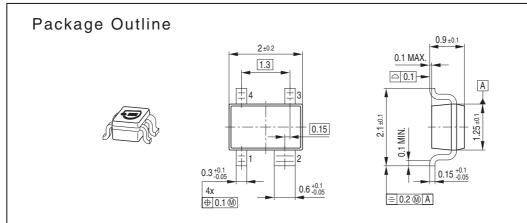


Source impedance for min. noise figure vs. frequency

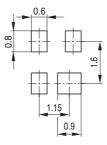
 $V_{\rm CE}$ = 3 V, $I_{\rm C}$ = 5 mA / 20 mA



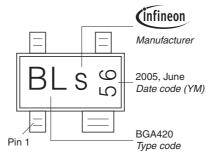




Foot Print

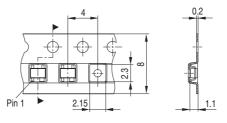


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel







Edition 2009-11-16

Published by Infineon Technologies AG 81726 Munich, Germany

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