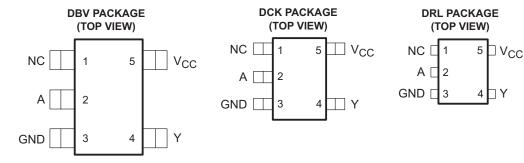


### SINGLE SCHMITT-TRIGGER INVERTER GATE

Check for Samples: SN74AHC1G14

#### **FEATURES**

- Operating Range 2-V to 5.5-V
- Max t<sub>pd</sub> of 10ns at 5-V
- Low Power Consumption, 10-μA Max I<sub>CC</sub>
- ±8-mA Output Drive at 5-V
- Latch-Up Performance Exceeds 250 mA Per JESD 17



NC – No internal connection
See mechanical drawings for dimensions.

#### DESCRIPTION

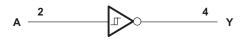
The SN74AHC1G14 contains one inverter gate. The device performs the Boolean function  $Y = \overline{A}$ .

The device functions as an independent inverter gate, but because of the Schmitt action, gates may have different input threshold levels for positive- (VT+) and negative-going (VT-) signals.

#### **FUNCTION TABLE**

INPUT A	OUTPUT Y
Н	L
L	Н

#### **LOGIC DIAGRAM (POSITIVE SIDE)**





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



#### **ABSOLUTE MAXIMUM RATINGS**

over operating free-air temperature range (unless otherwise noted) (1)

		VALUE	UNIT			
Supply voltage range, V <sub>CC</sub>		–0.5 to 7	V			
Input voltage range, V <sub>I</sub> <sup>(2)</sup>		–0.5 to 7	V			
Output voltage range, V <sub>O</sub> <sup>(2)</sup>		−0.5 to V <sub>CC</sub> + 0.5	V			
Input clamp current, I <sub>IK</sub> (V <sub>I</sub> < 0)	nput clamp current, I <sub>IK</sub> (V <sub>I</sub> < 0)					
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O >$	±20	mA				
Continuous output current, $I_O$ ( $V_O = 0$ to $V$	/cc)	±25	mA			
Continuous current through $V_{CC}$ or GND		±50	mA			
	DBV package	206				
Package thermal impedance, $\theta_{JA}^{\ \ (3)}$	DCK package	252	°C/W			
	DRL package	142				
Storage temperature range, T <sub>stg</sub>	-65 to 150	°C				

<sup>(1)</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

#### RECOMMENDED OPERATING CONDITIONS(1)

			MIN	MAX	UNIT
V <sub>CC</sub>	Supply voltage		2	5.5	V
VI	Input voltage	•	0	5.5	V
Vo	Output voltage		0	V <sub>CC</sub>	V
I <sub>OH</sub>		V <sub>CC</sub> = 2 V		-50	μΑ
	High-level output current	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$		-4	A
		$V_{CC} = 5 \text{ V} \pm 0.5 \text{ V}$		mA	
		V <sub>CC</sub> = 2 V		50	μΑ
$I_{OL}$	Low-level output current	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$		4	1
		$V_{CC} = 5 \text{ V} \pm 0.5 \text{ V}$		8	mA
T <sub>A</sub>	Operating free-air temperature		-40	125	°C

<sup>(1)</sup> All unused inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

Product Folder Links: SN74AHC1G14

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<sup>(3)</sup> The package thermal impedance is calculated in accordance with JESD 51-7.



#### **ELECTRICAL CHARACTERISTICS**

over operating free-air temperature range (unless otherwise noted)

						_	40°C 4-	Recom	mended	
PARAMETER	TEST CONDITIONS	vcc	T <sub>A</sub> = 25°C			1 <sub>A</sub> =	40°C to 5°C		40°C to 5°C	UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
V <sub>T+</sub>		3 V	1.2		2.2	1.2	2.2	1.2	2.2	
Positive-going input threshold		4.5 V	1.75		3.15	1.75	3.15	1.75	3.15	V
voltage		5.5 V	2.15		3.85	2.15	2.85	2.15	3.85	
V <sub>T</sub>		3 V	0.9		1.9	0.9	1.9	0.9	1.9	
Negative-going input threshold		4.5 V	1.35		2.75	1.35	2.75	1.35	2.75	V
voltage		5.5 V	1.65		3.35	1.65	3.35	1.65	3.35	
$\begin{array}{c} \Delta V_T \\ \text{Hysteresis} \\ (V_{T+} - V_{T-}) \end{array}$		3 V	0.3		1.2	0.3	1.2	0.25	1.2	
		4.5 V	0.4		1.4	0.4	1.4	0.35	1.4	V
		5.5 V	0.5		1.6	0.5	1.6	0.45	1.6	
		2 V	1.9	2		1.9		1.9		
	$I_{OH} = -50 \mu A$	3 V	2.9	3		2.9		2.9		
$V_{OH}$		4.5 V	4.4	4.5		4.4		4.4		V
	$I_{OH} = -4 \text{ mA}$	3 V	2.58			2.48		2.4		
	$I_{OL} = -8 \text{ mA}$	4.5 V	3.94			3.8		3.7		
		2 V			0.1		0.1		0.1	
	I <sub>OH</sub> = 50 μA	3 V			0.1		0.1		0.1	
$V_{OL}$		4.5 V			0.1		0.1		0.1	V
	I <sub>OH</sub> = 4 mA	3 V			0.36		0.44		0.55	
	I <sub>OL</sub> = 8 mA	4.5 V			0.36		0.44		0.55	
l <sub>l</sub>	V <sub>I</sub> = 5.5 V or GND	0 V to 5.5 V			±0.1		±1		±1	μA
I <sub>CC</sub>	$V_I = V_{CC}$ or GND, $IO = 0$	5.5 V			1		10		10	μA
C <sub>i</sub>	$V_I = V_{CC}$ or GND	5 V		2	10		10		10	pF

### **SWITCHING CHARACTERISTICS**

over recommended operating free-air temperature range,  $V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$  (unless otherwise noted) (see Figure 1)

	1 0	-	OUTPUT CAPACITANCE				l0°C to	Recomi	mended		
PARAMETER	FROM (INPUT)	TO (OUTPUT)		T <sub>A</sub> = 25°C		T <sub>A</sub> = -40°C to 85°C		T <sub>A</sub> = -40°C to 125°C		UNIT	
				TYP	MAX	MIN	MAX	MIN	MAX		
t <sub>PLH</sub>	Ао	V	C <sub>L</sub> = 15 pF	8.3	12.8	1	15	1	16	ns	
t <sub>PHL</sub>		ř		8.3	12.8	1	15	1	16		
t <sub>PLH</sub>	_	_	C <sub>L</sub> = 50 pF	10.8	16.3	1	18.5	1	19.5	ne	
t <sub>PHL</sub>	Α			10.8	16.3	1	18.5	1	19.5	ns	

Product Folder Links: SN74AHC1G14



#### **SWITCHING CHARACTERISTICS**

over recommended operating free-air temperature range,  $V_{CC} = 5 \text{ V} \pm 0.5 \text{ V}$  (unless otherwise noted) (see Figure 1)

	FROM (INPUT)	TO (OUTPUT)	OUTPUT CAPACITANCE	T <sub>A</sub> = 25°C		T 4	000 4-	Recomr	nended	
PARAMETER						T <sub>A</sub> = -40°C to 85°C		T <sub>A</sub> = -40°C to 125°C		UNIT
				TYP	MAX	MIN	MAX	MIN	MAX	
t <sub>PLH</sub>	A or B	V	C <sub>L</sub> = 15 pF	5.5	8.6	1	10	1	11	ns
t <sub>PHL</sub>		Y		5.5	8.6	1	10	1	11	
t <sub>PLH</sub>	A or B	<b>&gt;</b>	C <sub>L</sub> = 50 pF	7	10.6	1	12	1	11	ns
t <sub>PHL</sub>	AUIB	ĭ		7	10.6	1	12	1	11	

#### **OPERATING CHARACTERISTICS**

 $V_{CC} = 5 \text{ V}, T_A = 25^{\circ}\text{C}$ 

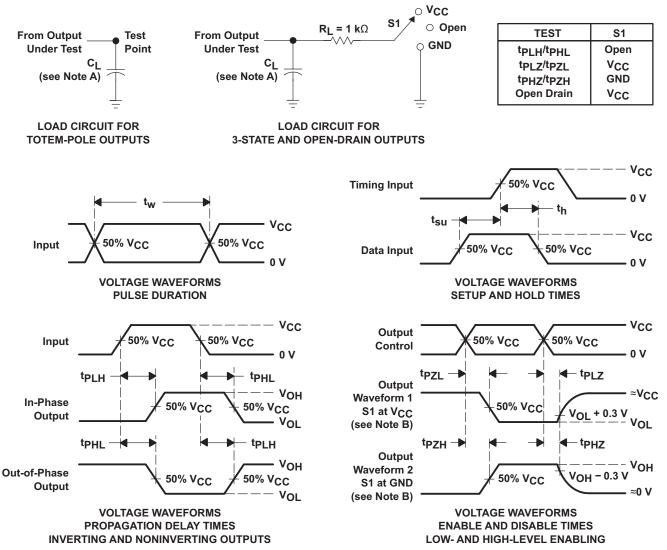
	PARAMETER	TEST CONDITIONS	TYP	UNIT
$C_{pd}$	Power dissipation capacitance	No load, f = 1 MHz	9	pF

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#### PARAMETER MEASUREMENT INFORMATION



- C<sub>L</sub> includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
  - Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  1 MHz,  $Z_O = 50 \Omega$ ,  $t_r \leq$  3 ns,  $t_f \leq$  1 mHz,  $t_f \leq$  1 mHz,  $t_f \leq$  2 ns,  $t_f \leq$  3 n
- The outputs are measured one at a time with one input transition per measurement.
- All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms

Product Folder Links: SN74AHC1G14

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#### **REVISION HISTORY**

Changes from Revision N (June 2005) to Revision O	Page
Changed document format from Quicksilver to DocZone.	
Changes from Revision O (January 2013) to Revision P	Page
Extended operating temperature range to 125°C	





19-Mar-2015

#### **PACKAGING INFORMATION**

Orderable Device	Status	Package Type		Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
SN74AHC1G14DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(A142 ~ A143 ~ A14G ~ A14J ~ A14L ~ A14S)	Samples
SN74AHC1G14DBVRE4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(A142 ~ A143 ~ A14G ~ A14J ~ A14L ~ A14S)	Samples
SN74AHC1G14DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(A142 ~ A143 ~ A14G ~ A14J ~ A14L ~ A14S)	Samples
SN74AHC1G14DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(A143 ~ A14G ~ A14L ~ A14S)	Samples
SN74AHC1G14DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(A143 ~ A14G ~ A14L ~ A14S)	Samples
SN74AHC1G14DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFJ ~ AFL ~ AFS)	Samples
SN74AHC1G14DCKRE4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFJ ~ AFL ~ AFS)	Samples
SN74AHC1G14DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFJ ~ AFL ~ AFS)	Samples
SN74AHC1G14DCKT	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFL ~ AFS)	Samples
SN74AHC1G14DCKTE4	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFL ~ AFS)	Samples
SN74AHC1G14DCKTG4	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	(AF3 ~ AFG ~ AFL ~ AFS)	Samples
SN74AHC1G14DRLR	ACTIVE	SOT	DRL	5	4000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	AFS	Samples
SN74AHC1G14DRLRG4	ACTIVE	SOT	DRL	5	4000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	AFS	Samples
SN74AHC1G14HDCK3	LIFEBUY	SC70	DCK	5		TBD	Call TI	Call TI	-40 to 125		

<sup>(1)</sup> The marketing status values are defined as follows: **ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.



#### PACKAGE OPTION ADDENDUM

19-Mar-2015

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between

the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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**PACKAGE MATERIALS INFORMATION** 

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#### TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	180.0	9.2	3.17	3.23	1.37	4.0	8.0	Q3
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	178.0	9.2	3.3	3.2	1.55	4.0	8.0	Q3
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	180.0	8.4	3.23	3.17	1.37	4.0	8.0	Q3
SN74AHC1G14DBVT	SOT-23	DBV	5	250	178.0	9.2	3.3	3.2	1.55	4.0	8.0	Q3
SN74AHC1G14DBVT	SOT-23	DBV	5	250	180.0	9.2	3.17	3.23	1.37	4.0	8.0	Q3
SN74AHC1G14DBVT	SOT-23	DBV	5	250	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
SN74AHC1G14DCKR	SC70	DCK	5	3000	178.0	9.0	2.4	2.5	1.2	4.0	8.0	Q3
SN74AHC1G14DCKR	SC70	DCK	5	3000	178.0	9.2	2.4	2.4	1.22	4.0	8.0	Q3
SN74AHC1G14DCKT	SC70	DCK	5	250	178.0	9.2	2.4	2.4	1.22	4.0	8.0	Q3
SN74AHC1G14DCKT	SC70	DCK	5	250	180.0	9.2	2.3	2.55	1.2	4.0	8.0	Q3
SN74AHC1G14DRLR	SOT	DRL	5	4000	180.0	8.4	1.98	1.78	0.69	4.0	8.0	Q3

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\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	205.0	200.0	33.0
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
SN74AHC1G14DBVR	SOT-23	DBV	5	3000	202.0	201.0	28.0
SN74AHC1G14DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
SN74AHC1G14DBVT	SOT-23	DBV	5	250	205.0	200.0	33.0
SN74AHC1G14DBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
SN74AHC1G14DCKR	SC70	DCK	5	3000	180.0	180.0	18.0
SN74AHC1G14DCKR	SC70	DCK	5	3000	180.0	180.0	18.0
SN74AHC1G14DCKT	SC70	DCK	5	250	180.0	180.0	18.0
SN74AHC1G14DCKT	SC70	DCK	5	250	205.0	200.0	33.0
SN74AHC1G14DRLR	SOT	DRL	5	4000	202.0	201.0	28.0

DBV (R-PDSO-G5)

### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. Falls within JEDEC MO-178 Variation AA.



## DBV (R-PDSO-G5)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



## DCK (R-PDSO-G5)

## PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. Falls within JEDEC MO-203 variation AA.



# DCK (R-PDSO-G5)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



# DRL (R-PDSO-N5)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M—1994.
- B. This drawing is subject to change without notice.
- Body dimensions do not include mold flash, interlead flash, protrusions, or gate burrs.

  Mold flash, interlead flash, protrusions, or gate burrs shall not exceed 0,15 per end or side.
- D. JEDEC package registration is pending.



## DRL (R-PDSO-N5)

#### PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Customers should contact their board fabrication site for minimum solder mask web tolerances between signal pads.
- E. Maximum stencil thickness 0,127 mm (5 mils). All linear dimensions are in millimeters.
- F. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC 7525 for stencil design considerations.
- G. Side aperture dimensions over—print land for acceptable area ratio > 0.66. Customer may reduce side aperture dimensions if stencil manufacturing process allows for sufficient release at smaller opening.



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