MC33078, MC33079

Low Noise Dual/Quad Operational Amplifiers

The MC33078/9 series is a family of high quality monolithic amplifiers employing Bipolar technology with innovative high performance concepts for quality audio and data signal processing applications. This family incorporates the use of high frequency PNP input transistors to produce amplifiers exhibiting low input voltage noise with high gain bandwidth product and slew rate. The all NPN output stage exhibits no deadband crossover distortion, large output voltage swing, excellent phase and gain margins, low open loop high frequency output impedance and symmetrical source and sink AC frequency performance.

The MC33078/9 family offers both dual and quad amplifier versions and is available in the plastic DIP and SOIC packages (P and D suffixes).

Features

- Dual Supply Operation: ±5.0 V to ±18 V
- Low Voltage Noise: 4.5 nV/$\sqrt{Hz}$
- Low Input Offset Voltage: 0.15 mV
- Low T.C. of Input Offset Voltage: 2.0 μV/°C
- Low Total Harmonic Distortion: 0.002%
- High Gain Bandwidth Product: 16 MHz
- High Slew Rate: 7.0 V/μs
- High Open Loop AC Gain: 800 @ 20 kHz
- Excellent Frequency Stability
- Large Output Voltage Swing: +14.1 V/ −14.6 V
- ESD Diodes Provided on the Inputs
- Pb−Free Packages are Available

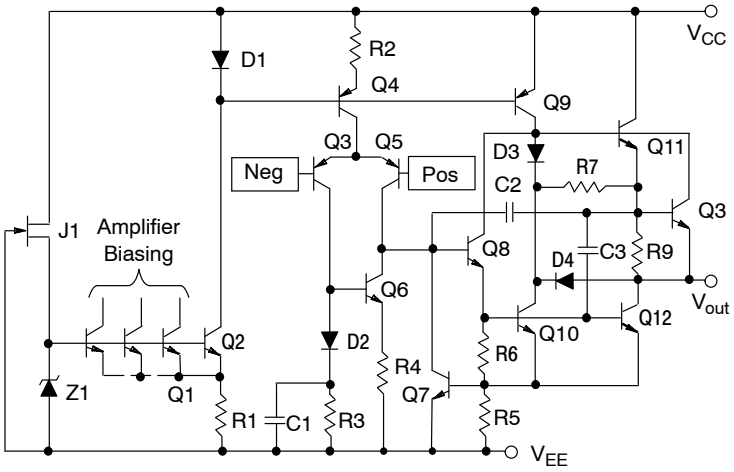
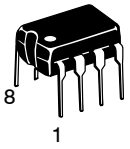


Figure 1. Representative Schematic Diagram
(Each Amplifier)

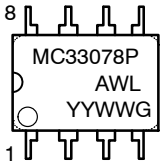
ON Semiconductor®

MARKING DIAGRAMS

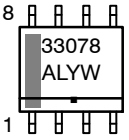
DUAL



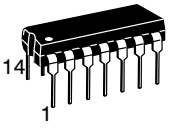
PDIP−8
P SUFFIX
CASE 626



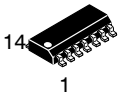
SOIC−8
D SUFFIX
CASE 751



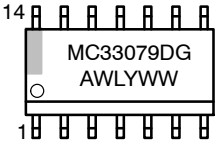
QUAD



PDIP−14
P SUFFIX
CASE 646



SOIC−14
D SUFFIX
CASE 751A



A = Assembly Location
WL, L = Wafer Lot
YY, Y = Year
WW, W = Work Week
G or ▪ = Pb−Free Package

ORDERING INFORMATION

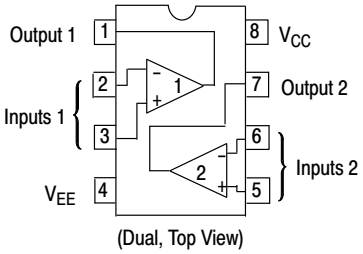
See detailed ordering and shipping information in the package dimensions section on page 10 of this data sheet.

Publication Order Number:
MC33078/D

PIN CONNECTIONS

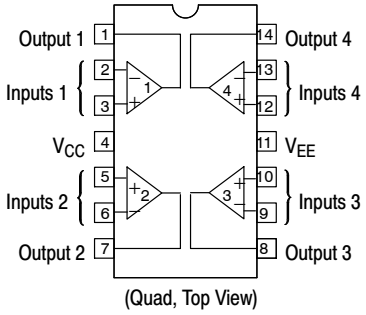
DUAL

CASE 626/751



QUAD

CASE 646/751A



MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Supply Voltage (V_{CC} to V_{EE})		V_S	+36	V
Input Differential Voltage Range		V_{IDR}	Note 1	V
Input Voltage Range		V_{IR}	Note 1	V
Output Short Circuit Duration (Note 2)		t_{SC}	Indefinite	sec
Maximum Junction Temperature		T_J	+150	°C
Storage Temperature		T_{stg}	−60 to +150	°C
ESD Protection at any Pin		V_{esd}		V
MC33078	− Human Body Model		600	
	− Machine Model		200	
MC33079	− Human Body Model		550	
	− Machine Model		150	
Maximum Power Dissipation		P_D	Note 2	mW
Operating Temperature Range		T_A	−40 to +85	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Either or both input voltages must not exceed the magnitude of V_{CC} or V_{EE}.
2. Power dissipation must be considered to ensure maximum junction temperature (T_J) is not exceeded (see Figure 2).

DC ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = −15 V, T_A = 25°C, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Input Offset Voltage (R_S = 10 Ω, V_{CM} = 0 V, V_O = 0 V)	$\|V_{IO}\|$				mV
(MC33078) T_A = +25°C		–	0.15	2.0	
T_A = −40° to +85°C		–	–	3.0	
(MC33079) T_A = +25°C		–	0.15	2.5	
T_A = −40° to +85°C		–	–	3.5	
Average Temperature Coefficient of Input Offset Voltage R_S = 10 Ω, V_{CM} = 0 V, V_O = 0 V, T_A = T_{low} to T_{high}	$\Delta V_{IO}/\Delta T$	–	2.0	–	μV/°C
Input Bias Current (V_{CM} = 0 V, V_O = 0 V)	I_{IB}				nA
T_A = +25°C		–	300	750	
T_A = −40° to +85°C		–	–	800	
Input Offset Current (V_{CM} = 0 V, V_O = 0 V)	I_{IO}				nA
T_A = +25°C		–	25	150	
T_A = −40° to +85°C		–	–	175	
Common Mode Input Voltage Range (ΔV_{IO} = 5.0 mV, V_O = 0 V)	V_{ICR}	±13	±14	–	V
Large Signal Voltage Gain (V_O = ±10 V, R_L = 2.0 kΩ)	A_{VOL}				dB
T_A = +25°C		90	110	–	
T_A = −40° to +85°C		85	–	–	
Output Voltage Swing (V_{ID} = ±1.0V)					V
R_L = 600 Ω	V_O+	–	+10.7	–	
R_L = 600 Ω	V_O−	–	−11.9	–	
R_L = 2.0 kΩ	V_O+	+13.2	+13.8	–	
R_L = 2.0 kΩ	V_O−	–	−13.7	−13.2	
R_L = 10 kΩ	V_O+	+13.5	+14.1	–	
R_L = 10 kΩ	V_O−	–	−14.6	−14	
Common Mode Rejection (V_{in} = ±13V)	CMR	80	100	–	dB
Power Supply Rejection (Note 3) V_{CC}/V_{EE} = +15 V/ −15 V to +5.0 V/ −5.0 V	PSR	80	105	–	dB
Output Short Circuit Current (V_{ID} = 1.0 V, Output to Ground)	I_{SC}				mA
Source		+15	+29	–	
Sink		−20	−37	–	
Power Supply Current (V_O = 0 V, All Amplifiers)	I_D				mA
(MC33078) T_A = +25°C		–	4.1	5.0	
T_A = −40° to +85°C		–	–	5.5	
(MC33079) T_A = +25°C		–	8.4	10	
T_A = −40° to +85°C		–	–	11	

3. Measured with V_{CC} and V_{EE} differentially varied simultaneously.

AC ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = −15 V, T_A = 25°C, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Slew Rate (V_{in} = −10 V to +10 V, R_L = 2.0 kΩ, C_L = 100 pF A_V = +1.0)	SR	5.0	7.0	–	V/μs
Gain Bandwidth Product (f = 100 kHz)	GBW	10	16	–	MHz
Unity Gain Bandwidth (Open Loop)	BW	–	9.0	–	MHz
Gain Margin (R_L = 2.0 kΩ) C_L = 0 pF C_L = 100 pF	A_m	 – –	 −11 −6.0	 – –	dB
Phase Margin (R_L = 2.0 kΩ) C_L = 0 pF C_L = 100 pF	ϕ_m	 – –	 55 40	 – –	Deg
Channel Separation (f = 20 Hz to 20 kHz)	CS	–	−120	–	dB
Power Bandwidth (V_O = 27 V_{pp}, R_L = 2.0 kΩ, THD ± 1.0%)	BW_p	–	120	–	kHz
Total Harmonic Distortion (R_L = 2.0 kΩ, f = 20 Hz to 20 kHz, V_O = 3.0 V_{rms}, A_V = +1.0)	THD	–	0.002	–	%
Open Loop Output Impedance (V_O = 0 V, f = 9.0 MHz)	$\|Z_O\|$	–	37	–	Ω
Differential Input Resistance (V_{CM} = 0 V)	R_{in}	–	175	–	kΩ
Differential Input Capacitance (V_{CM} = 0 V)	C_{in}	–	12	–	pF
Equivalent Input Noise Voltage (R_S = 100 Ω, f = 1.0 kHz)	e_n	–	4.5	–	nV/√Hz
Equivalent Input Noise Current (f = 1.0 kHz)	i_n	–	0.5	–	pA/√Hz

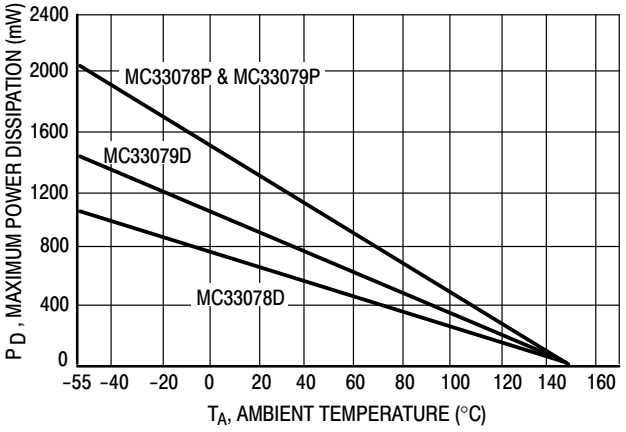


Figure 2. Maximum Power Dissipation versus Temperature

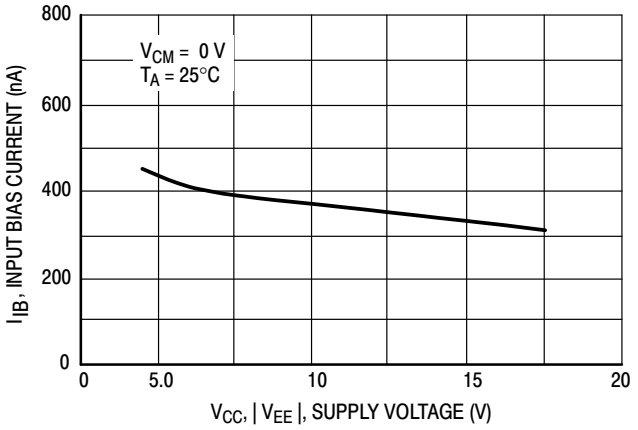


Figure 3. Input Bias Current versus Supply Voltage

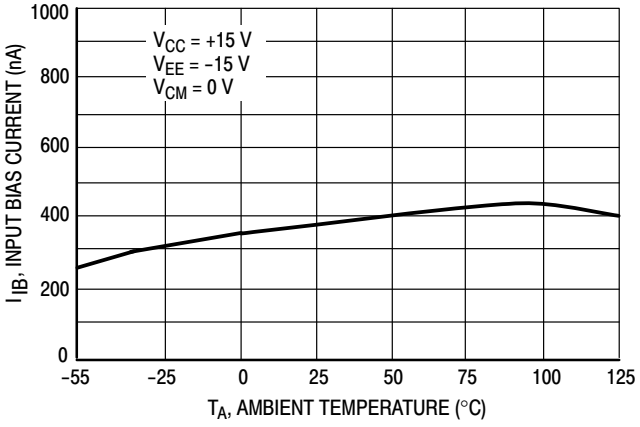


Figure 4. Input Bias Current versus Temperature

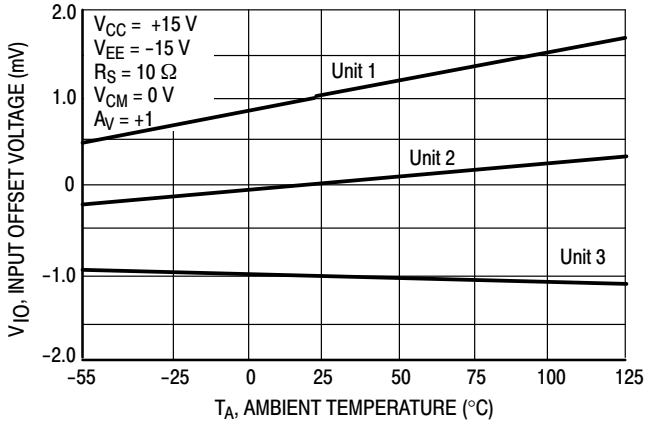
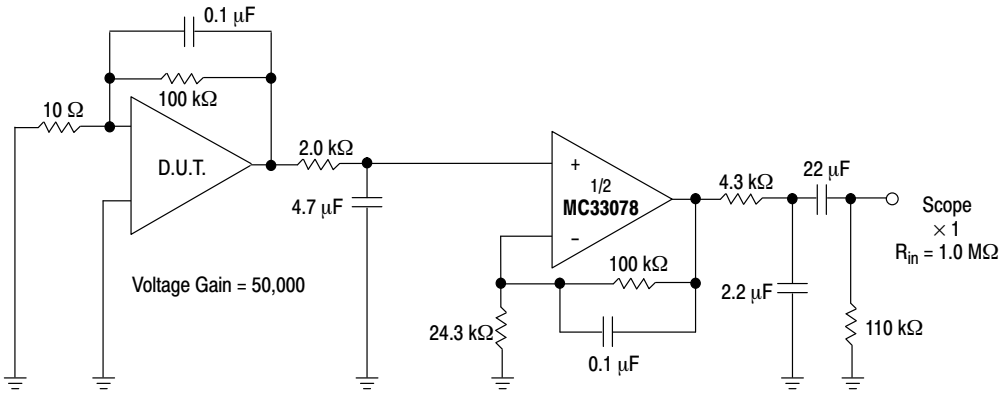


Figure 5. Input Offset Voltage versus Temperature



Note: All capacitors are non−polarized.

Figure 35. Voltage Noise Test Circuit (0.1 Hz to 10 Hz_{p-p})

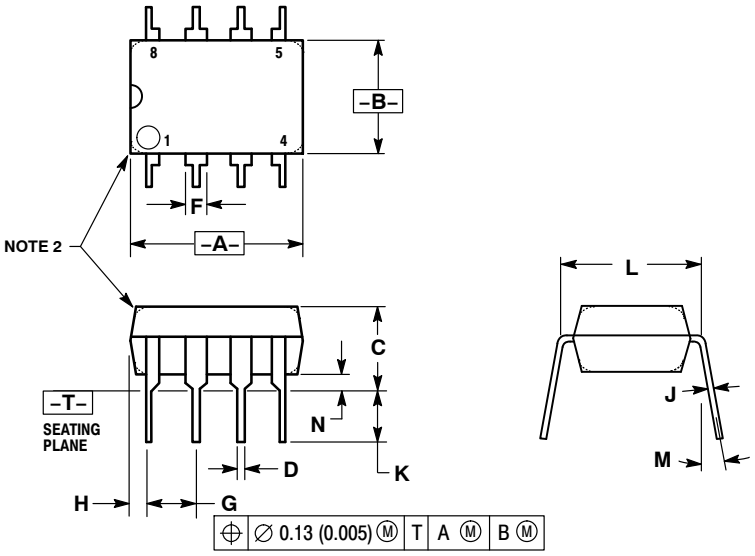
ORDERING INFORMATION

Device	Package	Shipping†
MC33078D	SOIC−8	98 Units / Rail
MC33078DG	SOIC−8 (Pb−Free)	
MC33078DR2	SOIC−8	2500 / Tape & Reel
MC33078DR2G	SOIC−8 (Pb−Free)	
MC33078P	PDIP−8	50 Units / Rail
MC33078PG	PDIP−8 (Pb−Free)	
MC33079D	SOIC−14	55 Units / Rail
MC33079DG	SOIC−14 (Pb−Free)	
MC33079DR2	SOIC−14	2500 / Tape & Reel
MC33079DR2G	SOIC−14 (Pb−Free)	
MC33079P	PDIP−14	25 Units / Rail
MC33079PG	PDIP−14 (Pb−Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

PDIP−8
P SUFFIX
CASE 626−05
ISSUE L



NOTES:
1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	---	10°	---	10°
N	0.76	1.01	0.030	0.040