40V DUAL NPN SMALL SIGNAL TRANSISTOR IN SOT363

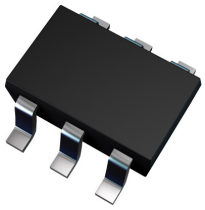
Features

- Epitaxial Planar Die Construction
- Ideal for Medium Power Amplification and Switching
- Ultra-Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

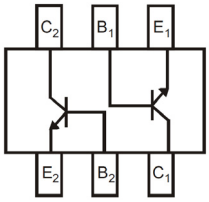
Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound,
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Finish. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.006 grams (approximate)

SOT363



Top View



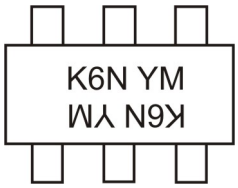
Device Schematic
Top View

Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMDT3904-7-F	K6N	7	8	3,000

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



K6N = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: X = 2010)
M = Month (ex: 9 = September)

Date Code Key

Year	2010	2011	2012	2013	2014	2015	2016	2017
Code	X	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Continuous Collector Current	I_C	200	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	625	°C/W
Operating and Storage and Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS					
Collector-Base Breakdown Voltage	BV_{CBO}	60	—	V	I_C = 100µA, I_E = 0
Collector-Emitter Breakdown Voltage (Note 6)	BV_{CEO}	40	—	V	I_C = 10.0mA, I_B = 0
Emitter-Base Breakdown Voltage	BV_{EBO}	6.0	—	V	I_E = 100µA, I_C = 0
Collector-Base Cutoff Current	I_{CBO}	—	50	nA	V_{CB} = 50V
Collector-Emitter Cutoff Current	I_{CEV}	—	50	nA	V_{CE} = 40V, $V_{BE(OFF)}$ = 3.0V
			50		V_{CE} = 40V, $V_{BE(ON)}$ = 0.25V
Emitter-Base Cutoff Current	I_{EBO}	—	50	nA	V_{EB} = 5V
ON CHARACTERISTICS (Note 6)					
DC Current Gain	h_{FE}	40 70 100 60 30	— — 300 — —	—	I_C = 100µA, V_{CE} = 1.0V I_C = 1.0mA, V_{CE} = 1.0V I_C = 10mA, V_{CE} = 1.0V I_C = 50mA, V_{CE} = 1.0V I_C = 100mA, V_{CE} = 1.0V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	0.20 0.30	V	I_C = 10mA, I_B = 1.0mA I_C = 50mA, I_B = 5.0mA
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	0.65 —	0.85 0.95	V	I_C = 10mA, I_B = 1.0mA I_C = 50mA, I_B = 5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	—	4.0	pF	V_{CB} = 5.0V, f = 1.0MHz, I_E = 0
Input Capacitance	C_{ibo}	—	8.0	pF	V_{EB} = 0.5V, f = 1.0MHz, I_C = 0
Input Impedance	h_{ie}	1.0	10	kΩ	V_{CE} = 10V, I_C = 1.0mA, f = 1.0kHz
Voltage Feedback Ratio	h_{re}	0.5	8.0	x 10^{-4}	
Small Signal Current Gain	h_{fe}	100	400	—	
Output Admittance	h_{oe}	1.0	40	µS	
Current Gain-Bandwidth Product	f_T	300	—	MHz	V_{CE} = 20V, I_C = 10mA, f = 100MHz
Noise Figure	NF	—	5.0	dB	V_{CE} = 5.0V, I_C = 100µA, R_S = 1.0kΩ, f = 1.0kHz
SWITCHING CHARACTERISTICS					
Delay Time	t_d	—	35	ns	V_{CC} = 3.0V, I_C = 10mA, $V_{BE(off)}$ = - 0.5V, I_{B1} = 1.0mA
Rise Time	t_r	—	35	ns	
Storage Time	t_s	—	200	ns	V_{CC} = 3.0V, I_C = 10mA, I_{B1} = I_{B2} = 1.0mA
Fall Time	t_f	—	50	ns	

Notes: 5. For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
6. Short duration pulse test used to minimize self-heating effect.

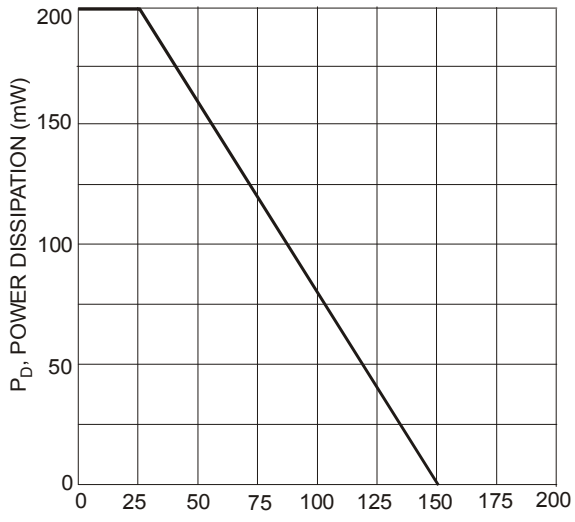


Fig. 1, Power Dissipation vs. Ambient Temperature (Total Device)

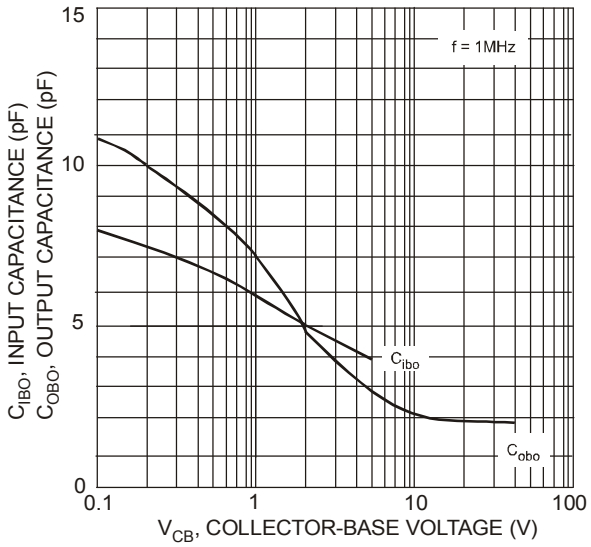


Fig. 2, Input and Output Capacitance vs. Collector-Base Voltage

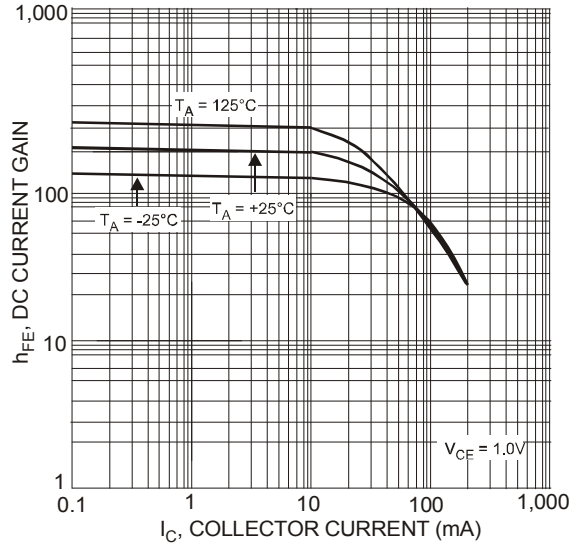


Fig. 3, Typical DC Current Gain vs. Collector Current

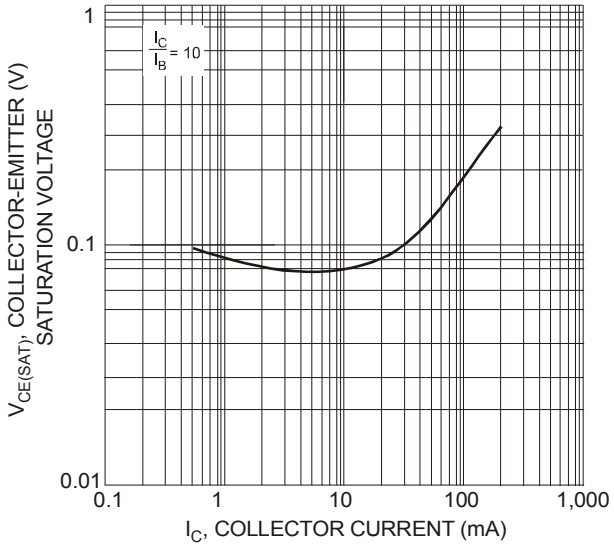


Fig. 4, Typical Collector-Emitter Saturation Voltage vs. Collector Current

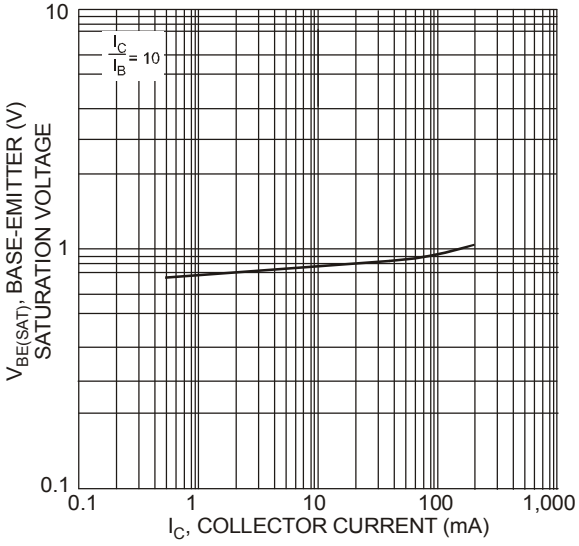
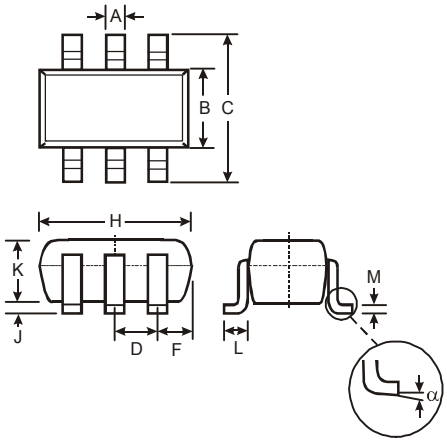


Fig. 5, Typical Base-Emitter Saturation Voltage vs. Collector Current

Package Outline Dimensions

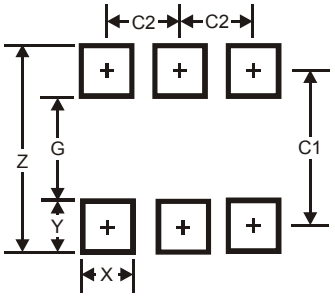
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363			
Dim	**Min**	**Max**	**Typ**
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

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