

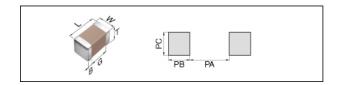
☆TDK Multilayer Ceramic Chip Capacitors

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Creation Date: April 21, 2014

C1005X5R0J105K050BB (TDK item description : C1005X5R0J105KT****)

Applications Commercial Grade Feature General General (Up to 50V) C1005 [EIA CC0402] Series











Size	
Length(L)	1.00mm +/-0.05mm
Width(W)	0.50mm +/-0.05mm
Thickness(T)	0.50mm +/-0.05mm
Terminal Width(B)	0.10mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern(PA)	0.30 to 0.50mm
Recommended Land Pattern(PB)	0.35 to 0.45mm
Recommended Land Pattern(PC)	0.40 to 0.60mm
Recommended Slit Pattern(SD)	

Electrical Characteristics	
Capacitance	1uF +/-10%
Rated Voltage	6.3Vdc
Temperature Characteristic	X5R(+/-15%)
Dissipation Factor	10% Max.
Insulation Resistance	100MΩ Min.

Other	
Soldering Method	Reflow
AEC Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	10000Pcs Min.

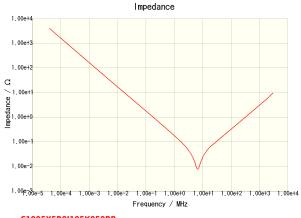
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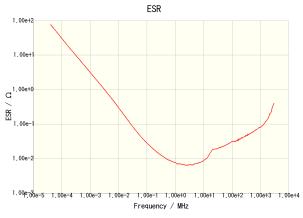
[•]All specifications are subject to change without notice.

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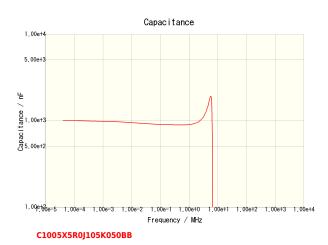
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Characteristic Graphs (This is reference data, and does not guarantee the product's characteristics.)

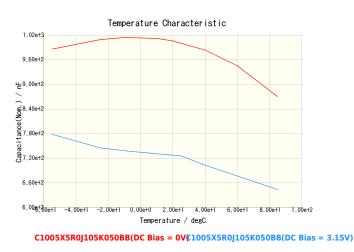


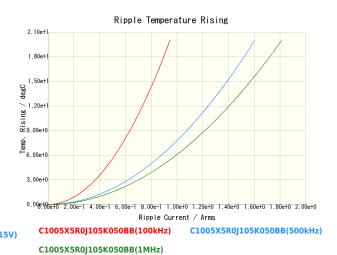


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