25AA320A/25LC320A

32K SPI Bus Serial EEPROM

Device Selection Table

Part Number	Vcc Range	Page Size	Temp. Ranges	Packages
25LC320A	2.5-5.5V	32 Byte	I,E	P, SN, ST, MS, MNY
25AA320A	1.8-5.5V	32 Byte	I	P, SN, ST, MS, MNY

Features:

- Max. Clock 10 MHz
- Low-Power CMOS Technology
  - Max. Write Current: 5 mA at 5.5V, 10 MHz
  - Read Current: 5 mA at 5.5V, 10 MHz
  - Standby Current: 5 μA at 5.5V
- 4096 x 8-bit Organization
- 32-Byte Page
- Self-Timed Erase and Write Cycles (5 ms max.)
- Block Write Protection
  - Protect none, 1/4, 1/2 or all of array
- Built-in Write Protection
  - Power-on/off data protection circuitry
  - Write enable latch
  - Write-protect pin
- Sequential Read
- High Reliability
  - Endurance: >1M erase/write cycles
  - Data retention: > 200 years
  - ESD protection: > 4000V
- Temperature Ranges Supported;
  - Industrial (I): -40°C to +85°C
  - Automotive (E): -40°C to +125°C
- Standard and Pb-Free Packages Available

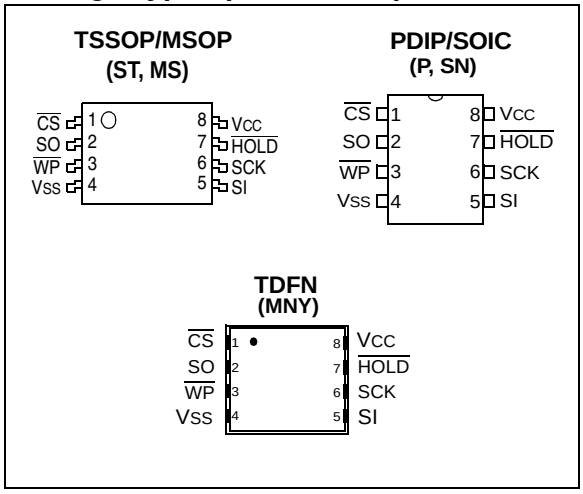
Description:

The Microchip Technology Inc. 25AA320A/25LC320A (25XX320A*) are 32 kbit Serial Electrically Erasable PROMs. The memory is accessed via a simple Serial Peripheral Interface (SPI) compatible serial bus. The bus signals required are a clock input (SCK) plus separate data in (SI) and data out (SO) lines. Access to the device is controlled through a Chip Select (CS) input.

Communication to the device can be paused via the hold pin (HOLD). While the device is paused, transitions on its inputs will be ignored, with the exception of Chip Select, allowing the host to service higher priority interrupts.

The 25XX320A is available in standard packages including 8-lead PDIP and SOIC, and advanced packaging including 8-lead MSOP, TSSOP and 2x3 TDFN. All packages are Pb-free.

Package Types (not to scale)



*25XX320A is used in this document as a generic part number for the 25AA320A, 25LC320A devices.

1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings(†)

VCC.....6.5V

All inputs and outputs w.r.t. VSS ..... -0.6V to VCC +1.0V

Storage temperature .....-65°C to 150°C

Ambient temperature under bias.....-65°C to 125°C

ESD protection on all pins.....4 kV

† NOTICE**:** Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for an extended period of time may affect device reliability.

TABLE 1-1: DC CHARACTERISTICS

DC CHARACTERISTICS			Industrial (I): TA = -40°C to +85°C VCC = 1.8V to 5.5V Automotive (E): TA = -40°C to +125°C VCC = 2.5V to 5.5V			
Param. No.	**Sym.**	**Characteristic**	**Min.**	**Max.**	**Units**	**Test Conditions**
D001	VIH1	High-level input voltage	.7 VCC	VCC+1	V	
D002	VIL1	Low-level input voltage	-0.3	0.3VCC	V	VCC ≥ 2.7V
D003	VIL2		-0.3	0.2VCC	V	VCC < 2.7V
D004	VOL	Low-level output voltage	—	0.4	V	IOL = 2.1 mA
D005	VOL		—	0.2	V	IOL = 1.0 mA, VCC < 2.5V
D006	VOH	High-level output voltage	VCC -0.5	—	V	IOH = -400 µA
D007	ILI	Input leakage current	—	±1	µA	$\overline{CS}$ = VCC, VIN = VSS TO VCC
D008	ILO	Output leakage current	—	±1	µA	$\overline{CS}$ = VCC, VOUT = VSS TO VCC
D009	CINT	Internal Capacitance (all inputs and outputs)	—	7	pF	TA = 25°C, CLK = 1.0 MHz, VCC = 5.0V **(Note)**
D010	ICC Read	Operating Current	— —	5 2.5	mA mA	VCC = 5.5V; FCLK = 10.0 MHz; SO = Open VCC = 2.5V; FCLK = 5.0 MHz; SO = Open
D011	ICC Write		— —	5 3	mA mA	VCC = 5.5V VCC = 2.5V
D012	ICCS	Standby Current	— —	5 1	µA µA	$\overline{CS}$ = VCC = 5.5V, Inputs tied to VCC or VSS, 125°C $\overline{CS}$ = VCC = 5.5V, Inputs tied to VCC or VSS, 85°C

Note: This parameter is periodically sampled and not 100% tested.

TABLE 1-2: AC CHARACTERISTICS

AC CHARACTERISTICS			Industrial (I): TA = -40°C to +85°C VCC = 1.8V to 5.5V Automotive (E): TA = -40°C to +125°C VCC = 2.5V to 5.5V			
Param. No.	**Sym.**	**Characteristic**	**Min.**	**Max.**	**Units**	**Test Conditions**
1	FCLK	Clock frequency	— — —	10 5 3	MHz MHz MHz	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
2	TCSS	$\overline{CS}$ setup time	50 100 150	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
3	TCSH	$\overline{CS}$ hold time	100 200 250	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
4	TCSD	$\overline{CS}$ disable time	50	—	ns	—
5	Tsu	Data setup time	10 20 30	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
6	THD	Data hold time	20 40 50	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
7	TR	CLK rise time	—	2	µs	**(Note 1)**
8	TF	CLK fall time	—	2	µs	**(Note 1)**
9	THI	Clock high time	50 100 150	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
10	TLO	Clock low time	50 100 150	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
11	TCLD	Clock delay time	50	—	ns	—
12	TCLE	Clock enable time	50	—	ns	—
13	TV	Output valid from clock low	— — —	50 100 160	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
14	THO	Output hold time	0	—	ns	**(Note 1)**
15	TDIS	Output disable time	— — —	40 80 160	ns ns ns	4.5V ≤ Vcc ≤ 5.5V**(Note 1)** 2.5V ≤ Vcc ≤ 4.5V**(Note 1)** 1.8V ≤ Vcc ≤ 2.5V**(Note 1)**
16	THS	$\overline{HOLD}$ setup time	20 40 80	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V

Note 1: This parameter is periodically sampled and not 100% tested.

2: TWC begins on the rising edge of $\overline{CS}$ after a valid write sequence and ends when the internal write cycle is complete.

3: This parameter is not tested but ensured by characterization. For endurance estimates in a specific application, please consult the Total Endurance™ Model which can be obtained from Microchip's web site

TABLE 1-2: AC CHARACTERISTICS (CONTINUED)

AC CHARACTERISTICS			Industrial (I): TA = -40°C to +85°C VCC = 1.8V to 5.5V Automotive (E): TA = -40°C to +125°C VCC = 2.5V to 5.5V			
Param. No.	**Sym.**	**Characteristic**	**Min.**	**Max.**	**Units**	**Test Conditions**
17	THH	$\overline{HOLD}$ hold time	20 40 80	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
18	THZ	$\overline{HOLD}$ low to output High-Z	30 60 160	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V**(Note 1)** 2.5V ≤ Vcc < 4.5V**(Note 1)** 1.8V ≤ Vcc < 2.5V**(Note 1)**
19	THV	$\overline{HOLD}$ high to output valid	30 60 160	— — —	ns ns ns	4.5V ≤ Vcc ≤ 5.5V 2.5V ≤ Vcc < 4.5V 1.8V ≤ Vcc < 2.5V
20	TWC	Internal write cycle time	—	5	ms	**(NOTE 2)**
21	—	Endurance	1M	—	E/W Cycles	**(NOTE 3)**

Note 1: This parameter is periodically sampled and not 100% tested.

2: TWC begins on the rising edge of $\overline{CS}$ after a valid write sequence and ends when the internal write cycle is complete.

3: This parameter is not tested but ensured by characterization. For endurance estimates in a specific application, please consult the Total Endurance™ Model which can be obtained from Microchip's web site at www.microchip.com.

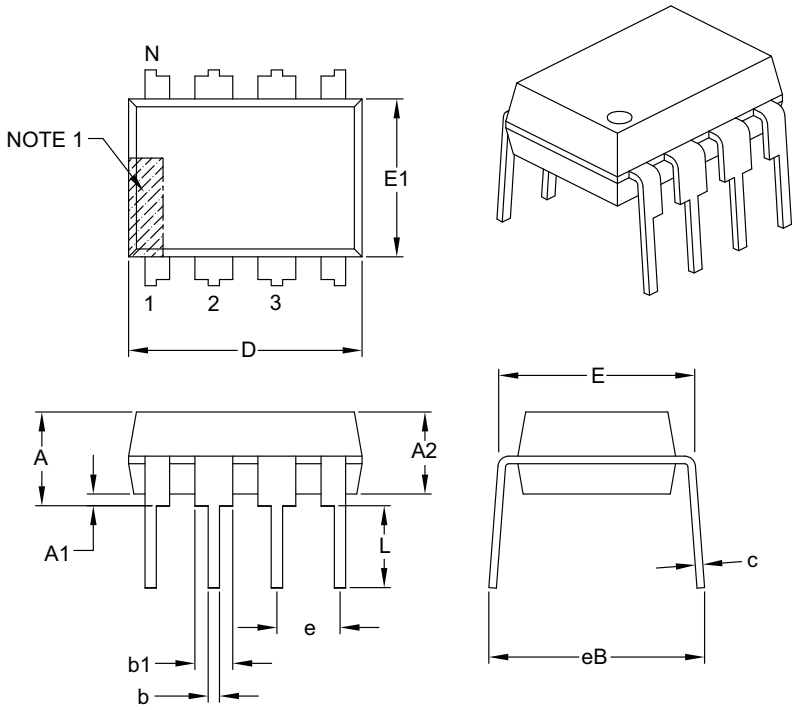
TABLE 1-3: AC TEST CONDITIONS

AC Waveform:	
VLO = 0.2V	—
VHI = VCC - 0.2V	**(Note 1)**
VHI = 4.0V	**(Note 2)**
CL = 50 pF	**—**
Timing Measurement Reference Level	
Input	0.5 VCC
Output	0.5 VCC

Note 1: For VCC ≤ 4.0V

2: For VCC > 4.0V

8-Lead Plastic Dual In-Line (P) – 300 mil Body [PDIP]



Units		INCHES		
Dimension Limits		MIN	NOM	MAX
Number of Pins	N		8	
Pitch	e		.100 BSC	
Top to Seating Plane	A	–	–	.210
Molded Package Thickness	A2	.115	.130	.195
Base to Seating Plane	A1	.015	–	–
Shoulder to Shoulder Width	E	.290	.310	.325
Molded Package Width	E1	.240	.250	.280
Overall Length	D	.348	.365	.400
Tip to Seating Plane	L	.115	.130	.150
Lead Thickness	c	.008	.010	.015
Upper Lead Width	b1	.040	.060	.070
Lower Lead Width	b	.014	.018	.022
Overall Row Spacing §	eB	–	–	.430

Notes:

1. Pin 1 visual index feature may vary, but must be located with the hatched area.
2. § Significant Characteristic.
3. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .010" per side.
4. Dimensioning and tolerancing per ASME Y14.5M.

   BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-018B

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO.	X	–	X	/XX
Device	Tape & Reel		Temp Range	Package

Device:	25AA320A =		32k-bit, 1.8V, SPI Serial EEPROM
	25LC320A =		32k-bit, 2.5V, SPI Serial EEPROM
Tape & Reel:	Blank	=	Standard packaging
	T	=	Tape & Reel
Temperature Range:	I	=	-40°C to+85°C
	E	=	-40°C to+125°C
Package:	MS	=	Plastic MSOP (Micro Small Outline), 8-lead
	P	=	Plastic DIP (300 mil body), 8-lead
	SN	=	Plastic SOIC (3.90 mm body), 8-lead
	ST	=	TSSOP, 8-lead
	MNY(1)	=	TDFN, 8-lead 2x3 mm

Note 1: "Y" indicates a Nickel Palladium Gold (NiPdAu) finish.

Examples:

a) 25AA320A-I/MS = 32k-bit, 1.8V Serial EEPROM, Industrial temp., MSOP package

b) 25AA320AT-I/SN = 32k-bit, 1.8V Serial EEPROM, Industrial temp., Tape & Reel, SOIC package

c) 25LC320AT-E/SN = 32k-bit, 2.5V Serial EEPROM, Extended temp., Tape & Reel, SOIC package

d) 25LC320AT-I/ST = 32k-bit, 2.5V Serial EEPROM, Industrial temp., Tape & Reel, TSSOP package