FAIRCHILD
SEMICONDUCTOR®

May 2003

ISL9R860PF2

8 A, 600 V, STEALTH™ Diode

Features

- Stealth Recovery t_{rr} = 28 ns (@I_F = 8 A)
- Max Forward Voltage, V_F = 2.4 V (@ T_C = 25°C)
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS compliant

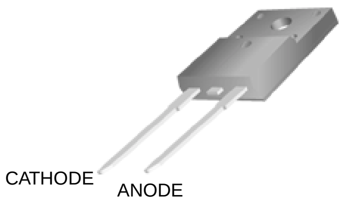
Applications

- Switch Mode Power Supplies
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

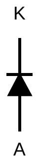
The ISL9R860PF2 is a STEALTH™ diode optimized for low loss performance in high frequency hard switched applications. The STEALTH™ family exhibits low reverse recovery current ($I_{RM(REC)}$) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH™ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package

TO-220F



Symbol



Device Maximum Ratings T_C= 25°C unless otherwise noted

Symbol	Parameter	Rating	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V_{RWM}	Working Peak Reverse Voltage	600	V
V_R	DC Blocking Voltage	600	V
$I_{F(AV)}$	Average Rectified Forward Current (T_C = 75°C)	8	A
I_{FRM}	Repetitive Peak Surge Current (20 kHz Square Wave)	16	A
I_{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60 Hz)	100	A
P_D	Power Dissipation	26	W
E_{AVL}	Avalanche Energy (1 A, 40 mH)	20	mJ
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	°C
T_L	Maximum Temperature for Soldering Leads at 0.063in (1.6 mm) from Case for 10s	300	°C

CAUTION: Stresses above those listed in “Device Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
R860PF2	ISL9R860PF2	TO-220F	N/A	50 Units

Electrical Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
Off State Characteristics							
I_R	Instantaneous Reverse Current	V_R = 600 V	T_C = 25°C	-	-	100	µA
			T_C = 125°C	-	-	1.0	mA
On State Characteristics							
V_F	Instantaneous Forward Voltage	I_F = 8 A	T_C = 25°C	-	2.0	2.4	V
			T_C = 125°C	-	1.6	2.0	V
Dynamic Characteristics							
C_J	Junction Capacitance	V_R = 10V , I_F = 0 A		-	30	-	pF
Switching Characteristics							
t_{rr}	Reverse Recovery Time	I_F = 1 A, dI_F/dt = 100 A/µs, V_R = 30 V		-	18	25	ns
		I_F = 8 A, dI_F/dt = 100 A/µs, V_R = 30 V		-	21	30	ns
t_{rr}	Reverse Recovery Time	I_F = 8 A, dI_F/dt = 200 A/µs, V_R = 390 V, T_C = 25°C		-	28	-	ns
I_{rr}	Maximum Reverse Recovery Current			-	3.2	-	A
Q_{rr}	Reverse Recovery Charge			-	50	-	nC
t_{rr}	Reverse Recovery Time	I_F = 8 A, dI_F/dt = 200 A/µs, V_R = 390 V, T_C = 125°C		-	77	-	ns
S	Softness Factor (t_b/t_a)			-	3.7	-	
I_{rr}	Maximum Reverse Recovery Current			-	3.4	-	A
Q_{rr}	Reverse Recovery Charge			-	150	-	nC
t_{rr}	Reverse Recovery Time	I_F = 8 A, dI_F/dt = 600 A/µs, V_R = 390 V, T_C = 125°C		-	53	-	ns
S	Softness Factor (t_b/t_a)			-	2.5	-	
I_{rr}	Maximum Reverse Recovery Current			-	6.5	-	A
Q_{rr}	Reverse Recovery Charge				195	-	nC
dI_M/dt	Maximum di/dt during t_b			-	500	-	A/µs
Thermal Characteristics							
$R_{\theta JC}$	Thermal Resistance Junction to Case			-	-	4.8	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220F		-	-	70	°C/W

Typical Performance Curves

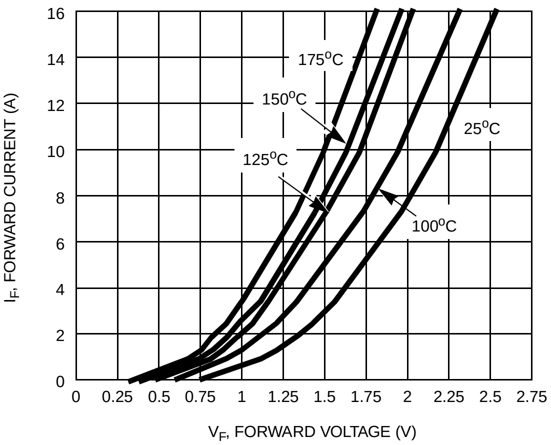


Figure 1. Forward Current vs Forward Voltage

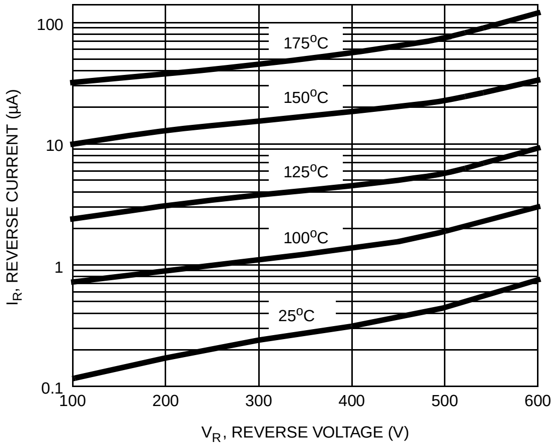


Figure 2. Reverse Current vs Reverse Voltage

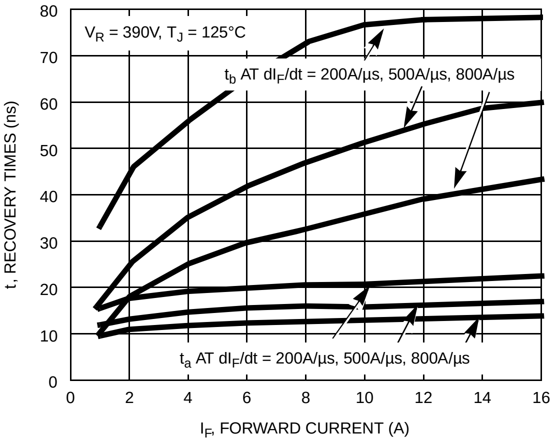


Figure 3. t_a and t_b Curves vs Forward Current

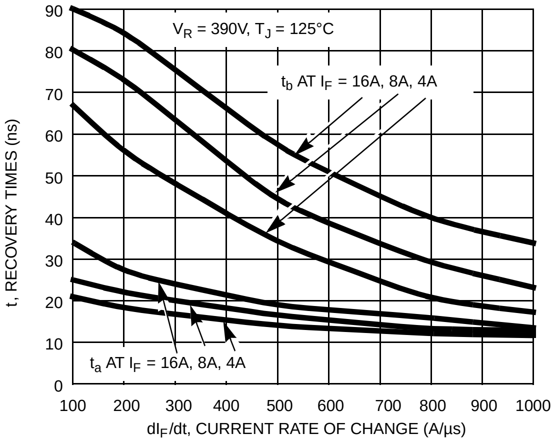


Figure 4. t_a and t_b Curves vs dI_F/dt

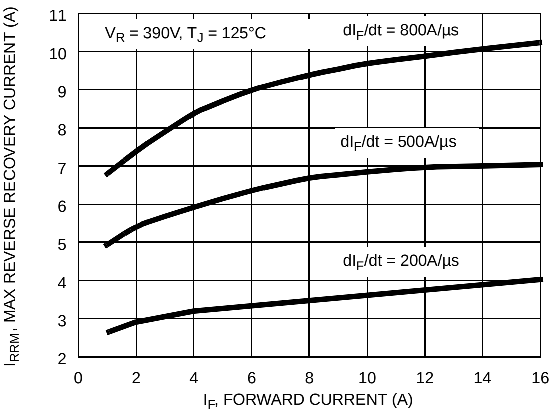


Figure 5. Maximum Reverse Recovery Current vs Forward Current

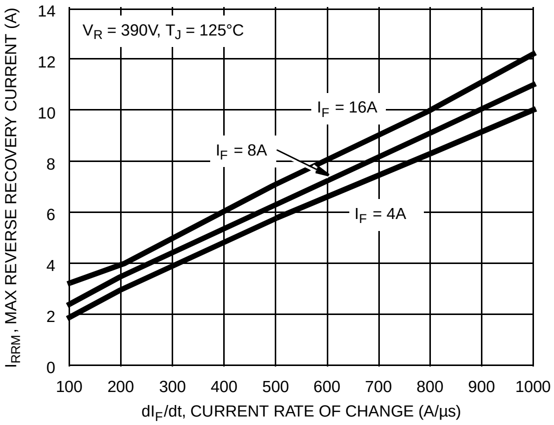


Figure 6. Maximum Reverse Recovery Current vs dI_F/dt

Typical Performance Curves (Continued)

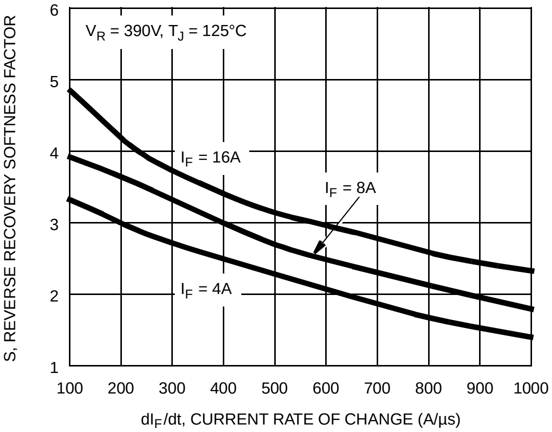


Figure 7. Reverse Recovery Softness Factor vs dI_F/dt

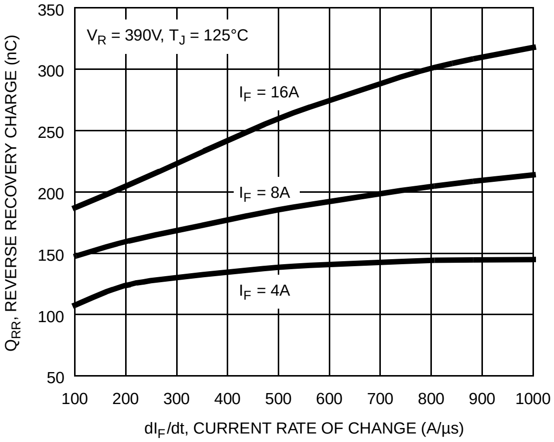


Figure 8. Reverse Recovery Charge vs dI_F/dt

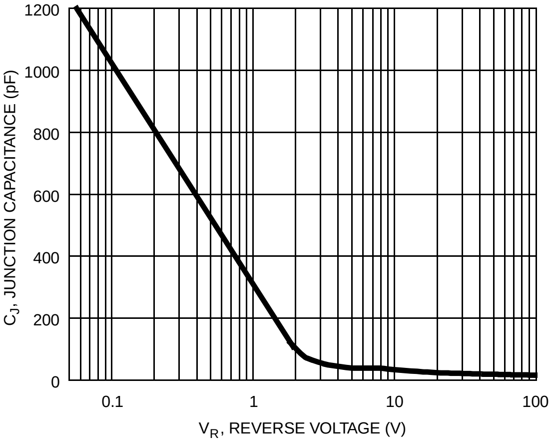


Figure 9. Junction Capacitance vs Reverse Voltage

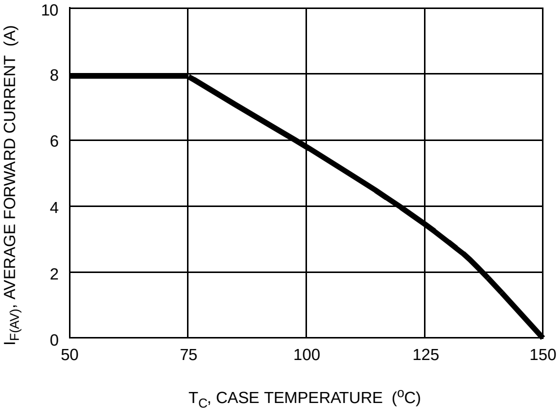


Figure 10. DC Current Derating Curve

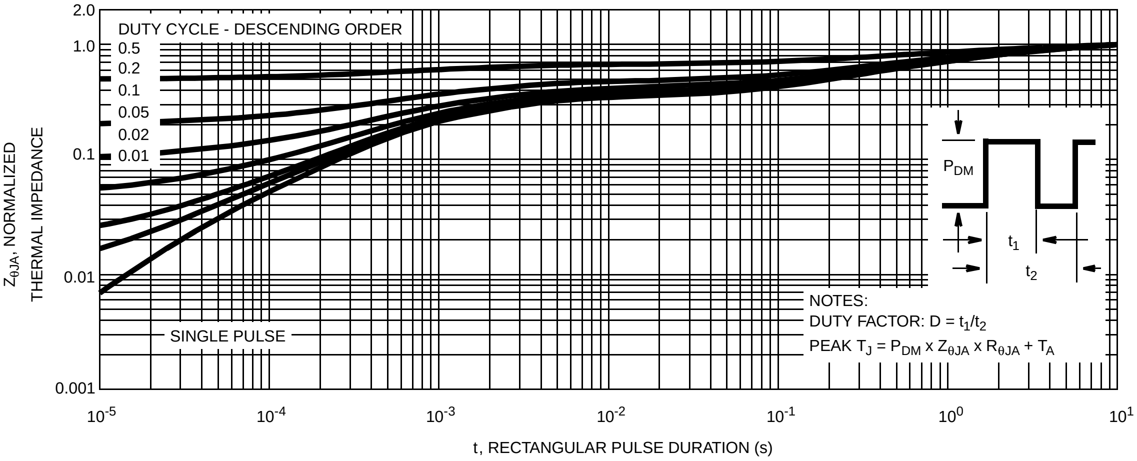


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuits and Waveforms

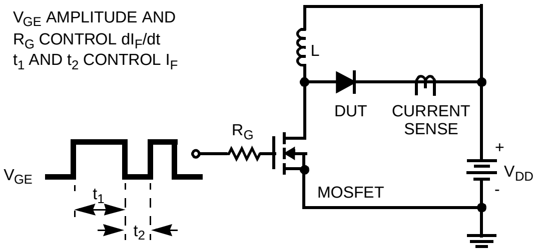


Figure 12. t_{rr} Test Circuit

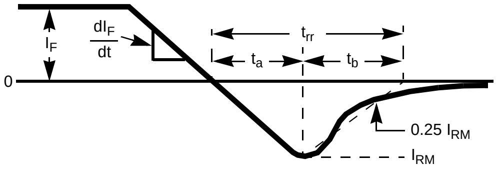


Figure 13. t_{rr} Waveforms and Definitions

I = 1A
L = 40mH
R < 0.1Ω
V_{DD} = 50V
$E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
Q_1 = IGBT (BV_{CES} > DUT $V_{R(AVL)}$)

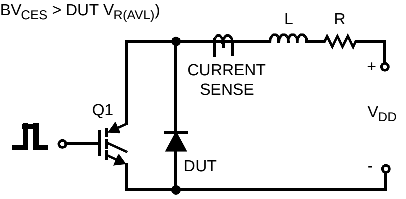


Figure 14. Avalanche Energy Test Circuit

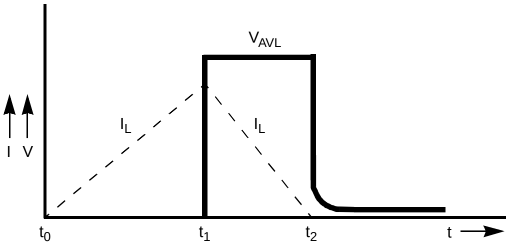


Figure 15. Avalanche Current and Voltage Waveforms

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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