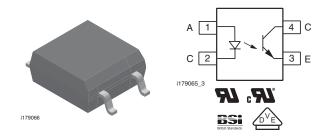


**Vishay Semiconductors** 

# Optocoupler Phototransistor Output, SOP-4, 100 mil pitch, **Mini-Flat Package**



### DESCRIPTION

Document Number: 83686

Rev. 2.1, 17-May-11

The SFH690ABT, SFH690AT, SFH690BT, SFH690CT, SFH690DT family has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin 100 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits. The SFH690 series is available only on tape and reel. There are 2000 parts per reel. Marking for SFH690AT is 690A; SFH690BT is 690B; SFH690CT is 690C; SFH690DT is 690D; SFH690ABT will be marked as 690A or 690B.

### **FEATURES**

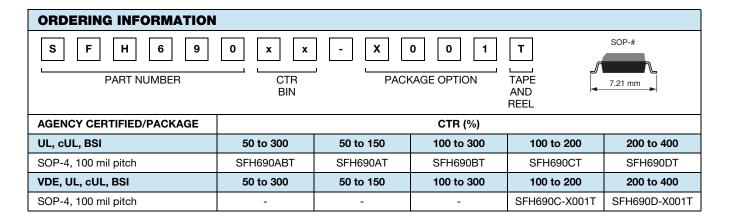
- SOP (small outline package)
- Isolation test voltage, 3750 V<sub>BMS</sub> (1 s)
- High collector emitter breakdown voltage,  $V_{CEO} = 70 V$
- Low saturation voltage
- · Fast switching times
- Temperature stable
- · Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **APPLICATIONS**

- · High density mounting or space sensitive PCBs
- PLCs
- Telecommunication

### AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- cUL tested to CSA 22.2 bulletin 5A
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884) available with option 1



RoHS COMPLIANT **GREEN** (5-2008)

For technical questions, contact: optocoupleranswers@vishay.com

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ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>amb</sub> = 25 °C, unless otherwis	e specified)			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V <sub>R</sub>	6	V	
DC forward current		١ <sub>F</sub>	50	mA	
Surge forward current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	2.5	А	
Power dissipation		P <sub>diss</sub>	80	mW	
OUTPUT	·				
Collector emitter voltage		V <sub>CEO</sub>	70	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
		Ι <sub>C</sub>	50	mA	
Collector current	t <sub>p</sub> ≤ 1 ms	Ι <sub>C</sub>	100	mA	
Power dissipation		P <sub>diss</sub>	150	mW	
COUPLER					
Isolation test voltage between emitter and detector (1 s)		V <sub>ISO</sub>	3750	V <sub>RMS</sub>	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
ISUIDIN TESISIONCE	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C	
Ambient temperature range		T <sub>amb</sub>	- 55 to + 100	°C	
Soldering temperature <sup>(1)</sup>	max. 10 s dip soldering distance to seating plane $\geq$ 1.5 mm	T <sub>sld</sub>	260	°C	

Notes

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

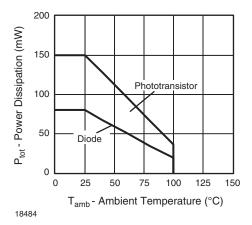


Fig. 1 - Permissible Power Dissipation vs. Ambient Temperature

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Optocoupler Phototransistor Output, SOP-4, Vishay Semiconductors 100 mil pitch, Mini-Flat Package

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I <sub>F</sub> = 5 mA	V <sub>F</sub>		1.15	1.4	V	
Reverse current	V <sub>R</sub> = 6 V	I <sub>R</sub>		0.01	10	μA	
Capacitance	$V_R = 0 V, f = 1 MHz$	Co		14		pF	
Thermal resistance		R <sub>thJA</sub>		750		K/W	
OUTPUT							
Collector emitter leakage current	V <sub>CE</sub> = 20 V	I <sub>CEO</sub>			100	nA	
Collector emitter capacitance	$V_{CE} = 5 V$ , f = 1 MHz	C <sub>CE</sub>		2.8		pF	
Thermal resistance		R <sub>thJA</sub>		500		K/W	
COUPLER							
Collector emitter saturation voltage	l <sub>F</sub> = 10 mA, l <sub>C</sub> = 2 mA	V <sub>CEsat</sub>		0.1	0.3	V	
Coupling capacitance	f = 1 MHz	C <sub>C</sub>		0.3		pF	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		SFH690ABT	CTR	50		300	%
I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	SFH690AT	CTR	50		150	%
		SFH690BT	CTR	100		300	%
		SFH690CT	CTR	100		200	%
		SFH690DT	CTR	200		400	%

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$I_{C}$ = 2 mA, $V_{CC}$ = 5 V, $R_{L}$ = 100 $\Omega$	t <sub>r</sub>		3		μs
Fall time	$I_{C}$ = 2 mA, $V_{CC}$ = 5 V, $R_{L}$ = 100 $\Omega$	t <sub>f</sub>		4		μs
Turn-on time	$I_C$ = 2 mA, $V_{CC}$ = 5 V, $R_L$ = 100 $\Omega$	t <sub>on</sub>		5		μs
Turn-off time	$I_{C}$ = 2 mA, $V_{CC}$ = 5 V, $R_{L}$ = 100 $\Omega$	t <sub>off</sub>		3		μs

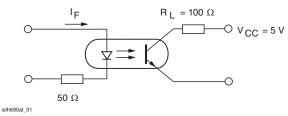


Fig. 2 - Switching Operation (without Saturation)



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SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				55/100/21				
Comparative tracking index		CTI	175		399			
VIOTM			6000			V		
V <sub>IORM</sub>			707			V		
P <sub>SO</sub>					350	mW		
I <sub>SI</sub>					150	mA		
T <sub>SI</sub>					175	°C		
Creepage distance			5			mm		
Clearance distance			5			mm		
Insulation thickness			0.4			mm		

Note

• As per IEC 60747-5-2, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

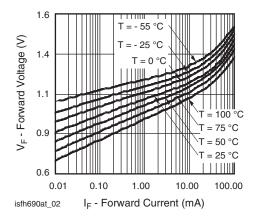
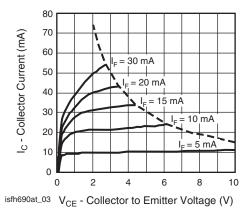
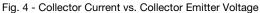


Fig. 3 - Diode Forward Voltage vs. Forward Current





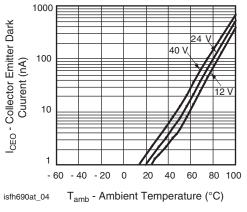


Fig. 5 - Collector to Emitter Dark Current vs. Ambient Temperature

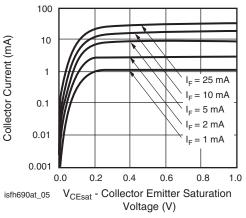


Fig. 6 - Collector Current vs. Collector Emitter Saturation Voltage

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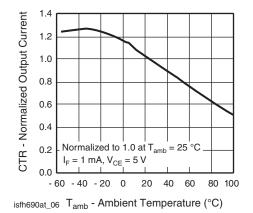


Fig. 7 - Normalized Output Current vs. Ambient Temperature

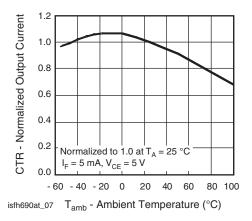


Fig. 8 - Normalized Output Current vs. Ambient Temperature

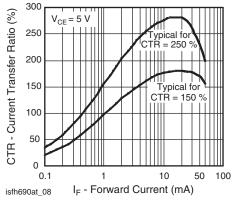


Fig. 9 - Current Transfer Ratio vs. Forward Current

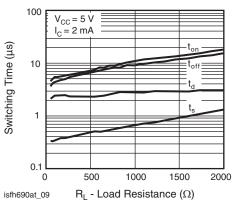
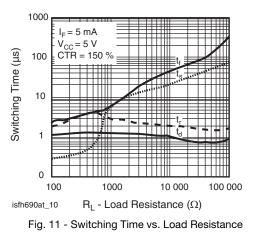


Fig. 10 - Switching Time vs. Load Resistance



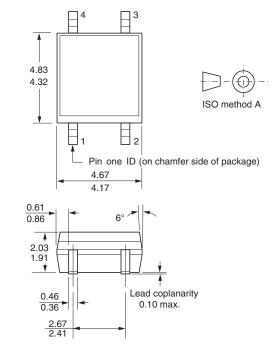
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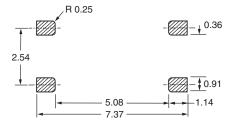
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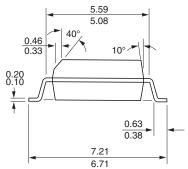
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### **PACKAGE DIMENSIONS** in millimeters

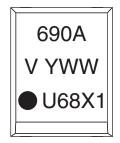






i178037

### PACKAGE MARKING (example of SFH690AT)



#### Notes

- Only option 1 is reflected in the package marking with the characters "X1" .
- Tape and reel suffix (T) is not part of the package marking •

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