



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL31A106KAHNNNF

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 10 µF, 25V, ±10%, X5R, 1206

A. Samsung Part Number

<u>CL</u> <u>31</u> <u>A</u> <u>106</u> <u>K</u> <u>A</u> <u>H</u> <u>N</u> <u>N</u> <u>N</u> <u>F</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-laye	Samsung Multi-layer Ceramic Capacitor				
② Size	1206 (inch code)	L: 3.2 ± 0.2	mm	W: 1.6 ± 0.2	mm	
③ Dielecti	ric X5R	8 Inner	electrode	Ni		
4 Capacit	ance 10 μF	Term	ination	Cu		
⑤ Capacit	ance ±10 %	Platir	ng	Sn 100%	(Pb Free)	
toleran	ce	9 Prod	uct	Normal		
6 Rated V	oltage 25 V	® Spec	ial	Reserved fo	r future use	
7 Thickne	ess 1.6 ± 0.2 mm	① Pack	aging	Embossed T	ype, 13" reel	

B. Samsung Reliablility Test and Judgement condition

	Performance	Test condition		
Capacitance Within specified tolerance		1kHz±10% 1.0±0.2Vrms		
Tan δ (DF)	0.1 max.			
Insulation 10,000Mohm or 500Mohm⋅ <i>µ</i> F		Rated Voltage 60~120 sec.		
Resistance	Whichever is Smaller			
Appearance	No abnormal exterior appearance	Microscope (×10)		
Withstanding	No dielectric breakdown or	250% of the rated voltage		
Voltage	mechanical breakdown			
Temperature	X5R			
Characterisitcs	(From -55 ℃ to 85 ℃, Capacitance change shoud be within ±15%)			
Adhesive Strength	No peeling shall be occur on the	500g·F, for 10±1 sec.		
of Termination	terminal electrode			
Bending Strength	Capacitance change: within ±12.5%	Bending to the limit (1mm)		
		with 1.0mm/sec.		
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder		
	is to be soldered newly	245±5℃, 3±0.3sec.		
		(preheating : 80~120℃ for 10~30sec.)		
Resistance to	Capacitance change: within ±7.5%	Solder pot : 270±5℃, 10±1sec.		
Soldering heat	Tan δ, IR : initial spec.			

	Performance	Test condition	
Vibration Test	Capacitance change: within ±5%	Amplitude : 1.5mm	
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)	
		2hours × 3 direction (x, y, z)	
Moisture	Capacitance change: within ±12.5%	With rated voltage	
Resistance	Tan δ: 0.125 max	40±2℃, 90~95%RH, 500+12/-0hrs	
	IR: 12.5MΩ·μF or Over		
High Temperature	Capacitance change : within ±12.5%	With 150% of the rated voltage	
Resistance	Tan δ: 0.125 max	Max. operating temperature	
	IR: 25MΩ·μF or Over		
		1000+48/-0hrs	
Temperature	Capacitance change: within ±7.5%	1 cycle condition	
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25 °C	
		→ Max. operating temperature → 25°C	
		5 cycle test	

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}$ C , 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.