

# NEC

## INVERTER

### 55PW131

## DATA SHEET

(1st edition)

**All information is subject to change without notice.**

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## INTRODUCTION

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NEC products are classified into the following three quality grades:

***"Standard", "Special", "Specific"***

The ***"Specific"*** quality grade applies only to applications developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a product depend on its quality grade, as indicated below. Customers must check the quality grade of each application before using it in a particular application.

***Standard:*** Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

***Special:*** Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

***Specific:*** Military systems, aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems (medical equipment, etc.) and any other equipment

The quality grade of this product is ***"Standard"*** unless otherwise specified in this document. If customers intend to use this product for applications other than those specified for ***"Standard"*** quality grade, they should contact NEC Corporation sales representative in advance.

Anti-radioactive design is not implemented in this product.

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**1. OUTLINE**

1.1 STRUCTURE AND PRINCIPLE

55PW131 inverter for LCD (Liquid crystal display) modules is composed of a DC/AC inversion circuit, a luminance control circuit and a boosting transformer.

The DC/AC inversion circuit inverts a direct current (DC) power supply into an altar current (AC) by the center-tap transmitter circuit that used transistors.

The luminance control circuit can control the luminance of cold cathode lamps for LCD backlight unit.

The boosting transformer is translated the low AC voltage that obtained from a DC/AC inversion circuit to the high AC voltage. Also the high AC voltage is outputted from a secondary side of the boosting transformer.

1.2 APPLICATIONS

- High AC voltage generator of cold cathode fluorescent lamp for LCD

1.3 FEATURES

- Pulse width modulation circuit
- Alert circuit for malfunction

**2. GENERAL SPECIFICATIONS**

<i><b>Driving system</b></i>	Externally commutated system
<i><b>Luminance control system</b></i>	Pulse width modulation
<i><b>Input voltage for power supply</b></i>	12.0 V (typ.)
<i><b>Output voltage</b></i>	At steady state 340 Vrms (typ.)  At open (e.g. Start-working of lamp) 1,250 Vrms (typ.)
<i><b>Combined load</b></i>	Resistance 63 kΩ (typ.)  Stray capacity -
<i><b>Oscillation frequency</b></i>	43 kHz (typ.)
<i><b>Board size</b></i>	103.0 (W) × 34.0 (H) × 11.0 (D) mm (typ.)
<i><b>Weight</b></i>	20.0 g (typ.)
<i><b>Adaptable product</b></i>	LCD module NL3224BC35-20  Lamp holder unit 55LHS11

**3. DETAILED SPECIFICATIONS**

**3.1 MECHANICAL SPECIFICATIONS**

Parameter	Specification	Unit
Board size	103.0 ± 0.5 (W) × 34.0 ± 2.0 (H) × 11.0 ± 1.0 (D) Note1	mm
Weight	20.0 (typ.), 23.0 (max.)	g

Note1: See "6. OUTLINE DRAWINGS".

**3.2 ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Rating	Unit	Remarks
Combined load	Resistance	RL	70	kΩ	Ta = 25°C
	Stray capacity	CL	-	pF	
Input voltage	Power supply for inverter	VDDDB	0 to +14.0	V	
	BRTI signal	VBI	-1.0 to +14.0	V	
	BRTC signal	VBC	0 to +5.0	V	
Storage temperature		Tst	-30 to +85	°C	
Operating temperature	Front surface	TopF	-10 to +70	°C	
	Rear surface	TopR	-10 to +70	°C	
Relative humidity Note1		RH	≤ 95	%	Ta ≤ 40°C
			≤ 85	%	40 < Ta ≤ 50°C
			≤ 70	%	50 < Ta ≤ 55°C
			≤ 60	%	55 < Ta ≤ 60°C
			≤ 50	%	60 < Ta ≤ 65°C
			≤ 42	%	65 < Ta ≤ 70°C
Absolute humidity Note1		-	≤ 78 Note2	g/m <sup>3</sup>	Ta > 70°C

Note1: No condensation

Note2: Ta = 70°C, RH = 42%

3.3 ELECTRICAL CHARACTERISTICS

3.3.1 Driving for inverter

(Ta = 25°C)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remarks	
Combined load	Resistance	RL	-	63	70	kΩ	-	
	Stray capacity	CL	-	-	-	pF		
Input voltage	Power supply for inverter	VDDB	10.8	12.0	13.2	V		
	BRTI signal	VBI	0	-	2.5	V		
	BRTC signal	Low	VBCL	0	-	0.4		V
High		VBCH	2.5	-	VDDB	V		at inverter power ON
Input current	Power supply for inverter	IDDB	-	400	600	mA	at maximum luminance, VDDB = 12.0V Note1	
Output voltage	Power supply for LCD lamp	VS	1,200	1,250	1,500	Vrms	Starting voltage for lamp, RL = ∞, CL = ∞	
	AM signal	Low	VBA	0	-	0.5	V	at normal
		High	VBA	4.5	5.0	5.5	V	at malfunction
Output current	LCD lamp	IBL	4.5	5.0	5.5	mArms	-	
Oscillation frequency		FO	39	43	47	kHz		
Luminance control frequency for LCD lamp		FL	300	330	360	Hz		

Note1: The power supply lines (VDDB and GNDB) occurs large ripple voltage while luminance control of LCD lamps. There is the possibility that the ripple voltage produces acoustic noise and signal wave noise in audio circuit and so on. Put a capacitor (5,000 to 6,000μF) between the power source lines (VDDB and GNDB) to reduce the noise, if the noise occurred in the circuit.

3.3.2 Fuses

Fusing line	Fuse		Rating	Fusing current Note1
	Type	Supplier		
VDDB	CCP2E15HTE	KOA Corporation	0.6 A	1.5 A
			72 V	

Note1: The power supply capacity should be more than the fusing current. If the power supply capacity is less than the fusing current, the fuse may not blow for a short time, and then nasty smell, smoking and so on may occur.

3.4 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

3.4.1 Detail of interface pins

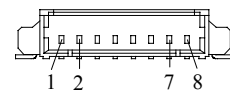
CN1 socket: 53261-0890 (MOLEX Inc.)

Adaptable plug: 51021-0800 (MOLEX Inc.)

Pin No.	Symbol	Function	Remarks
1	VDDB	Power supply	-
2	VDDB	Power supply	
3	GNDB	Ground	
4	GNDB	Ground	
5	BRTC	Inverter ON/OFF signal	ON: High, OFF: Low
6	BRTI	Input of luminance control by resistor / voltage control method	Note1
7	GNDB	Ground	-
8	AC	Alert for malfunction signal	5.0V output at malfunction

Note1: See "3.5 LUMINANCE CONTROLS".

CN1: Figure of socket

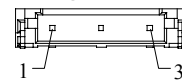


CN2 socket: SM02 (8.0) B-BHS-1-TB (J.S.T. Mfg Co., Ltd.)

Adaptable plug: BHR-03VS-1 (J.S.T. Mfg Co., Ltd.)

Pin No.	Symbol	Signal	Remarks
1	VBLH	High voltage (Hot)	-
2	NC	Non connection	
3	VBLC	Low voltage (Cold)	

CN2: Figure of socket

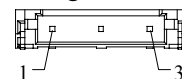


CN3 socket: SM02 (8.0) B-BHS-1-TB (J.S.T. Mfg Co., Ltd.)

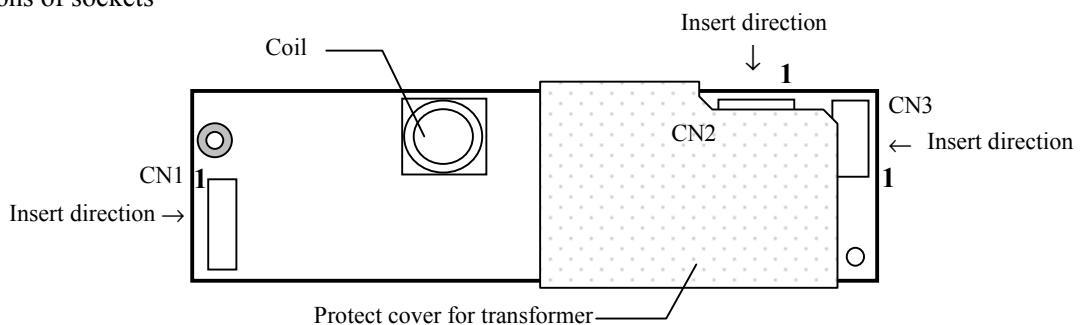
Adaptable plug: BHR-03VS-1 (J.S.T. Mfg Co., Ltd.)

Pin No.	Symbol	Signal	Remarks
1	VBLH	High voltage (Hot)	-
2	NC	Non connection	
3	VBLC	Low voltage (Cold)	

CN3: Figure of socket




3.4.2 Positions of sockets



3.5 LUMINANCE CONTROLS

Luminance control functions are used when control the luminance of LCD lamps.

Method	Adjustment and luminance ratio						
Resistor control	<ul style="list-style-type: none"> <li>• Adjustment The variable resistor (<b>R</b>) for luminance control should be 50kΩ ±5%, B curve, 1/10W. Minimum point of the resistor is the maximum luminance. Also maximum point of the resistor is the minimum luminance.</li> </ul>  <ul style="list-style-type: none"> <li>• Luminance ratio Note1</li> </ul> <table border="1" data-bbox="539 775 1391 878"> <thead> <tr> <th>Resistance</th> <th>Luminance ratio</th> </tr> </thead> <tbody> <tr> <td>0kΩ</td> <td>100% (Maximum)</td> </tr> <tr> <td>50kΩ</td> <td>10% (Minimum)</td> </tr> </tbody> </table>	Resistance	Luminance ratio	0kΩ	100% (Maximum)	50kΩ	10% (Minimum)
Resistance	Luminance ratio						
0kΩ	100% (Maximum)						
50kΩ	10% (Minimum)						
Voltage control	<ul style="list-style-type: none"> <li>• Adjustment This control method can carry out continuation adjustment of luminance, if it is adjusted within the rated voltage for BRTI signal (VBI).</li> </ul> <ul style="list-style-type: none"> <li>• Luminance ratio Note1</li> </ul> <table border="1" data-bbox="539 1059 1391 1164"> <thead> <tr> <th>BRTI voltage (VBI)</th> <th>Luminance ratio</th> </tr> </thead> <tbody> <tr> <td>0V</td> <td>100% (Maximum)</td> </tr> <tr> <td>2.5V</td> <td>10% (Minimum)</td> </tr> </tbody> </table>	BRTI voltage (VBI)	Luminance ratio	0V	100% (Maximum)	2.5V	10% (Minimum)
BRTI voltage (VBI)	Luminance ratio						
0V	100% (Maximum)						
2.5V	10% (Minimum)						

Note1: These data are the target values.

4. RELIABILITY TESTS


Test item	Condition	Judgment
High temperature and humidity (Non operation)	① 70 ± 2°C, RH = 90% ② 500hours	No physical damage No electrical damage
High temperature (Operation)	① 70 ± 3°C ② 500h	
High temperature (Non operation)	① 80 ± 3°C ② 240h	
Low temperature (Operation)	① -10 ± 3°C ② 240hours	
Low temperature (Non operation)	① -30 ± 3°C ② 240hours	
Thermal shock (Operation)	① -30 ± 3°C...30minutes 80 ± 3°C...30minutes ② 100cycles, 1hour/cycle	
Vibration (Non operation)	① 5 to 200Hz, 29.4 m/s <sup>2</sup> ② 10 minute/cycle ③ X, Y direction...2 hours each directions ④ Z direction...4 hours	
Mechanical shock (Non operation)	① 980m/ s <sup>2</sup> , 11ms ② ±X, ±Y, ±Z direction ③ 5 times each directions	




**5. PRECAUTIONS**


5.1 MEANING OF CAUTION SIGNS


The following caution signs have very important meaning. **Be sure to read "5.2 CAUTIONS", after understanding this contents!**

	This sign has the meaning that customer will get an electrical shock, if customer has wrong operations.
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	This sign has the meaning that customer will be injured by himself, if customer has wrong operations.
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5.2 CAUTIONS

	<b>Do not touch HIGH VOLTAGE PART of the inverter while turned on! Danger of an electrical shock.</b>
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	<p><b>* Pay attention to burn injury for the working inverter! It may be over 25°C from ambient temperature.</b></p> <p><b>* Do not shock the inverter! Danger of breaking, because they are composed of sensitive parts. (Shock: To be not greater 980m/s<sup>2</sup> and to be not greater 11ms)</b></p>
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5.3 ATTENTIONS

5.3.1 Handling of the product

- ① Take hold of both ends without touch the mounting parts when customer pulls out products from inner packing box. If customer touches it, products may be broken down or out of adjustment, because of stress to mounting parts.
- ② If customer puts down the product temporarily, the product puts on flat subsoil as a non-mounting parts side turns down.
- ③ Take the measures of electrostatic discharge such as earth band, ionic shower and so on, when customer deals with the product, because products may be damaged by electrostatic.
- ④ Do not push-pull the interface connectors while the product is working, because wrong power sequence may break down the product.

5.3.2 Environment

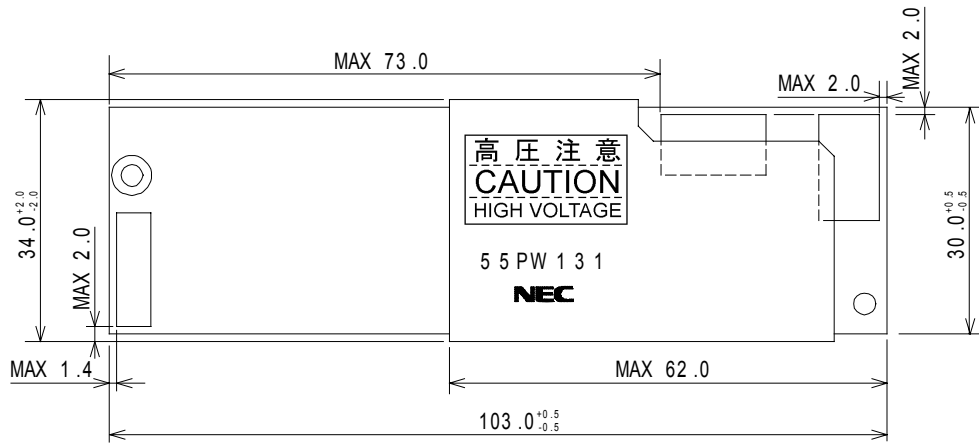
- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in antistatic pouch in room temperature, because of avoidance for dusts and sunlight, if customer stores the product.
- ② Do not operate in high magnetic field. Circuit boards may be broken down by it.

### 5.3.3 Other

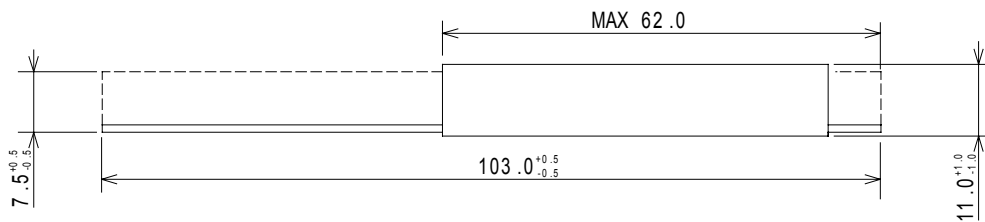
- ① All GNDB and VDDB terminals should be used without a non-connected line.
- ② Do not disassemble a product or adjust volume without permission of NEC Corporation.
- ③ Pay attention not to insert waste materials inside of products, if customer uses screw nails.
- ④ Put the spacer of 1.0mm thickness or more on a product rear side, because of the protection for contortion.

6. OUTLINE DRAWINGS

6.1 FRONT VIEW



6.2 SIDE VIEW



6.3 REAR VIEW

