DS96172,DS96174

DS96172/DS96174 RS-485/RS-422 Quad Differential Line Drivers

TEXAS INSTRUMENTS

Literature Number: SNLS390C

National Semiconductor

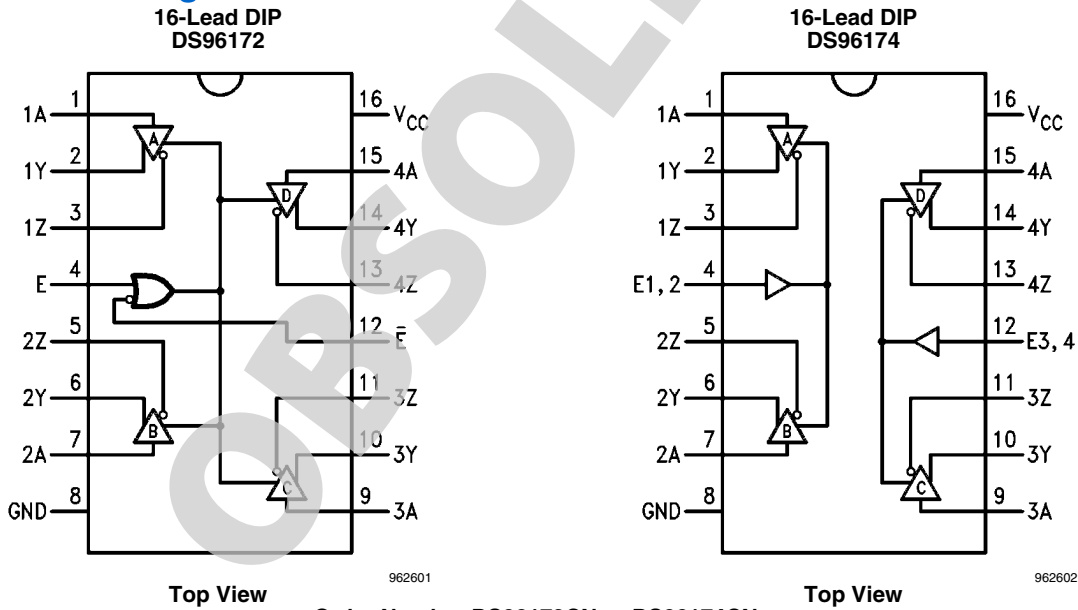## DS96172/DS96174

RS-485/RS-422 Quad Differential Line Drivers

General Description

The DS96172 and DS96174 are high speed quad differential line drivers designed to meet EIA Standard RS-485. The devices have TRI-STATE® outputs and are optimized for balanced multipoint data bus transmission at rates up to 10 Mbps. The drivers have wide positive and negative common mode range for multipoint applications in noisy environments. Positive and negative current-limiting is provided which protects the drivers from line fault conditions over a +12V to −7.0V common mode range. A thermal shutdown feature is also provided and occurs at junction temperature of approximately 160°C. The DS96172 features an active high and active low Enable, common to all four drivers. The DS96174 features separate active high Enables for each driver pair. Compatible RS-485 receivers, transceivers, and repeaters are also offered to provide optimum bus performance. The respective device types are DS96173, DS96175, DS96176 AND DS96177.

Features

■ Meets EIA Standard RS-485 and RS-422A
■ Monotonic differential output switching
■ Transmission rate to 10 Mbs
■ TRI-STATE outputs
■ Designed for multipoint bus transmission
■ Common mode output voltage range: −7V to +12V
■ Operates from single +5V supply
■ Thermal shutdown protection
■ DS96172/DS96174 are lead and function compatible with the SN75172/75174 or the AM26LS31/MC3487 respectively

Connection Diagrams

16-Lead DIP
DS96172

962601

Top View

16-Lead DIP
DS96174

962602

Top View

Order Number DS96172CN or DS96174CN
See NS Package Number N16E

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

Absolute Maximum Ratings (*Note 2*)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Range	
Molded DIP	−65°C to +150°C
Operating Temperature Range	0°C to +70°C
Lead Temperature	
Molded DIP (soldering, 10 sec.)	265°C
Supply Voltage	7V
Enable Input Voltage	5.5V

Maximum Power Dissipation		
(*Note 1*)	25°C	
N-Molded Package	1.98W	

Recommended Operating Conditions

	Min	Typ	Max	Units
Supply Voltage (V_{CC})	4.75	5	5.25	V
Common Mode Output Voltage (V_{OC})	−7		+12	V
Output Current HIGH (I_{OH})			−60	mA
Output Current LOW (I_{OL})			60	mA
Operating Temperature (T_A)	0	25	70	°C

Note 1: Derate molded DIP package 16mW/°C above 25°C.

Electrical Characteristics (*Note 3*, *Note 4*)

over recommended temperature and supply voltage ranges, unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
V_{IH}	Input Voltage HIGH			2			V
V_{IL}	Input Voltage LOW					0.8	V
V_{OH}	Output Voltage HIGH	I_{OH} = −20 mA			3.1		V
V_{OL}	Output Voltage LOW	I_{OL} = 20 mA			0.8		V
V_{IC}	Input Clamp Voltage	I_I = −18 mA				−1.5	V
$|V_{OD1}|$	Differential Output Voltage	I_O = 0 mA				6	V
$|V_{OD2}|$	Differential Output Voltage	R_L = 54Ω, *Figure 1*		1.5	2		V
		R_L = 100Ω, *Figure 1*		2	2.3		V
$\Delta|V_{OD}|$	Change in Magnitude of Differential Output Voltage (*Note 5*)	R_L = 54Ω or 100Ω, *Figure 1*				±0.2	V
V_{OC}	Common Mode Output Voltage (*Note 6*)	R_L = 54Ω, *Figure 1*				3	V
$\Delta|V_{OC}|$	Change in Magnitude of Common Mode Output Voltage (*Note 5*)					±0.2	V
I_O	Output Current with Power Off	V_{CC} = 0V, V_O = −7.0V to 12V				±100	µA
I_{OZ}	High Impedance State Output Current	V_O = −7.0V to 12V			±50	±200	µA
I_{IH}	Input Current HIGH	V_I = 2.7V				20	µA
I_{IL}	Input Current LOW	V_I = 0.5V				−100	µA
I_{OS}	Short Circuit Output Current (*Note 7*)	V_O = −7.0V				−250	mA
		V_O = 0V				−150	
		V_O = V_{CC}				150	
		V_O = 12V				250	
I_{CC}	Supply Current (All Drivers)	No Load	Outputs Enabled		50	70	mA
			Output Disabled		50	60	

Switching Characteristics

$V_{CC} = 5V$, $T_A = 25°C$

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{DD}	Differential Output Delay Time	$R_L = 60\Omega$, *Figure 2*		15	25	ns
t_{TD}	Differential Output Transition Time			15	25	ns
t_{PLH}	Propagation Delay Time, Low-to-High Level Output	$R_L = 27\Omega$, *Figure 3*		12	20	ns
t_{PHL}	Propagation Delay Time, High-to-Low Level Output			12	20	ns
t_{PZH}	Output Enable Time to High Level	$R_L = 110\Omega$, *Figure 4*		30	45	ns
t_{PZL}	Output Enable Time to Low Level	$R_L = 110\Omega$, *Figure 5*		30	45	ns
t_{PHZ}	Output Disable Time from High Level	$R_L = 110\Omega$, *Figure 4*		25	35	ns
t_{PLZ}	Output Disable Time from Low Level	$R_L = 110\Omega$, *Figure 5*		30	45	ns

Note 2: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 3: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS96172/DS96174. All typicals are given for $V_{CC} = 5V$ and $T_A = 25°C$.

Note 4: All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 5: $\Delta |V_{OD}|$ and $\Delta |V_{OC}|$ are the changes in magnitude of V_{OD} and V_{OC} respectively, that occur when the input is changed from a high level to a low level.

Note 6: In EIA Standards RS-422A and RS-485, V_{OC}, which is the average of the two output voltages with respect to ground, is called output offset voltage, V_{OS}.

Note 7: Only one output at a time should be shorted.

Parameter Measurement Information

962604
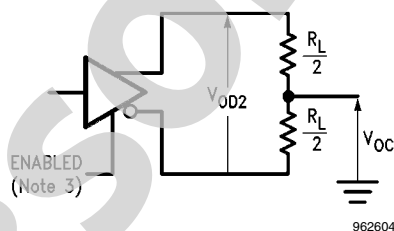
FIGURE 1. Differential and Common Mode Output Voltage

962605

962606
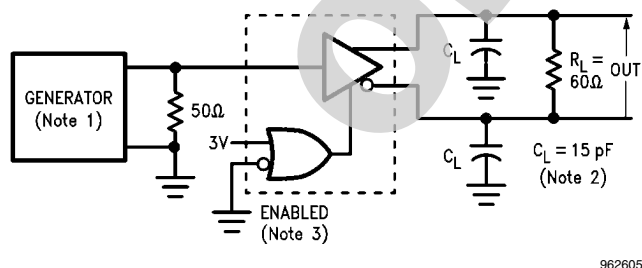
FIGURE 2. Differential Output Delay and Transition Times

www.national.com

2.3V

$R_L = 27\Omega$

S1

GENERATOR (Note 1)

50Ω

OUT

$C_L = 15\,pF$ (Note 2)

3V

ENABLED (Note 3)

962607

IN

3V

0V

1.5V   1.5V

t_{PLH} t_{PHL}

Y OUT

2.3V   2.3V

V_{OH}

V_{OL}

t_{PHL} t_{PLH}

Z OUT

2.3V   2.3V

V_{OH}

V_{OL}

962608
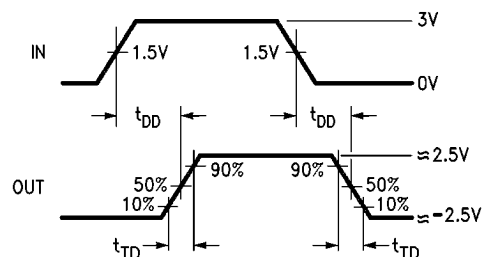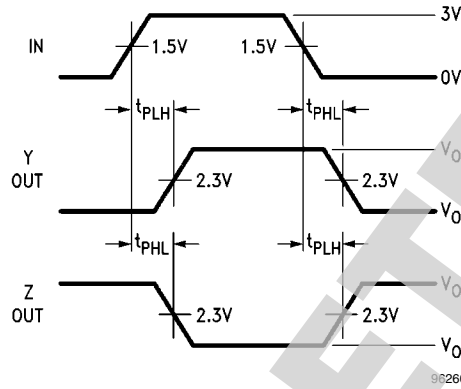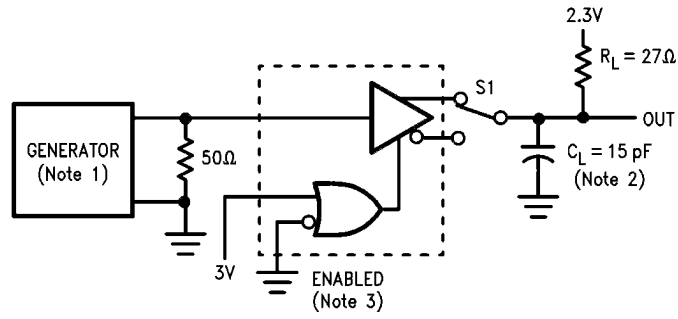
FIGURE 3. Propagation Delay Times

0V or 3V

S1

$C_L = 50\,pF$ (Note 2)

OUT

$R_L = 110\Omega$

GENERATOR (Note 1)

50Ω

3V (Note 4)

IN

3V

0V

1.5V   1.5V

t_{PZH} t_{PHZ}

OUT

2.3V

V_{OH}

0.5V

$V_{OFF} \cong 0V$

962610

962609

FIGURE 4. t_{PZH} and t_{PHZ}

5V

$R_L = 110\Omega$

0V or 3V

S1

OUT

$C_L = 50\,pF$ (Note 2)

GENERATOR (Note 1)

50Ω

3V (Note 4)

IN

3V

0V

1.5V   1.5V

t_{PZL} t_{PLZ}

OUT

2.3V

5V

V_{OL}

0.5V

962612

962611
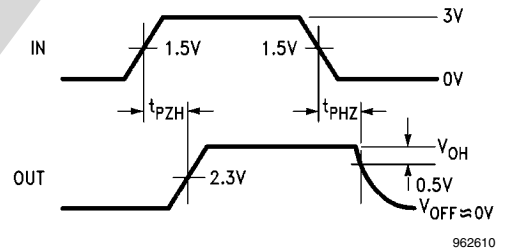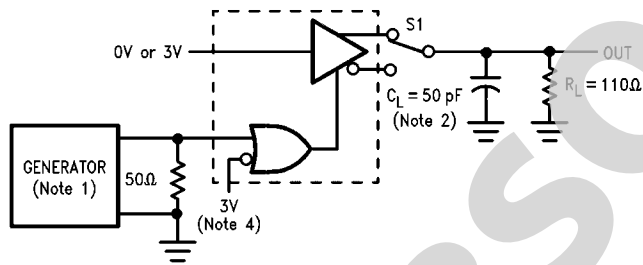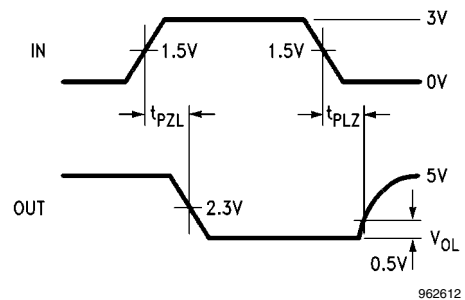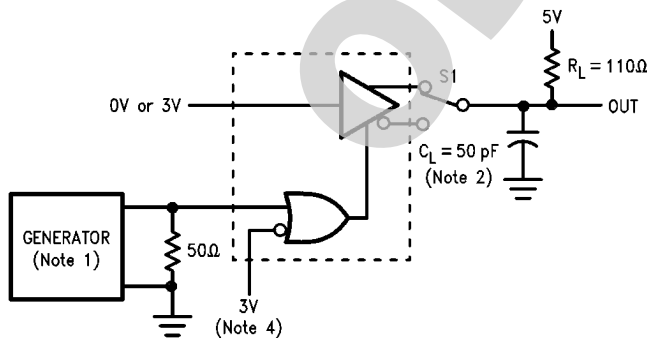
FIGURE 5. t_{PZL} and t_{PLZ}

Note 8: The input pulse is supplied by a generator having the following characteristics: PRR = 1.0 MHz, duty cycle = 50%, $t_r \leq 5.0$ ns, $t_f \leq 5.0$ ns, $Z_O = 50\Omega$.

Note 9: C_L includes probe and jig capacitance.

Note 10: DS96172 with active high and active low Enables is shown here. DS96174 has active high Enable only.

Note 11: To test the active low Enable $\overline{E}$ of DS96172, ground E and apply an inverted waveform to $\overline{E}$. DS96174 has active high Enable only.

Function Tables

DS96172

Input	Enables		Outputs	
A	E	Ē	Y	Z
H	H	X	H	L
L	H	X	L	H
H	X	L	H	L
L	X	L	L	H
X	L	H	Z	Z

DS96174

Input	Enable	Outputs	
		Y	Z
H	H	H	L
L	H	L	H
X	L	Z	Z

H = High Level
X = Immaterial
L = Low Level
Z = High Impedance (off)

Typical Application
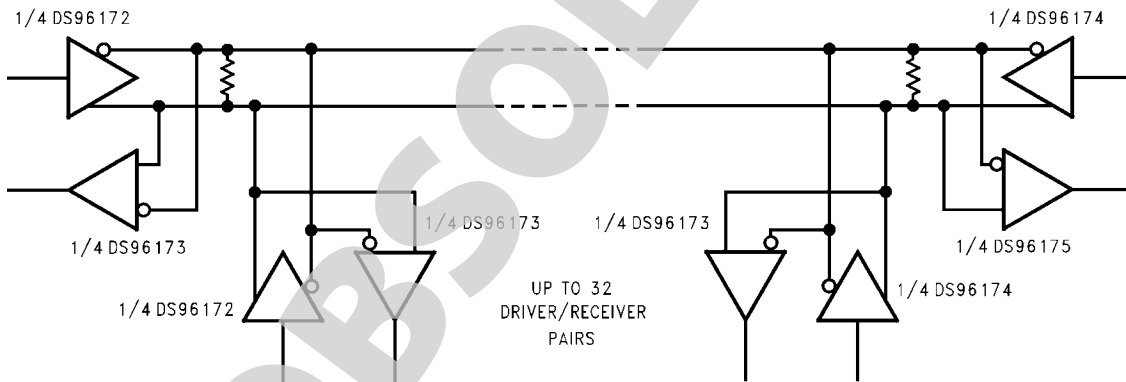
Note: The line length should be terminated at both ends in its characteristic impedance.
Stub lengths off the main line should be kept as short as possible.

FIGURE 6.

Physical Dimensions inches (millimeters) unless otherwise noted

OPTION 01

OPTION 02

0.740 − 0.780
(18.80 − 19.81)

0.090
(2.286)

PIN NO. 1
IDENT

0.250 ± 0.010
(6.350 ± 0.254)

INDEX
AREA

PIN NO. 1
IDENT

0.130 ± 0.005
(3.302 ± 0.127)

0.060
(1.524) TYP

4° TYP
OPTIONAL

0.300 − 0.320
(7.620 − 8.128)

0.065
(1.651)

0.145 − 0.200
(3.683 − 5.080)

95° ± 5°

0.008 − 0.016
(0.203 − 0.406) TYP

0.020
(0.508) MIN

90° ± 4° TYP

0.280
(7.112)
MIN

0.125 − 0.150
(3.175 − 3.810)

0.030 ± 0.015
(0.762 ± 0.381)

$(0.325 {+0.040 \atop -0.015})$

0.014 − 0.023
(0.356 − 0.584)
TYP

0.100 ± 0.010
(2.540 ± 0.254)
TYP

$(8.255 {+1.016 \atop -0.381})$

N16E (REV F)

0.050 ± 0.010
(1.270 ± 0.254)
TYP

Molded Dual-In-Line Package (N)
Order Number DS96172CN or DS96174CN
NS Package Number N16E

OBSOLETE

OBSOLETE

Notes

For more National Semiconductor product information and proven design tools, visit the following Web sites at:
www.national.com

Products		Design Support	
Amplifiers	www.national.com/amplifiers	WEBENCH® Tools	www.national.com/webench
Audio	www.national.com/audio	App Notes	www.national.com/appnotes
Clock and Timing	www.national.com/timing	Reference Designs	www.national.com/refdesigns
Data Converters	www.national.com/adc	Samples	www.national.com/samples
Interface	www.national.com/interface	Eval Boards	www.national.com/evalboards
LVDS	www.national.com/lvds	Packaging	www.national.com/packaging
Power Management	www.national.com/power	Green Compliance	www.national.com/quality/green
Switching Regulators	www.national.com/switchers	Distributors	www.national.com/contacts
LDOs	www.national.com/ldo	Quality and Reliability	www.national.com/quality
LED Lighting	www.national.com/led	Feedback/Support	www.national.com/feedback
Voltage References	www.national.com/vref	Design Made Easy	www.national.com/easy
PowerWise® Solutions	www.national.com/powerwise	Applications & Markets	www.national.com/solutions
Serial Digital Interface (SDI)	www.national.com/sdi	Mil/Aero	www.national.com/milaero
Temperature Sensors	www.national.com/tempsensors	SolarMagic™	www.national.com/solarmagic
PLL/VCO	www.national.com/wireless	PowerWise® Design University	www.national.com/training

IMPORTANT NOTICE