## DS96172,DS96174

DS96172/DS96174 RS-485/RS-422 Quad Differential Line Drivers



Literature Number: SNLS390C



#### DS96172/DS96174

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### RS-485/RS-422 Quad Differential Line Drivers

#### **General Description**

The DS96172 and DS96174 are high speed guad differential line drivers designed to meet EIA Standard RS-485. The devices have TRI-STATE® outputs and are optimized for balanced multipoint data bus transmission at rates up to 10 Mbps. The drivers have wide positive and negative common mode range for multipoint applications in noisy environments. Positive and negative current-limiting is provided which protects the drivers from line fault conditions over a +12V to -7.0V common mode range. A thermal shutdown feature is also provided and occurs at junction temperature of approximately 160°C. The DS96172 features an active high and active low Enable, common to all four drivers. The DS96174 features separate active high Enables for each driver pair. Compatible RS-485 receivers, transceivers, and repeaters are also offered to provide optimum bus performance. The respective device types are DS96173, DS96175. DS96176 AND DS96177.

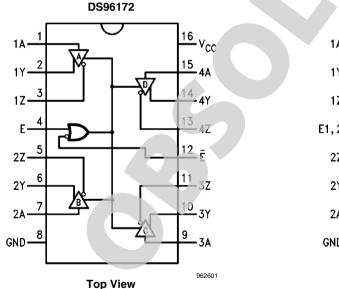
16-Lead DIP

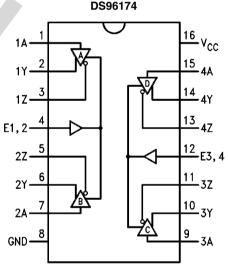
#### **Features**

- Meets EIA Standard RS-485 and RS-422A
- Monotonic differential output switching
- Transmission rate to 10 Mbs
- TRI-STATE outputs
- Designed for multipoint bus transmission
- Common mode output voltage range: -7V to +12V
- Operates from single +5V supply
- Thermal shutdown protection
- DS96172/DS96174 are lead and function compatible with the SN75172/75174 or the AM26LS31/MC3487 respectively

16-Lead DIP

## **Connection Diagrams**





**Top View** 

Order Number DS96172CN or DS96174CN See NS Package Number N16E

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#### **Absolute Maximum Ratings** (Note 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Range

Molded DIP -65°C to +150°C

Operating Temperature Range 0°C to +70°C

Lead Temperature

Molded DIP (soldering, 10 sec.) 265°C

Supply Voltage 7V

Enable Input Voltage

Maximum Power Dissipation
(Note 1) 25°C
N-Molded Package 1.98W

# **Recommended Operating Conditions**

	Min	Тур	Max	Units
Supply Voltage (V <sub>CC</sub> )	4.75	5	5.25	V
Common Mode Output				
Voltage (V <sub>OC</sub> )	<b>-</b> 7		+12	V
Output Current HIGH (I <sub>OH</sub> )			-60	mA
Output Current LOW (I <sub>OL</sub> )			60	mA
Operating Temperature $(T_A)$	0	25	70	°C

Note 1: Derate molded DIP package 16mW/°C above 25°C.

#### **Electrical Characteristics** (Note 3, Note 4)

over recommended temperature and supply voltage ranges, unless otherwise specified

5.5V

Symbol	Parameter		Conditions	Min	Тур	Max	Units
V <sub>IH</sub>	Input Voltage HIGH			2			V
V <sub>IL</sub>	Input Voltage LOW					0.8	V
V <sub>OH</sub>	Output Voltage HIGH	I <sub>OH</sub> = -20 m	A		3.1		V
V <sub>OL</sub>	Output Voltage LOW	I <sub>OL</sub> = 20 mA			0.8		V
V <sub>IC</sub>	Input Clamp Voltage	$I_1 = -18 \text{ mA}$				-1.5	V
IV <sub>OD1</sub> I	Differential Output Voltage	$I_0 = 0 \text{ mA}$				6	V
IV <sub>OD2</sub> I	Differential Output Voltage	$R_L = 54\Omega$ , F	igure 1	1.5	2		V
		$R_L = 100\Omega$ ,	Figure 1	2	2.3		V
$\Delta  V_{OD} $	Change in Magnitude of Differential	$R_L = 54\Omega$ or	100Ω, Figure 1			±0.2	V
	Output Voltage (Note 5)						
V <sub>oc</sub>	Common Mode Output Voltage (Note 6)	$R_L = 54\Omega$ , F	igure 1			3	V
ΔIV <sub>OC</sub> I	Change in Magnitude of Common Mode					±0.2	V
	Output Voltage (Note 5)						
I <sub>o</sub>	Output Current with Power Off	$V_{CC} = 0V, V$	<sub>O</sub> = -7.0V to 12V			±100	μΑ
l <sub>oz</sub>	High Impedance State Output Current	$V_0 = -7.0V$	to 12V		±50	±200	μΑ
I <sub>IH</sub>	Input Current HIGH	V <sub>I</sub> = 2.7V				20	μA
I <sub>IL</sub>	Input Current LOW	V <sub>I</sub> = 0.5V				-100	μA
I <sub>os</sub>	Short Circuit Output Current	$V_0 = -7.0V$				-250	
	(Note 7)					-150	mA
		$V_O = V_{CC}$				150	
		V <sub>O</sub> = 12V				250	
I <sub>cc</sub>	Supply Current (All Drivers)	No Load	Outputs Enabled		50	70	mA
			Output Disabled		50	60	

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#### **Switching Characteristics**

 $V_{CC} = 5V, T_A = 25^{\circ}C$ 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>DD</sub>	Differential Output Delay Time	$R_L = 60\Omega$ , Figure 2		15	25	ns
t <sub>TD</sub>	Differential Output Transition Time			15	25	ns
t <sub>PLH</sub>	Propagation Delay Time, Low-to-High Level Output	$R_L = 27\Omega$ , Figure 3		12	20	ns
t <sub>PHL</sub>	Propagation Delay Time, High-to-Low Level Output			12	20	ns
t <sub>PZH</sub>	Output Enable Time to High Level	$R_L = 110\Omega$ , Figure 4		30	45	ns
t <sub>PZL</sub>	Output Enable Time to Low Level	$R_L = 110\Omega$ , Figure 5		30	45	ns
t <sub>PHZ</sub>	Output Disable Time from High Level	$R_L = 110\Omega$ , Figure 4		25	35	ns
t <sub>PLZ</sub>	Output Disable Time from Low Level	$R_L = 110\Omega$ , Figure 5		30	45	ns

**Note 2:** "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 3: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS96172/DS96174. All typicals are given for  $V_{CC} = 5V$  and  $T_A = 25$ °C.

Note 4: All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 5: Δ |V<sub>OD</sub>| and Δ|V<sub>OC</sub>| are the changes in magnitude of V<sub>OD</sub> and V<sub>OC</sub> respectively, that occur when the input is changed from a high level to a low level.

Note 6: In EIA Standards RS-422A and RS-485, V<sub>OC</sub>, which is the average of the two output voltages with respect to ground, is called output offset voltage, V. . .

Note 7: Only one output at a time should be shorted.

#### **Parameter Measurement Information**

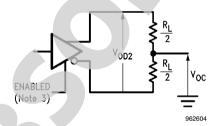


FIGURE 1. Differential and Common Mode Output Voltage

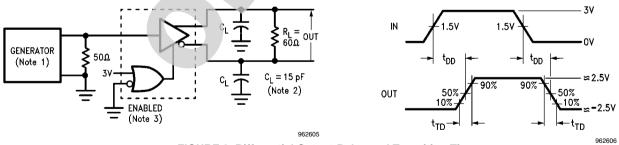


FIGURE 2. Differential Output Delay and Transition Times

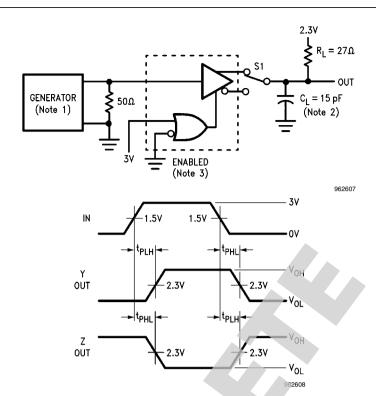


FIGURE 3. Propagation Delay Times

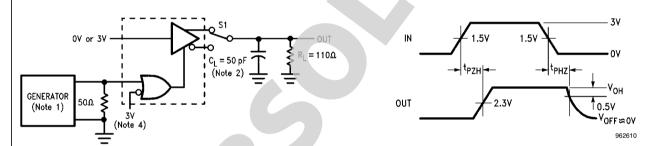


FIGURE 4. t<sub>PZH</sub> and t<sub>PHZ</sub>

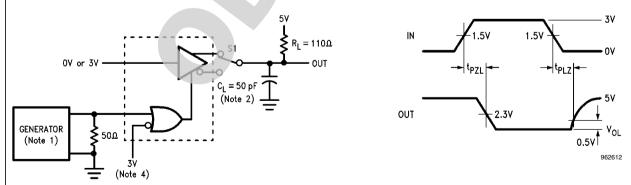


FIGURE 5.  $t_{PZL}$  and  $t_{PLZ}$ 

Note 8: The input pulse is supplied by a generator having the following characteristics: PRR = 1.0 MHz, duty cycle = 50%,  $t_r \le 5.0$  ns,  $t_i \le 5.0$  ns,  $t_$ 

Note 10: DS96172 with active high and active low Enables is shown here. DS96174 has active high Enable only.

Note 11: To test the active low Enable  $\overline{E}$  of DS96172, ground E and apply an inverted waveform to  $\overline{E}$ . DS96174 has active high Enable only.

#### **Function Tables**

DS96172

Input	Enables		Outputs	
Α	E	Ē	Υ	Z
Н	Н	X	Н	L
L	Н	Χ	L	Н
Н	Х	L	Н	L
L	Х	L	L	Н
X	L	Н	Z	Z

DS96174

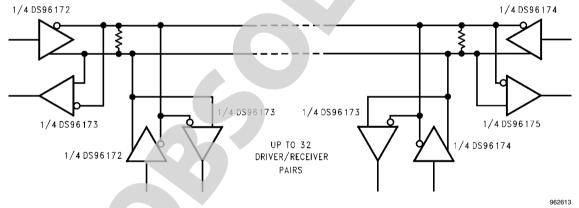
Input	Enable	Outputs		
		Y Z		
Н	Н	H L		
L	Н	L H		
X	L	Z Z		

H = High Level X = Immaterial

L = Low Level

Z = High Impedance (off)

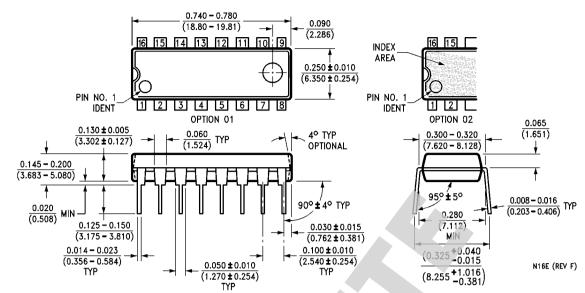
## **Typical Application**



Note: The line length should be terminated at both ends in its characteristic impedance. Stub lengths off the main line should be kept as short as possible.

FIGURE 6.

## Physical Dimensions inches (millimeters) unless otherwise noted



Molded Dual-In-Line Package (N)
Order Number DS96172CN or DS96174CN
NS Package Number N16E





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#### **Notes**

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