

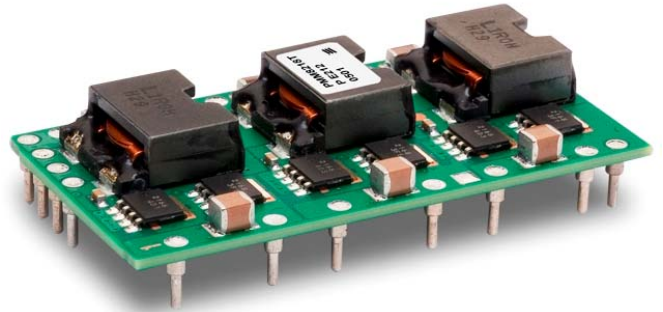
PMM 8000 series
POL regulator, Input 8-14 V, Output 50 A/275 W

EN/LZT 146 339 R1A April 2006

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Key Features

- 50A output current
- 8-14V input voltage range
- Output voltages from 0.8V up to 5.5V
- Industry standard POLA™ compatible
- 52 x 26.7x 9.07 mm (2.05 x 1.05 x 0.357 in.)
- High efficiency, up to .96%
- Auto Track™ sequencing pin
- Differential output voltage sense
- More than 1.27 million hours MTBF



General Characteristics

- Operating temperature: -40°C to 85 °C
- Start up into a pre-biased output safe
- Output short-circuit protection
- Over temperature protection
- On/Off inhibit control
- Margin up/down control
- Highly automated manufacturing ensures quality
- ISO 9001/14001 certified supplier

Safety Approvals



Design for Environment



Meets requirements in high-temperature lead-free soldering processes.

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General Information

Ordering Information

See Contents for individual product ordering numbers.

Option	Suffix	Ordering No.
Through hole pin	P	PMM 8218T P
SMD pin	S	PMM 8218T S
SMD pin, leadfree reflow temperature capable	R	PMM 8218T SR

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses Telcordia SR332.

Predicted MTBF for the series is:

- 1.27 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in the products:

- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)
- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication
(Note: the products are manufactured in lead-free soldering processes and the lead present in the solder is only located in the terminal plating finishes on some components)

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6 σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

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Safety Specification

General information

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without “Conditions of Acceptability”. It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 “*Safety of information technology equipment*”.

There are other more product related standards, e.g. IEEE802.3af “Ethernet LAN/MAN Data terminal equipment power”, and ETS300132-2 “Power supply interface at the input to telecommunications equipment; part 2: DC”, but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.

In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).

Leakage current is less than 1 μ A at nominal input voltage.

24 V DC systems

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems

If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

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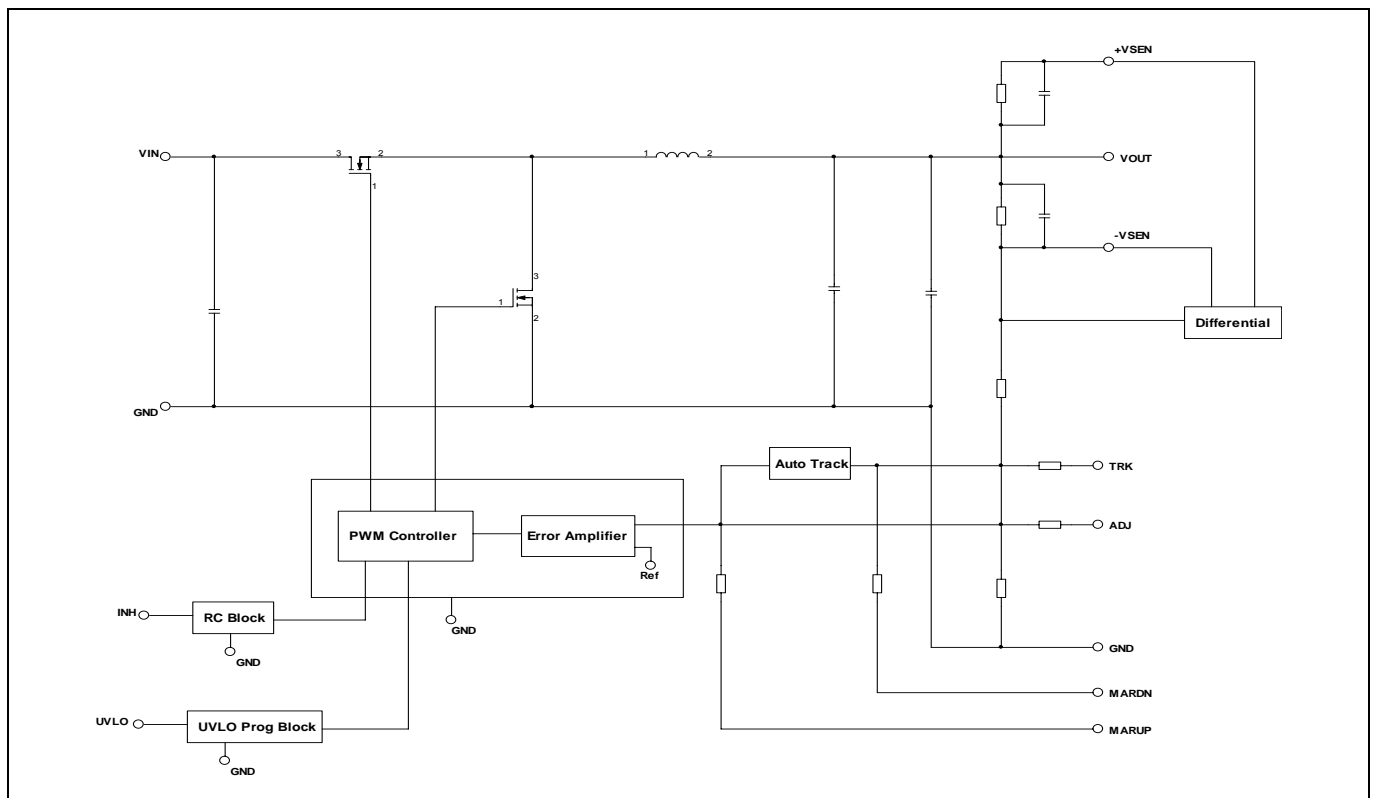
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Absolute Maximum Ratings

Characteristics		min	typ	max	Unit
T_{amb}	Operating Temperature (see Thermal Consideration section)	-40		85	°C
T_s	Storage temperature	-40		125	°C
V_I	Input voltage	8.0	12.0	14.0	V
V_{inh}	Inhibit On/Off pin voltage (see Operating Information section)	Positive logic option		Open	V
		Negative logic option	N/A	N/A	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



PMM 8000 series
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1.0 V/50 A Electrical Specification
PMM 8218T
 $T_{ref} = -40$ to $+85^{\circ}\text{C}$, $V_I = 8.0$ to 14.0 V, $R_{adj} = 38.3$ k Ω , unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O , unless otherwise specified under Conditions.

 Additional $C_{in} = 1000\mu\text{F}$ and $C_{out} = 660\mu\text{F}$. See Operating Information section for selection of capacitor types.

Connect the sense pin, where available, to the output pin.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		8.0		14.0	V
UVLO	Undervoltage lockout	Pin 8 open	On-threshold			V
			Hysteresis			
C_I	Internal input capacitance			80		μF
P_O	Output power		0		50	W
η	Efficiency	50 % of max I_O		85.9		%
		max I_O		82.5		
P_d	Power Dissipation	max I_O		10.6		W
P_{ii}	Input idling power	$I_O = 0$, $V_I = 12.0$ V		1360		mW
P_{inh}	Input standby power	$V_I = 12.0$ V (turned off with INHIBIT)		420		mW
I_S	Static Input current	$V_I = 12.0$ V, max I_O		5.1		A
f_s	Switching frequency	0-100 % of max I_O	900	1050	1200	kHz

V_{O_i}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O	0.980	1.000	1.020	V
V_O	Output voltage tolerance band	10-100 % of max I_O	0.970		1.030	V
	Idling voltage	$I_O = 0$		0.998		V
	Line regulation	max I_O		± 5		mV
	Load regulation	$V_I = 12.0$ V, 0-100 % of max I_O		± 5		mV
V_{tr}	Load transient voltage deviation	$V_I = 12.0$ V, Load step 50-100-50 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		± 75		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10-90 % of V_O)	max I_O		2.2		ms
t_s	Start-up time (from V_I connection to 90% of V_O)				6.4	
t_f	Ramp-down time (from 90-10 % of V_O)	Max I_O		10		μs
		$I_O = 0.1$ A		25.1		ms
t_{inh}	INHIBIT start-up time	Max I_O		5.6		ms
	INHIBIT shutdown fall time (From INHIBIT off to 10% of V_O)	Max I_O		30		μs
		$I_O = 0.1$ A		3.8		ms
I_O	Output current		0		50	A
I_{lim}	Current limit threshold	$T_{ref} < \text{max } T_{ref}$		95		A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{O_i}		15		mVp-p

Note 1: Output filter according to Ripple & Noise section

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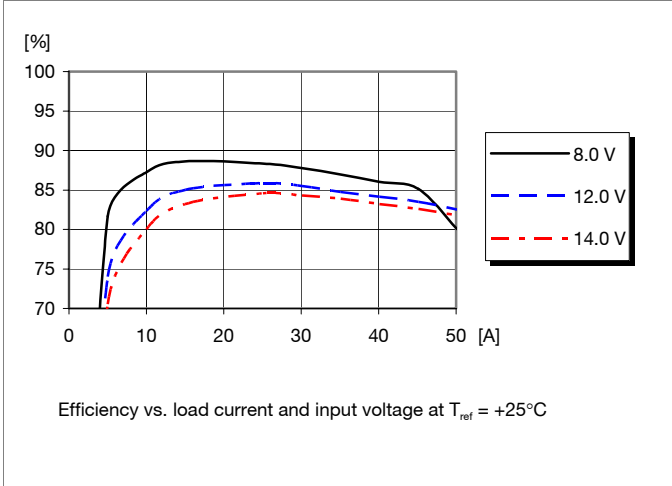
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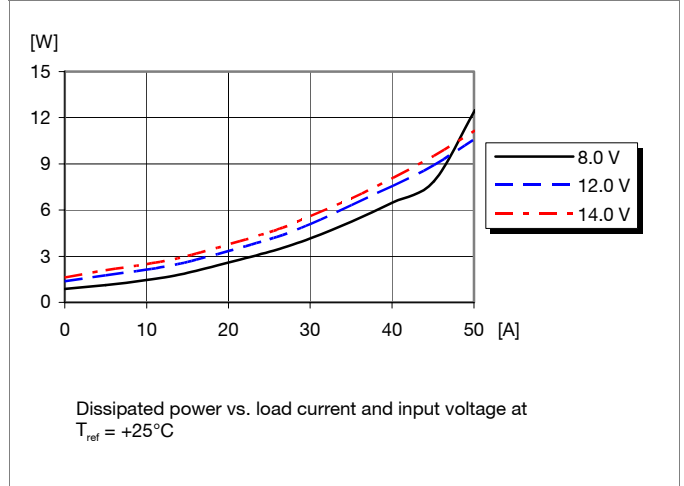
1.0 V/50 A Typical Characteristics

PMM 8218T

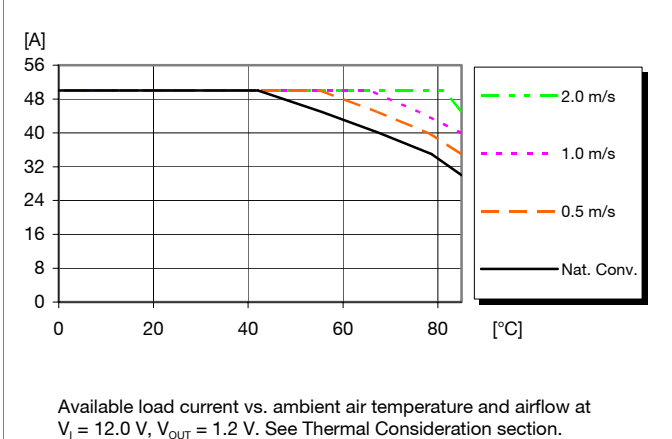
Efficiency



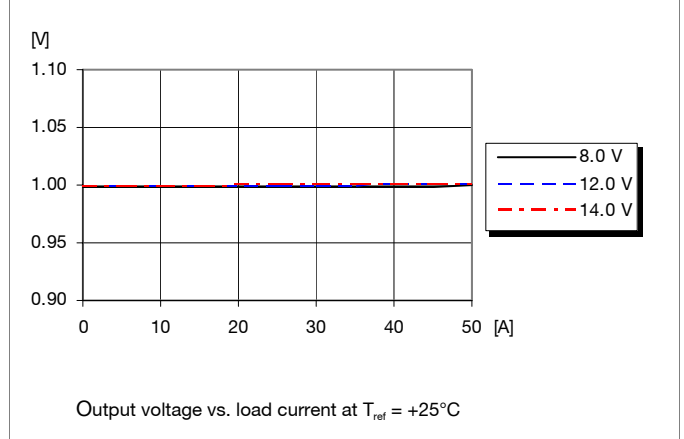
Power Dissipation



Output Current Derating



Output Characteristics



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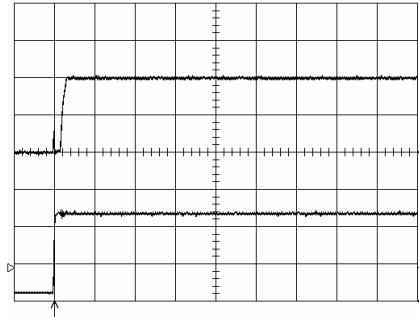
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1.0 V/50 A Typical Characteristics

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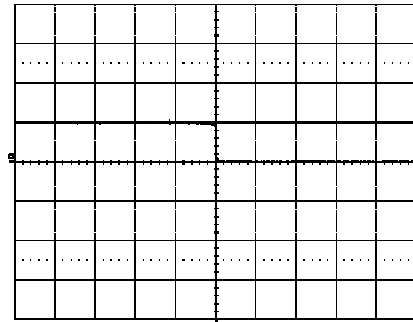
Start-up



Start-up enabled by connecting V_i at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$.

Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (5 V/div.).
Time scale: 20 ms/div.

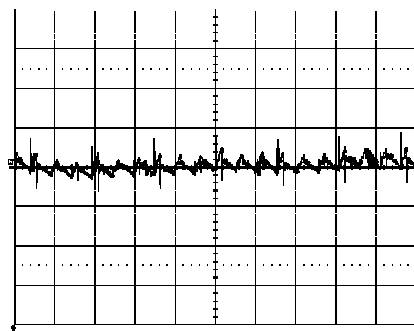
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$.

Top trace: output voltage (1.0 V/div.).
Time scale: 1.0 ms/div.

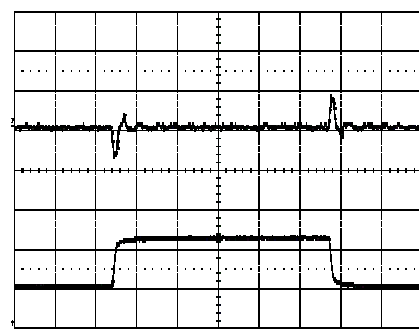
Output Ripple & Noise



Output voltage ripple (20 mV/div.) at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$. Time scale: 2 μs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (25-50-25 A) at:
 $T_{ref} = +25^\circ\text{C}$, $V_i = 12.0\text{ V}$.

Top trace: output voltage (100 mV/div.).
Bottom trace: load current (20 A/div.).
Time scale: 0.2 ms/div.

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1.2 V/50 A Electrical Specification
PMM 8218T

$T_{ref} = -40$ to $+85^{\circ}\text{C}$, $V_I = 8.0$ to 14.0 V, $R_{adj} = 18.2$ k Ω , unless otherwise specified under Conditions.

Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O , unless otherwise specified under Conditions.

Additional $C_{in} = 1000\mu\text{F}$ and $C_{out} = 660\mu\text{F}$. See Operating Information section for selection of capacitor types.

Connect the sense pin, where available, to the output pin.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		8.0		14.0	V
UVLO	Undervoltage lockout	Pin 8 open		On-threshold	7.5	V
				Hysteresis	1	
C_I	Internal input capacitance			80		μF
P_O	Output power		0		60	W
η	Efficiency	50 % of max I_O		87.9		%
		max I_O		84.9		
P_d	Power Dissipation	max I_O		10.7		W
P_{ii}	Input idling power	$I_O = 0$, $V_I = 12.0$ V		1400		mW
P_{inh}	Input standby power	$V_I = 12.0$ V (turned off with INHIBIT)		420		mW
I_S	Static Input current	$V_I = 12.0$ V, max I_O		5.9		A
f_s	Switching frequency	0-100 % of max I_O	900	1050	1200	kHz

V_{oi}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O	1.176	1.200	1.224	V
V_O	Output voltage tolerance band	10-100 % of max I_O	1.164		1.236	V
	Idling voltage	$I_O = 0$		1.200		V
	Line regulation	max I_O		± 5		mV
	Load regulation	$V_I = 12.0$ V, 0-100% of max I_O		± 5		mV
V_{tr}	Load transient voltage deviation	$V_I = 12.0$ V, Load step 50-100-50 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		± 80		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10-90 % of V_O)	max I_O		2.2		ms
t_s	Start-up time (from V_I connection to 90% of V_O)				6.3	
t_f	Ramp-down time (from 90-10 % of V_O)	Max I_O		60		μs
		$I_O = 0.1$ A		29.9		ms
t_{inh}	INHIBIT start-up time	Max I_O		5.6		ms
	INHIBIT shutdown fall time (From INHIBIT off to 10% of V_O)	Max I_O		30		μs
		$I_O = 0.1$ A		4.5		ms
I_O	Output current		0		50	A
I_{lim}	Current limit threshold	$T_{ref} < \max T_{ref}$		95		A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{oi}		15		mVp-p

Note 1: Output filter according to Ripple & Noise section

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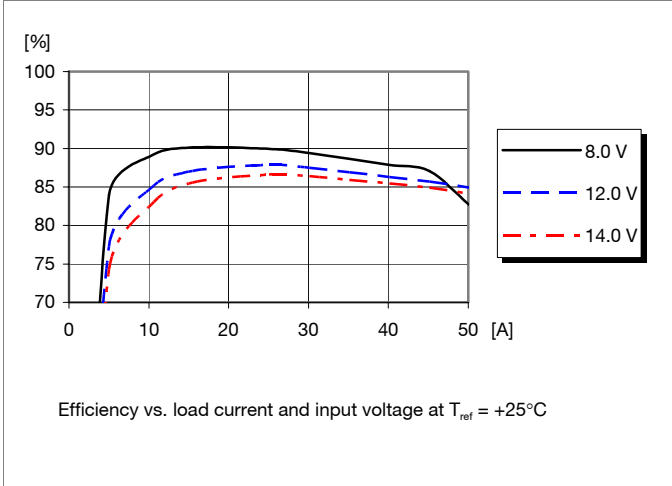
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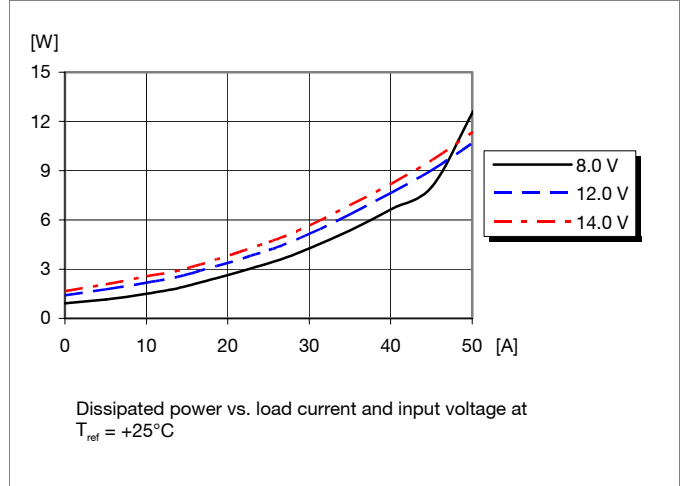
1.2 V/50 A Typical Characteristics

PMM 8218T

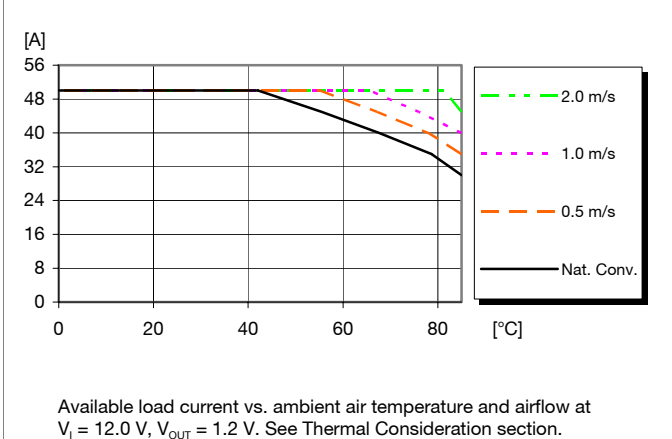
Efficiency



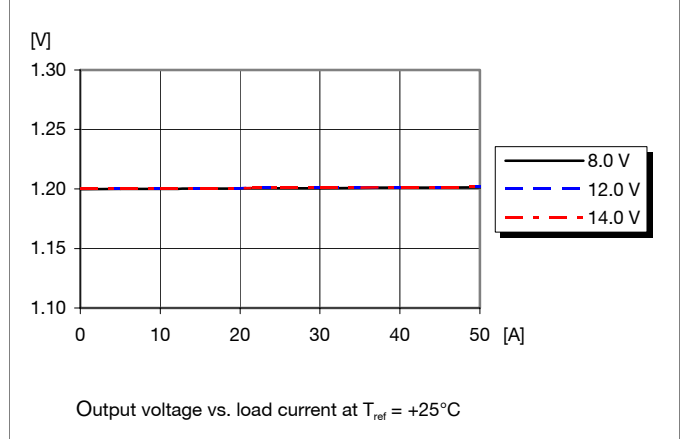
Power Dissipation



Output Current Derating



Output Characteristics



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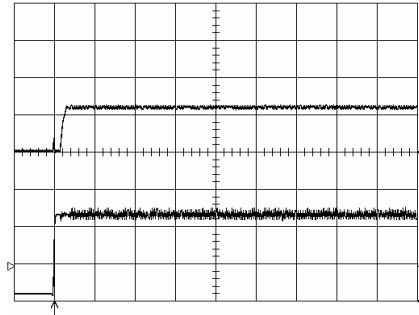
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1.2 V/50 A Typical Characteristics

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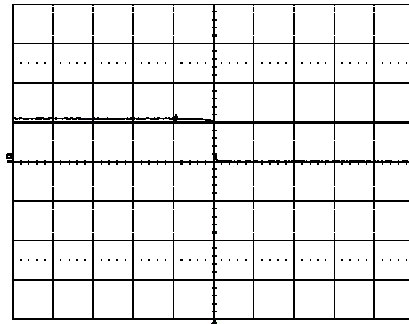
Start-up



Start-up enabled by connecting V_i at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$.

Top trace: output voltage (1.0 V/div.).
Bottom trace: input voltage (5 V/div.).
Time scale: 20 ms/div.

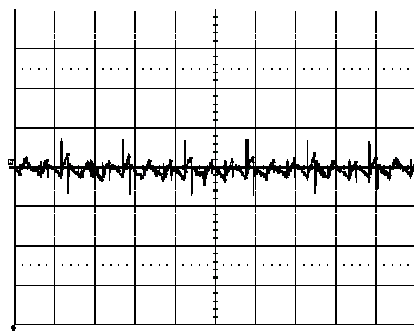
Shut-down



Shut-down enabled by disconnecting V_i at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$.

Top trace: output voltage (1.0 V/div.).
Time scale: 1.0 ms/div.

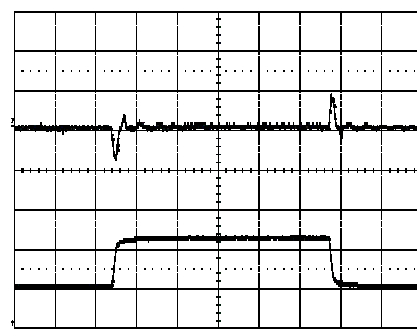
Output Ripple & Noise



Output voltage ripple (20 mV/div.) at:
 $T_{ref} = +25^\circ\text{C}$, $I_o = 50\text{ A}$ resistive load,
 $V_i = 12.0\text{ V}$. Time scale: 2 μs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (25-50-25 A) at:
 $T_{ref} = +25^\circ\text{C}$, $V_i = 12.0\text{ V}$.

Top trace: output voltage (100 mV/div.).
Bottom trace: load current (20 A/div.).
Time scale: 0.2 ms/div.

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1.5 V/50 A Electrical Specification
PMM 8218T
 $T_{ref} = -40$ to $+85^{\circ}\text{C}$, $V_I = 8.0$ to 14.0 V, $R_{adj} = 9.76$ k Ω , unless otherwise specified under Conditions.

 Typical values given at: $T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O , unless otherwise specified under Conditions.

 Additional $C_{in} = 1000\mu\text{F}$ and $C_{out} = 660\mu\text{F}$. See Operating Information section for selection of capacitor types.

Connect the sense pin, where available, to the output pin.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		8.0		14.0	V
UVLO	Undervoltage lockout	Pin 8 open	On-threshold		7.5	V
			Hysteresis		1	
C_I	Internal input capacitance			80		μF
P_O	Output power		0		75	W
η	Efficiency	50 % of max I_O		89.8		%
		max I_O		87.3		
P_d	Power Dissipation	max I_O		10.9		W
P_{ii}	Input idling power	$I_O = 0$, $V_I = 12.0$ V		1490		mW
P_{inh}	Input standby power	$V_I = 12.0$ V (turned off with INHIBIT)		420		mW
I_S	Static Input current	$V_I = 12.0$ V, max I_O		7.2		A
f_s	Switching frequency	0-100% of max I_O	900	1050	1200	kHz

V_{O_i}	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$, $V_I = 12.0$ V, max I_O	1.470	1.500	1.530	V
V_O	Output voltage tolerance band	10-100% of max I_O	1.455		1.545	V
	Idling voltage	$I_O = 0$		1.497		V
	Line regulation	max I_O		± 5		mV
	Load regulation	$V_I = 12.0$ V, 0-100% of max I_O		± 5		mV
V_{tr}	Load transient voltage deviation	$V_I = 12.0$ V, Load step 50-100-50 % of max I_O , $di/dt = 1$ A/ μs , see Note 1		± 80		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10–90 % of V_O)	max I_O		2.4		ms
t_s	Start-up time (from V_I connection to 90% of V_O)				6.3	
t_f	Ramp-down time (from 90–10 % of V_O)	Max I_O		100		μs
		$I_O = 0.1$ A		36.2		ms
t_{inh}	INHIBIT start-up time	Max I_O		5.6		ms
	INHIBIT shutdown fall time (From INHIBIT off to 10% of V_O)	Max I_O		70		μs
		$I_O = 0.1$ A		5.6		ms
I_O	Output current		0		50	A
I_{lim}	Current limit threshold	$T_{ref} < \text{max } T_{ref}$		95		A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O , V_{O_i}		15		mVp-p

Note 1: Output filter according to Ripple & Noise section

PMM 8000 series
POL regulator, Input 8-14 V, Output 50 A/275 W

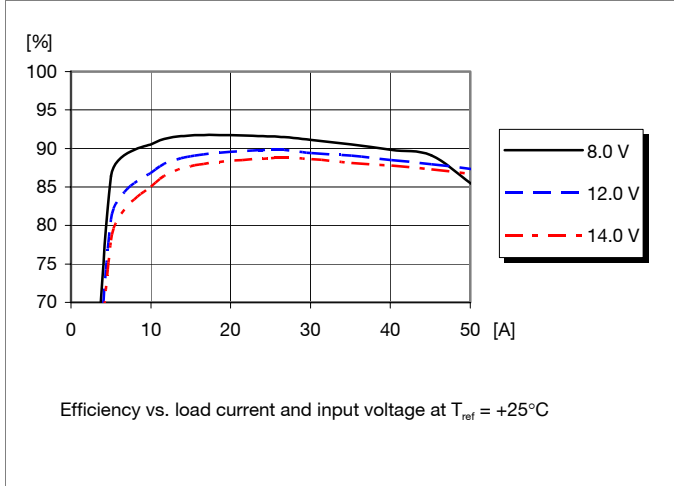
EN/LZT 146 339 R1A April 2006

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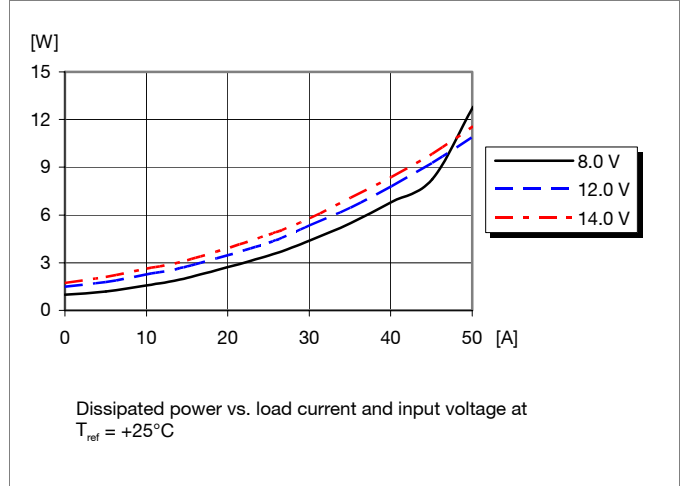
1.5 V/50 A Typical Characteristics

PMM 8218T

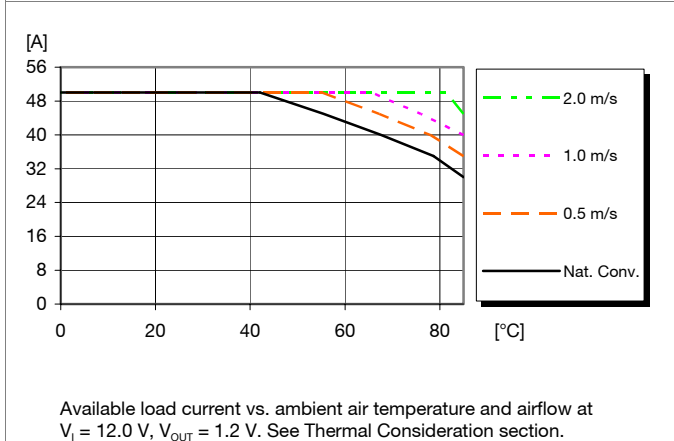
Efficiency



Power Dissipation



Output Current Derating



Output Characteristics

