BCP69; BC869; BC69PA

20 V, 2 A PNP medium power transistors Rev. 7 — 12 October 2011

Product data sheet

1. **Product profile**

1.1 General description

PNP medium power transistor series in Surface-Mounted Device (SMD) plastic packages.

Product overview Table 1.

Type number 11	Package	NPN complement		
	NXP	JEITA	JEDEC	
BCP69	SOT223	SC-73	-	BCP68
BC869	SOT89	SC-62	TO-243	BC868
BC69PA	SOT1061	-	-	BC68PA

^[1] Valid for all available selection groups.

1.2 Features and benefits

- High current
- Three current gain selections
- High power dissipation capability
- Exposed heatsink for excellent thermal and electrical conductivity (SOT89, SOT1061)
- Leadless very small SMD plastic package with medium power capability (SOT1061)
- AEC-Q101 qualified

1.3 Applications

- Linear voltage regulators
- High-side switches
- Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-20	V
I _C	collector current		-	-	-2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-3	Α



Table 2. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
h _{FE}	DC current gain	$V_{CE} = -1 \text{ V};$ $I_{C} = -500 \text{ mA}$	<u>[1]</u> 85	-	375	
	h _{FE} selection -16	$V_{CE} = -1 \text{ V};$ $I_{C} = -500 \text{ mA}$	11 100	-	250	
	h _{FE} selection -25	$V_{CE} = -1 \text{ V};$ $I_{C} = -500 \text{ mA}$	<u>[1]</u> 160	-	375	

^[1] Pulse test: $t_p \leq 300~\mu s;~\delta = 0.02.$

2. Pinning information

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Ta	ы	_	າ	0	in	-	in	~
14	u	ıe	J	_		ш	ш	u

Pin	Description	Simplified outline	Graphic symbol
SOT223			
1	base		0.4
2	collector	4	2, 4
3	emitter		1—
4	collector	1 2 3	3 sym028
SOT89			
1	emitter		_
2	collector		2
3	base	$\begin{bmatrix} \vdots \\ \vdots \\ 3 & 2 & 1 \end{bmatrix}$	3 ————————————————————————————————————
SOT1061			
1	base		2
2	emitter	3	3
3	collector	Transparent top view	1

3. Ordering information

Table 4. Ordering information

Type number[1]	Package						
	Name	Description	Version				
BCP69	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223				
BC869	SC-62	plastic surface-mounted package; exposed die pad for good heat transfer; 3 leads	SOT89				
BC69PA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body $2\times2\times0.65$ mm	SOT1061				

^[1] Valid for all available selection groups.

4. Marking

Table 5. Marking codes

3	
Type number	Marking code
BCP69	BCP69
BCP69-16	BCP69/16
BCP69-25	BCP69/25
BC869	CEC
BC869-16	CGC
BC869-25	CHC
BC69PA	B3
BC69-16PA	ВМ
BC69-25PA	BN

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-32	V
V_{CEO}	collector-emitter voltage	open base	-	-20	V
V_{EBO}	emitter-base voltage	open collector	-	-5	V
I _C	collector current		-	-2	Α
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-3	Α
I _B	base current		-	-0.4	Α
I _{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	-0.4	Α
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$			
	BCP69		<u>[1]</u> _	0.65	W
			[2] _	1.00	W
			[3]	1.35	W
	BC869		<u>[1]</u> _	0.50	W
			[2]	0.95	W
			[3]	1.35	W
	BC69PA		<u>[1]</u> _	0.42	W
			[2]	0.83	W
			[3]	1.10	W
			<u>[4]</u> _	0.81	W
			[5]	1.65	W
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

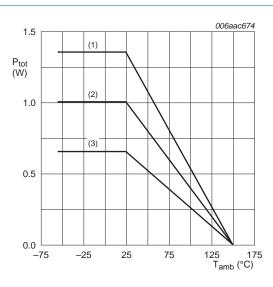
^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

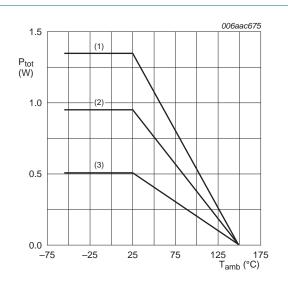
^[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

^[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm².



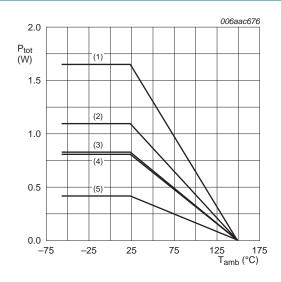
- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig 1. Power derating curves SOT223



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig 2. Power derating curves SOT89



- (1) FR4 PCB, 4-layer copper, mounting pad for collector 1 cm²
- (2) FR4 PCB, single-sided copper, mounting pad for collector 6 cm²
- (3) FR4 PCB, single-sided copper, mounting pad for collector 1 cm²
- (4) FR4 PCB, 4-layer copper, standard footprint
- (5) FR4 PCB, single-sided copper, standard footprint

Fig 3. Power derating curves SOT1061

6. Thermal characteristics

Table 7 Thermal characteristics

Symbol Parameter Conditions Min Typ Max R _{th(j-a)} thermal resistance from in free air	Unit
R _{th(j-a)} thermal resistance from in free air	
junction to ambient	
BCP69 <u>[1]</u> 192	K/W
2 125	K/W
<u>[3]</u> 93	K/W
BC869 [1] 250	K/W
2 132	K/W
<u>[3]</u> 93	K/W
BC69PA [1] 298	K/W
2 151	K/W
<u>3</u> 114	K/W
<u>[4]</u> 154	K/W
<u>[5]</u> 76	K/W
R _{th(j-sp)} thermal resistance from junction to solder point	
BCP69 - 16	K/W
BC869 16	K/W
BC69PA 20	K/W

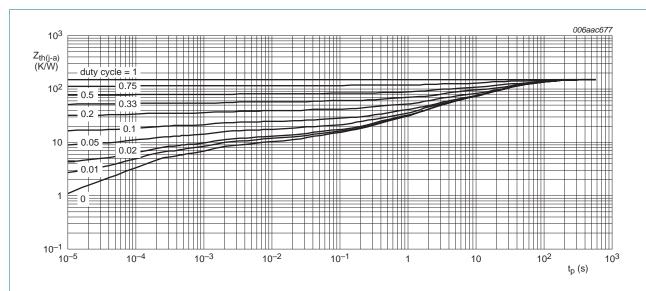
^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

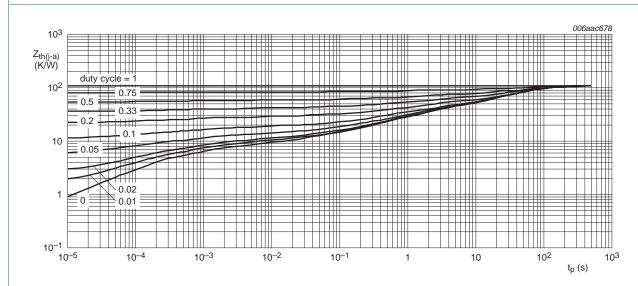
^[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

^[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm².



FR4 PCB, standard footprint

Fig 4. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values



FR4 PCB, mounting pad for collector 1 cm²

Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values

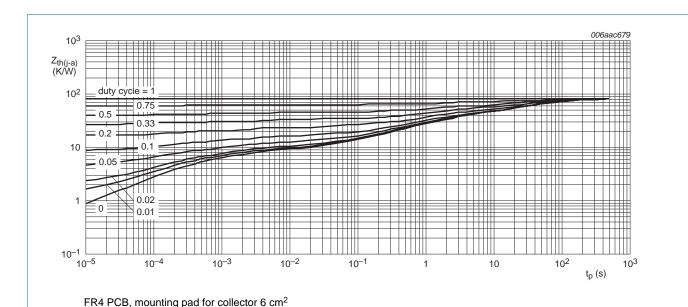
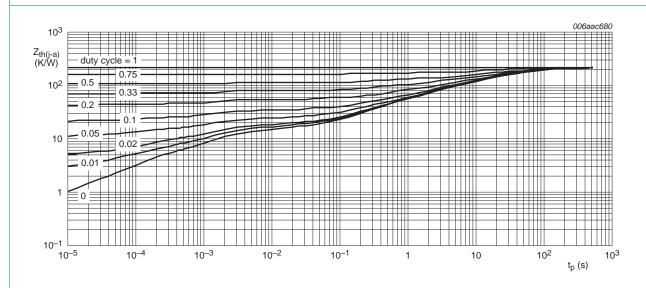


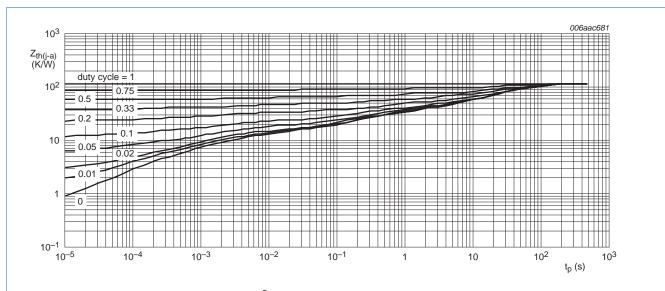
Fig 6. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values



FR4 PCB, standard footprint

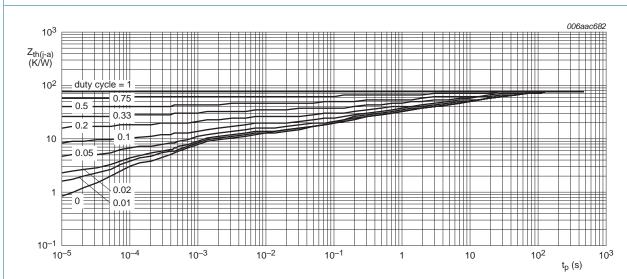
Fig 7. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89;

typical values



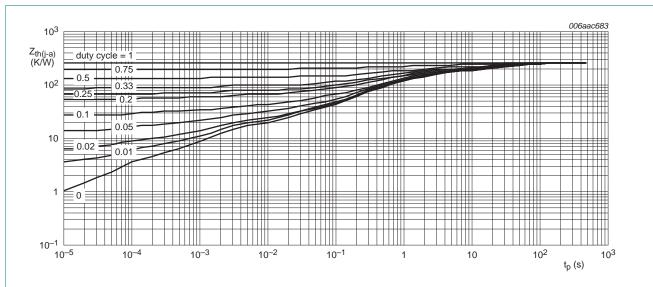
FR4 PCB, mounting pad for collector 1 cm²

Fig 8. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89; typical values



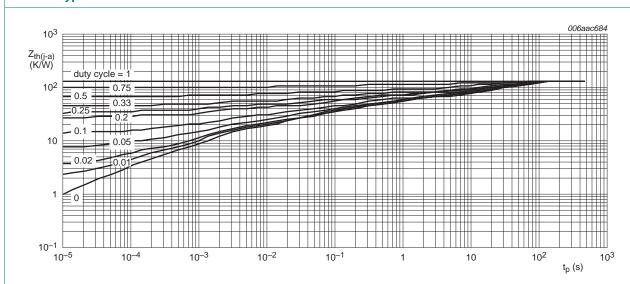
FR4 PCB, mounting pad for collector 6 cm²

Fig 9. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89; typical values



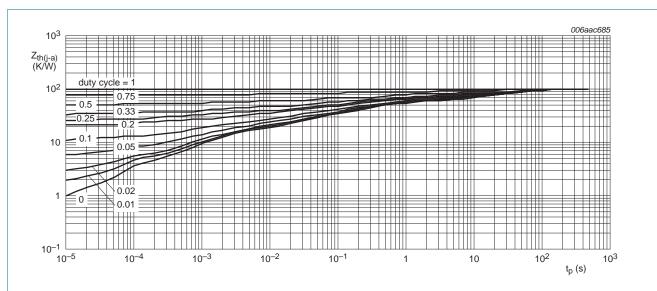
FR4 PCB, single-sided copper, standard footprint

Fig 10. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



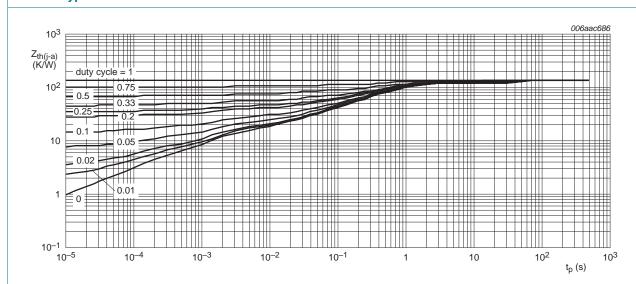
FR4 PCB, single-sided copper, mounting pad for collector 1 cm²

Fig 11. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



FR4 PCB, single-sided copper, mounting pad for collector 6 cm²

Fig 12. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



FR4 PCB, 4-layer copper, standard footprint

Fig 13. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values

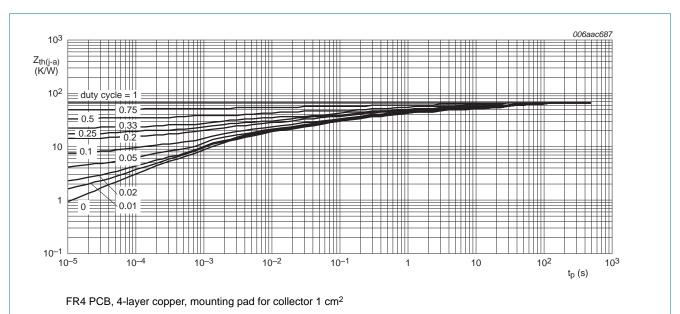


Fig 14. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values

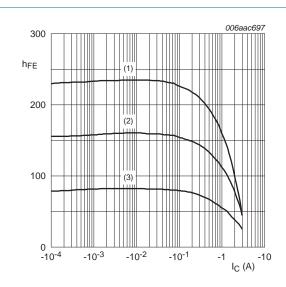
7. Characteristics

Table 8. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I_{CBO}	collector-base cut-off	$V_{CB} = -25 \text{ V}; I_E = 0 \text{ A}$		-	-	-100	nΑ
	current	$V_{CB} = -25 \text{ V; } I_E = 0 \text{ A;}$ $T_j = 150 \text{ °C}$		-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$		-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -10 \text{ V}$					
		$I_C = -5 \text{ mA}$		50	-	-	
	DC current gain	$V_{CE} = -1 V$					
		$I_C = -500 \text{ mA}$	[1]	85	-	375	
		$I_C = -1 A$	[1]	60	-	-	
		$I_C = -2 A$	[1]	40	-	-	
	DC current gain	$V_{CE} = -1 V$					
	h _{FE} selection -16	$I_C = -500 \text{ mA}$	[1]	100	-	250	
	h _{FE} selection -25	$I_C = -500 \text{ mA}$	[1]	160	-	375	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -1 A$; $I_B = -100 \text{ mA}$	[1]	-	-	-0.5	V
		$I_C = -2 \text{ A}; I_B = -200 \text{ mA}$	<u>[1]</u>			-0.6	V
V_{BE}	base-emitter voltage	$V_{CE} = -10 \text{ V}; I_{C} = -5 \text{ mA}$	[1]	-	-	-0.7	V
		$V_{CE} = -1 \ V; \ I_{C} = -1 \ A$	<u>[1]</u>	-	-	–1	V
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ $f = 1 \text{ MHz}$		-	28	-	pF
f _T	transition frequency	$V_{CE} = -5 \text{ V}; I_{C} = -50 \text{ mA};$ f = 100 MHz		40	140	-	MHz

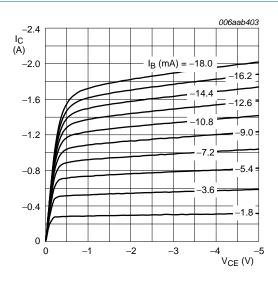
^[1] Pulse test: $t_p \le 300~\mu s;~\delta = 0.02.$



$$V_{CE} = -1 V$$

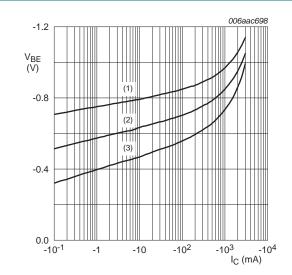
- (1) $T_{amb} = 100 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 15. h_{FE} selection -16: DC current gain as a function of collector current; typical values



T_{amb} = 25 °C

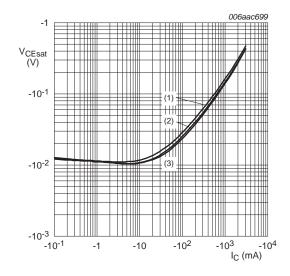
Fig 16. h_{FE} selection -16: collector current as a function of collector-emitter voltage; typical values





- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 100 \, ^{\circ}C$

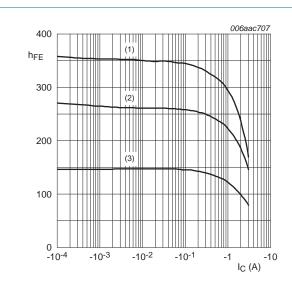
Fig 17. h_{FE} selection -16: base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

- (1) $T_{amb} = 100 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

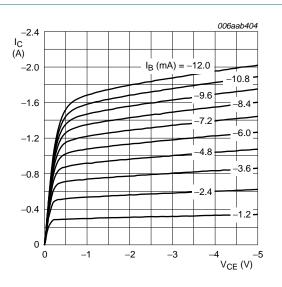
Fig 18. h_{FE} selection -16: collector-emitter saturation voltage as a function of collector current; typical values



$$V_{CE} = -1 V$$

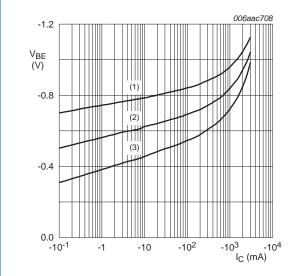
- (1) $T_{amb} = 100 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

Fig 19. h_{FE} selection -25: DC current gain as a function of collector current; typical values



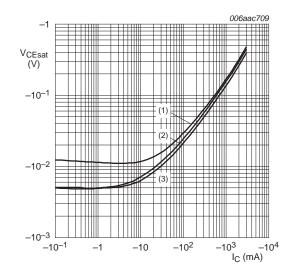
T_{amb} = 25 °C

Fig 20. h_{FE} selection -25: collector current as a function of collector-emitter voltage; typical values



- $V_{CE} = -1 V$
- (1) $T_{amb} = -55 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 100 \, ^{\circ}C$

Fig 21. h_{FE} selection -25: base-emitter voltage as a function of collector current; typical values



- $I_C/I_B = 10$
- (1) $T_{amb} = 100 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = -55 \, ^{\circ}C$

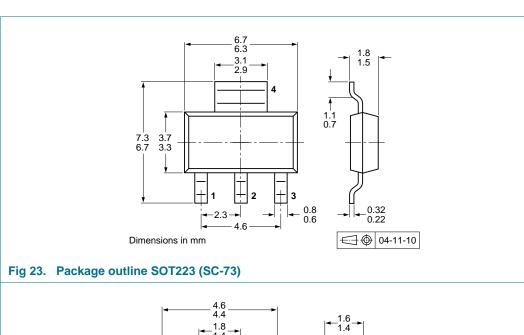
Fig 22. h_{FE} selection -25: collector-emitter saturation voltage as a function of collector current; typical values

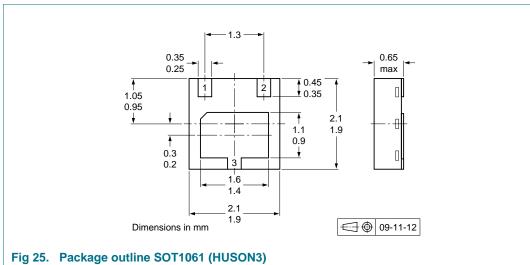
8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline





10. Packing information

Packing methods Table 9.

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Туре	Package	·		Packing quantity		
number[2]				1000	3000	4000
BCP69	SOT223	8 mm pitch, 12 mm tape and reel		-115	-	-135
BC869	SOT89	8 mm pitch, 12 mm tape and reel; T1	[3]	-115	-	-135
		8 mm pitch, 12 mm tape and reel; T3	[4]	-146	-	-
BC69PA	SOT1061	4 mm pitch, 8 mm tape and reel		-	-115	-

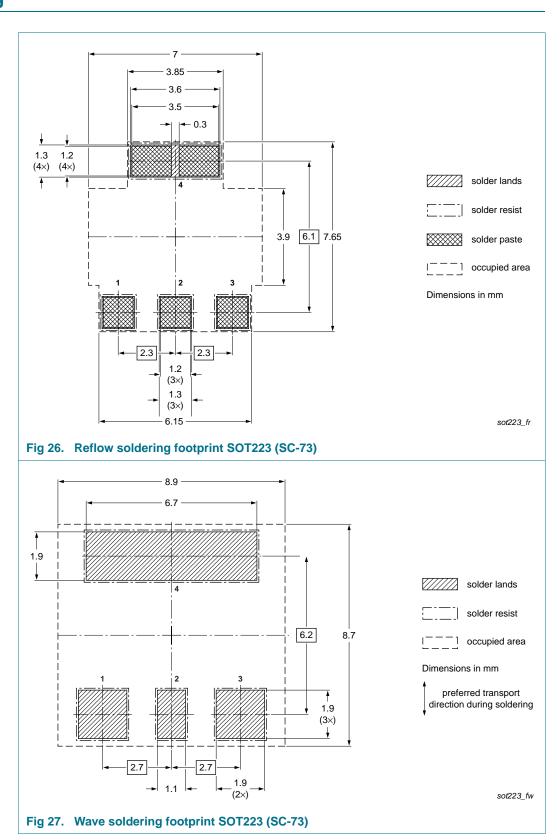
^[1] For further information and the availability of packing methods, see Section 14.

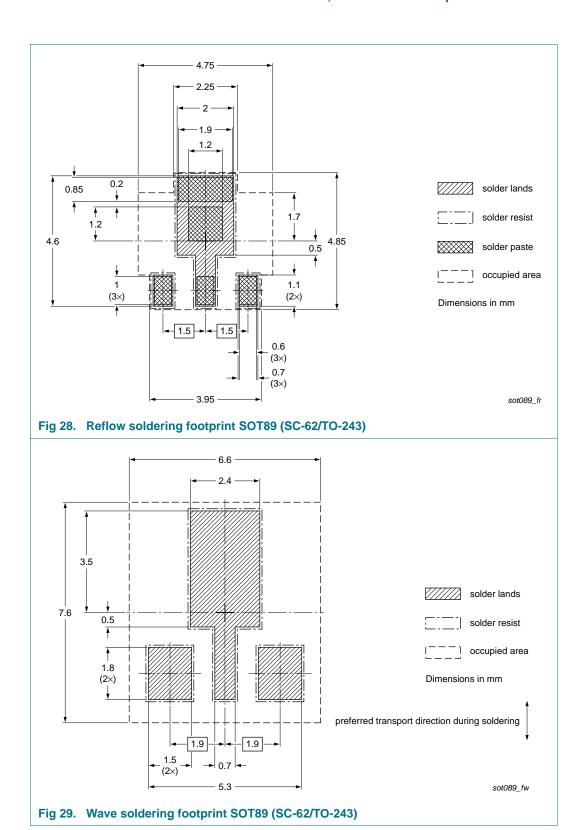
Valid for all available selection groups.

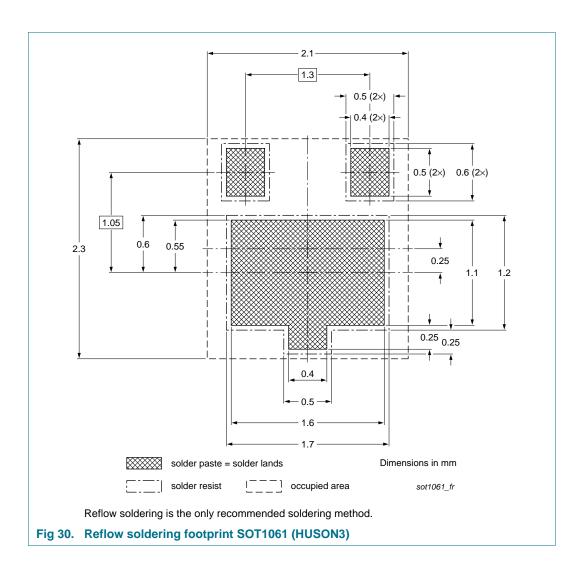
T1: normal taping

^[4] T3: 90° rotated taping

11. Soldering







12. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP69_BC869_BC69PA v.7	20111012	Product data sheet	-	BC869_6 BCP69_6
Modifications:		of this document has been of NXP Semiconductors.	redesigned to co	mply with the new identity
	 Legal texts 	have been adapted to the	new company nar	ne where appropriate.
	 Type number 	er BC69PA added		
	 Type number 	er BCP69-16/DG and BCP	69-16/IN removed	I
	 Section 1 "F 	Product profile": updated		
	 Section 2 "F 	Pinning information": updat	ed	
		Ordering information": upda	ated	
		Marking": updated		
	 Section 10 ' 	"Packing information":upda	ited	
	• <u>Table 6, 7</u> a	nd 8: updated according to	latest measurem	ents
	 Figure 1, 15 	to 18 updated		
	• Figure 2 to	14, 24 to 25, 28 to 30: add	ed	
BC869_6	20041108	Product data sheet	-	BC869_5
BC869_5	20031202	Product specification	-	BC869_4
BC869_4	19990408	Product specification	-	BC869_3
BC869_3	19980716	Product specification	-	BC869_CNV_2
BC869_CNV_2	19970401	Product specification	-	-
BCP69_6	20081202	Product data sheet	-	BCP69_5
BCP69_5	20031125	Product specification	-	BCP69_4
BCP69_4	20021115	Product specification	-	BCP69_3
BCP69_3	19990408	Product specification	-	BCP69_CNV_2
BCP69_CNV_2	19970312	Product specification	-	-

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

13.2 Definitions

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14. Contact information

For more information, please visit: http://www.nxp.com

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