

November 2013

KSH44H11 / KSH44H11I **NPN Epitaxial Silicon Transistor**

Features

• Lead Formed for Surface Mount Application (No Suffix)

- Straight Lead (I-PAK, "- I" Suffix)
- · Electrically Similar to Popular KSE44H
- · Fast Switching Speeds
- Low Collector-Emitter Saturation Voltage

Applications

- · Switching Regulators
- Converters
- · Power Amplifiers

Description

Designed for general-purpose power and switching, such as output or driver stages in applications.



Ordering Information

Part Number	Top Mark	Package	Packing Method
KSH44H11TF	KSH44H11	TO-252 3L (DPAK)	Tape and Reel
KSH44H11TM	KSH44H11	TO-252 3L (DPAK)	Tape and Reel
KSH44H11ITU	KSH44H11-I	TO-251 3L (IPAK)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit	
V _{CEO}	Collector-Emitter Voltage	80	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current (DC)	8	Α	
I _{CP}	Collector Current (Pulse)	16	А	
D	Collector Dissipation (T _C = 25°C)	20.00	W	
P_{C}	Collector Dissipation (T _A = 25°C)	1.75	vv	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-65 to 150	°C	

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Electrical Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	I _C = 30 mA, I _B = 0	80			V
I _{CEO}	Collector Cut-Off Current	$V_{CE} = 80 \text{ V}, I_{B} = 0$			10	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{BE} = 5 \text{ V}, I_{C} = 0$			50	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 1 \text{ V, } I_{C} = 2 \text{ A}$	60			
	DC Current Gain	$V_{CE} = 1 \text{ V, } I_{C} = 4 \text{ A}$	40			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 8 \text{ A}, I_B = 0.4 \text{ A}$			1	V
V _{BE} (on)	Base-Emitter Saturation Voltage	$I_C = 8 \text{ A}, I_B = 0.8 \text{ A}$			1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ A}, I_{C} = 0.5 \text{ A}$		50		MHz
C _{ob}	Output Capacitance	V _{CB} = 10 V, f = 1 MHz		130		pF
t _{ON}	Turn-On Time	L 5 A		300		ns
t _{STG}	Storage Time	I _C = 5 A I _{B1} = -I _{B2} = - 0.5 A		500		ns
t _F	Fall Time	181 - 182 - 2.0 M		140		ns

Note:

1. Pulse test: pw \leq 300 μ s, duty cycle \leq 2%.

Typical Performance Characteristics

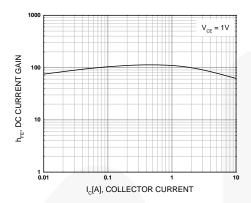


Figure 1. DC Current Gain

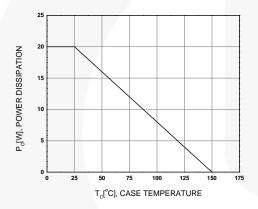


Figure 3. Power Derating

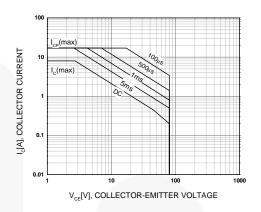
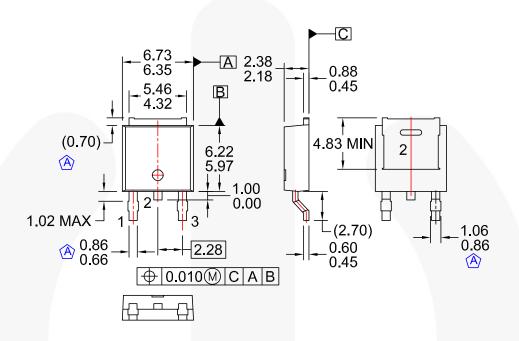


Figure 2. Safe Operating Area

Physical Dimensions

TO-252 3L



NOTES: UNLESS OTHERWISE SPECIFIED

- (A) CONFORMS TO JEDEC TO-252 VARIATION AB EXCEPT WHERE NOTED
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DRAWING CONFORMS TO ASME Y14.5M-1994
- D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- E) FORMERLY NAMED BD1733
- F) DRAWING FILE NAME: MKT-TO252D03REV1

Figure 4. 3-LEAD, TO-252, JEDEC TO-252 VAR. AB, SURFACE MOUNT (DPAK) (ACTIVE)

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Physical Dimensions (Continued)

TO-251 3L

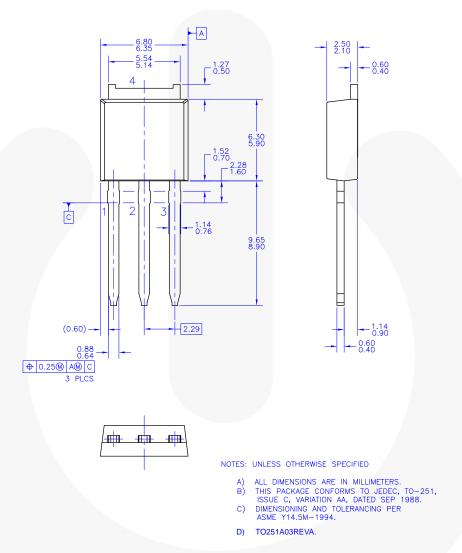


Figure 5. TO-251 (IPAK) MOLDED, 3-LEAD (ACTIVE)

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Definition of Terms		
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