

# MC74LCX125

## Low-Voltage CMOS Quad Buffer

### With 5 V-Tolerant Inputs and Outputs (3-State, Non-Inverting)

The MC74LCX125 is a high performance, non-inverting quad buffer operating from a 2.3 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A  $V_I$  specification of 5.5 V allows MC74LCX125 inputs to be safely driven from 5.0 V devices. The MC74LCX125 is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24 mA at the outputs. The Output Enable ( $\overline{OE_n}$ ) inputs, when HIGH, disable the outputs by placing them in a HIGH Z condition.

#### Features

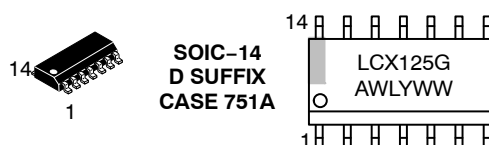
- Designed for 2.3 to 3.6 V  $V_{CC}$  Operation
- 5.0 V Tolerant – Interface Capability With 5.0 V TTL Logic
- Supports Live Insertion and Withdrawal
- $I_{OFF}$  Specification Guarantees High Impedance When  $V_{CC} = 0$  V
- LVTTTL Compatible
- LVC MOS Compatible
- 24 mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current in all Three Logic States (10  $\mu$ A)  
Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500 mA
- ESD Performance: Human Body Model >2000 V  
Machine Model >200 V
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



ON Semiconductor®

<http://onsemi.com>

#### MARKING DIAGRAMS



A = Assembly Location  
L, WL = Wafer Lot  
Y, YY = Year  
W, WW = Work Week  
G or ■ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

# MC74LCX125

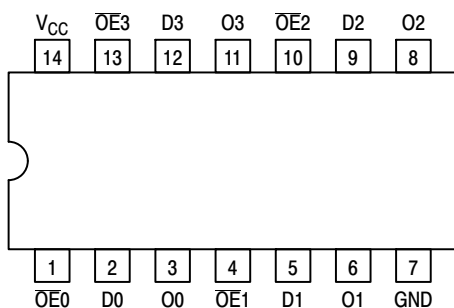


Figure 1. Pinout: 14-Lead (Top View)

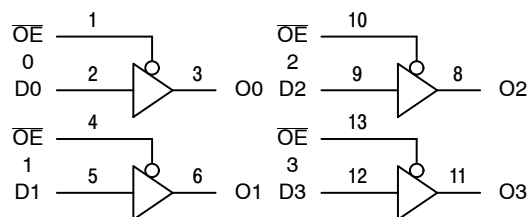


Figure 2. Logic Diagram

## PIN NAMES

| Pins            | Function             |
|-----------------|----------------------|
| OE <sub>n</sub> | Output Enable Inputs |
| D <sub>n</sub>  | Data Inputs          |
| O <sub>n</sub>  | 3-State Outputs      |

## TRUTH TABLE

| INPUTS          |                | OUTPUTS        |
|-----------------|----------------|----------------|
| OE <sub>n</sub> | D <sub>n</sub> | O <sub>n</sub> |
| L               | L              | L              |
| L               | H              | H              |
| H               | X              | Z              |

H = High Voltage Level  
 L = Low Voltage Level  
 Z = High Impedance State  
 X = High or Low Voltage Level and Transitions Are Acceptable; for I<sub>CC</sub> reasons, DO NOT FLOAT Inputs

## MAXIMUM RATINGS

| Symbol           | Parameter                        | Value                                         | Condition                             | Units |
|------------------|----------------------------------|-----------------------------------------------|---------------------------------------|-------|
| V <sub>CC</sub>  | DC Supply Voltage                | -0.5 to +7.0                                  |                                       | V     |
| V <sub>I</sub>   | DC Input Voltage                 | -0.5 ≤ V <sub>I</sub> ≤ +7.0                  |                                       | V     |
| V <sub>O</sub>   | DC Output Voltage                | -0.5 ≤ V <sub>O</sub> ≤ +7.0                  | Output in 3-State                     | V     |
|                  |                                  | -0.5 ≤ V <sub>O</sub> ≤ V <sub>CC</sub> + 0.5 | Output in HIGH or LOW State. (Note 1) | V     |
| I <sub>IK</sub>  | DC Input Diode Current           | -50                                           | V <sub>I</sub> < GND                  | mA    |
| I <sub>OK</sub>  | DC Output Diode Current          | -50                                           | V <sub>O</sub> < GND                  | mA    |
|                  |                                  | +50                                           | V <sub>O</sub> > V <sub>CC</sub>      | mA    |
| I <sub>O</sub>   | DC Output Source/Sink Current    | ±50                                           |                                       | mA    |
| I <sub>CC</sub>  | DC Supply Current Per Supply Pin | ±100                                          |                                       | mA    |
| I <sub>GND</sub> | DC Ground Current Per Ground Pin | ±100                                          |                                       | mA    |
| T <sub>STG</sub> | Storage Temperature Range        | -65 to +150                                   |                                       | °C    |
| MSL              | Moisture Sensitivity             |                                               | Level 1                               |       |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. I<sub>O</sub> absolute maximum rating must be observed.

# MC74LCX125

## RECOMMENDED OPERATING CONDITIONS

| Symbol              | Parameter                                                                                                                                               | Min        | Typ                  | Max              | Units |
|---------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------|------------|----------------------|------------------|-------|
| $V_{CC}$            | Supply Voltage<br>Operating<br>Data Retention Only                                                                                                      | 2.0<br>1.5 | 2.5, 3.3<br>2.5, 3.3 | 3.6<br>3.6       | V     |
| $V_I$               | Input Voltage                                                                                                                                           | 0          |                      | 5.5              | V     |
| $V_O$               | Output Voltage<br>HIGH or LOW State<br>3-State                                                                                                          | 0<br>0     |                      | $V_{CC}$<br>5.5  | V     |
| $I_{OH}$            | HIGH Level Output Current<br>$V_{CC} = 3.0\text{ V} - 3.6\text{ V}$<br>$V_{CC} = 2.7\text{ V} - 3.0\text{ V}$<br>$V_{CC} = 2.3\text{ V} - 2.7\text{ V}$ |            |                      | -24<br>-12<br>-8 | mA    |
| $I_{OL}$            | LOW Level Output Current<br>$V_{CC} = 3.0\text{ V} - 3.6\text{ V}$<br>$V_{CC} = 2.7\text{ V} - 3.0\text{ V}$<br>$V_{CC} = 2.3\text{ V} - 2.7\text{ V}$  |            |                      | +24<br>+12<br>+8 | mA    |
| $T_A$               | Operating Free-Air Temperature                                                                                                                          | -40        |                      | +85              | °C    |
| $\Delta t/\Delta V$ | Input Transition Rise or Fall Rate, $V_{IN}$ from 0.8 V to 2.0 V,<br>$V_{CC} = 3.0\text{ V}$                                                            | 0          |                      | 10               | ns/V  |

## DC ELECTRICAL CHARACTERISTICS

| Symbol          | Characteristic                    | Condition                                                                                              | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ |         | Units         |
|-----------------|-----------------------------------|--------------------------------------------------------------------------------------------------------|-------------------------------------------------|---------|---------------|
|                 |                                   |                                                                                                        | Min                                             | Max     |               |
| $V_{IH}$        | HIGH Level Input Voltage (Note 2) | $2.3\text{ V} \leq V_{CC} \leq 2.7\text{ V}$                                                           | 1.7                                             |         | V             |
|                 |                                   | $2.7\text{ V} \leq V_{CC} \leq 3.6\text{ V}$                                                           | 2.0                                             |         |               |
| $V_{IL}$        | LOW Level Input Voltage (Note 2)  | $2.3\text{ V} \leq V_{CC} \leq 2.7\text{ V}$                                                           |                                                 | 0.7     | V             |
|                 |                                   | $2.7\text{ V} \leq V_{CC} \leq 3.6\text{ V}$                                                           |                                                 | 0.8     |               |
| $V_{OH}$        | HIGH Level Output Voltage         | $2.3\text{ V} \leq V_{CC} \leq 3.6\text{ V}; I_{OL} = 100\text{ }\mu\text{A}$                          | $V_{CC} - 0.2$                                  |         | V             |
|                 |                                   | $V_{CC} = 2.3\text{ V}; I_{OH} = -8\text{ mA}$                                                         | 1.8                                             |         |               |
|                 |                                   | $V_{CC} = 2.7\text{ V}; I_{OH} = -12\text{ mA}$                                                        | 2.2                                             |         |               |
|                 |                                   | $V_{CC} = 3.0\text{ V}; I_{OH} = -18\text{ mA}$                                                        | 2.4                                             |         |               |
|                 |                                   | $V_{CC} = 3.0\text{ V}; I_{OH} = -24\text{ mA}$                                                        | 2.2                                             |         |               |
| $V_{OL}$        | LOW Level Output Voltage          | $2.3\text{ V} \leq V_{CC} \leq 3.6\text{ V}; I_{OL} = 100\text{ }\mu\text{A}$                          |                                                 | 0.2     | V             |
|                 |                                   | $V_{CC} = 2.3\text{ V}; I_{OL} = 8\text{ mA}$                                                          |                                                 | 0.6     |               |
|                 |                                   | $V_{CC} = 2.7\text{ V}; I_{OL} = 12\text{ mA}$                                                         |                                                 | 0.4     |               |
|                 |                                   | $V_{CC} = 3.0\text{ V}; I_{OL} = 16\text{ mA}$                                                         |                                                 | 0.4     |               |
|                 |                                   | $V_{CC} = 3.0\text{ V}; I_{OL} = 24\text{ mA}$                                                         |                                                 | 0.55    |               |
| $I_{OZ}$        | 3-State Output Current            | $V_{CC} = 3.6\text{ V}, V_{IN} = V_{IH} \text{ or } V_{IL},$<br>$V_{OUT} = 0 \text{ to } 5.5\text{ V}$ |                                                 | $\pm 5$ | $\mu\text{A}$ |
| $I_{OFF}$       | Power Off Leakage Current         | $V_{CC} = 0, V_{IN} = 5.5\text{ V} \text{ or } V_{OUT} = 5.5\text{ V}$                                 |                                                 | 10      | $\mu\text{A}$ |
| $I_{IN}$        | Input Leakage Current             | $V_{CC} = 3.6\text{ V}, V_{IN} = 5.5\text{ V} \text{ or } \text{GND}$                                  |                                                 | $\pm 5$ | $\mu\text{A}$ |
| $I_{CC}$        | Quiescent Supply Current          | $V_{CC} = 3.6\text{ V}, V_{IN} = 5.5\text{ V} \text{ or } \text{GND}$                                  |                                                 | 10      | $\mu\text{A}$ |
| $\Delta I_{CC}$ | Increase in $I_{CC}$ per Input    | $2.3 \leq V_{CC} \leq 3.6\text{ V}; V_{IH} = V_{CC} - 0.6\text{ V}$                                    |                                                 | 500     | $\mu\text{A}$ |

2. These values of  $V_I$  are used to test DC electrical characteristics only.

# MC74LCX125

## AC CHARACTERISTICS ( $t_R = t_F = 2.5 \text{ ns}$ ; $R_L = 500 \Omega$ )

| Symbol                                 | Parameter                                      | Waveform | Limits                          |            |                         |            |                                 |            | Units |
|----------------------------------------|------------------------------------------------|----------|---------------------------------|------------|-------------------------|------------|---------------------------------|------------|-------|
|                                        |                                                |          | T <sub>A</sub> = −40°C to +85°C |            |                         |            |                                 |            |       |
|                                        |                                                |          | V <sub>CC</sub> = 3.3 V ± 0.3 V |            | V <sub>CC</sub> = 2.7 V |            | V <sub>CC</sub> = 2.5 V ± 0.2 V |            |       |
|                                        |                                                |          | C <sub>L</sub> = 50 pF          |            | C <sub>L</sub> = 50 pF  |            | C <sub>L</sub> = 30 pF          |            |       |
|                                        |                                                |          | Min                             | Max        | Min                     | Max        | Min                             | Max        |       |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay Time<br>Input to Output      | 1        | 1.5<br>1.5                      | 6.0<br>6.0 | 1.5<br>1.5              | 6.5<br>6.5 | 1.5<br>1.5                      | 7.2<br>7.2 | ns    |
| t <sub>PZH</sub><br>t <sub>PZL</sub>   | Output Enable Time to<br>High and Low Level    | 2        | 1.5<br>1.5                      | 7.0<br>7.0 | 1.5<br>1.5              | 8.0<br>8.0 | 1.5<br>1.5                      | 9.1<br>9.1 | ns    |
| t <sub>PHZ</sub><br>t <sub>PLZ</sub>   | Output Disable Time From<br>High and Low Level | 2        | 1.5<br>1.5                      | 6.0<br>6.0 | 1.5<br>1.5              | 7.0<br>7.0 | 1.5<br>1.5                      | 7.2<br>7.2 | ns    |
| t <sub>OSHL</sub><br>t <sub>OSLH</sub> | Output-to-Output Skew<br>(Note 3)              |          |                                 | 1.0<br>1.0 |                         |            |                                 |            | ns    |

3. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW ( $t_{OSHL}$ ) or LOW-to-HIGH ( $t_{OSLH}$ ); parameter guaranteed by design.

## DYNAMIC SWITCHING CHARACTERISTICS

| Symbol    | Characteristic                         | Condition                                                                                                                                                                                                    | $T_A = +25^\circ\text{C}$ |              |     | Units |
|-----------|----------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------|--------------|-----|-------|
|           |                                        |                                                                                                                                                                                                              | Min                       | Typ          | Max |       |
| $V_{OLP}$ | Dynamic LOW Peak Voltage<br>(Note 4)   | $V_{CC} = 3.3 \text{ V}$ , $C_L = 50 \text{ pF}$ , $V_{IH} = 3.3 \text{ V}$ , $V_{IL} = 0 \text{ V}$<br>$V_{CC} = 2.5 \text{ V}$ , $C_L = 30 \text{ pF}$ , $V_{IH} = 2.5 \text{ V}$ , $V_{IL} = 0 \text{ V}$ |                           | 0.8<br>0.6   |     | V     |
| $V_{OLV}$ | Dynamic LOW Valley Voltage<br>(Note 4) | $V_{CC} = 3.3 \text{ V}$ , $C_L = 50 \text{ pF}$ , $V_{IH} = 3.3 \text{ V}$ , $V_{IL} = 0 \text{ V}$<br>$V_{CC} = 2.5 \text{ V}$ , $C_L = 30 \text{ pF}$ , $V_{IH} = 2.5 \text{ V}$ , $V_{IL} = 0 \text{ V}$ |                           | -0.8<br>-0.6 |     | V     |

4. Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH-to-LOW or LOW-to-HIGH. The remaining output is measured in the LOW state.

## CAPACITIVE CHARACTERISTICS

| Symbol    | Parameter                     | Condition                                                          | Typical | Units |
|-----------|-------------------------------|--------------------------------------------------------------------|---------|-------|
| $C_{IN}$  | Input Capacitance             | $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$         | 7       | pF    |
| $C_{OUT}$ | Output Capacitance            | $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$         | 8       | pF    |
| $C_{PD}$  | Power Dissipation Capacitance | 10 MHz, $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$ | 25      | pF    |

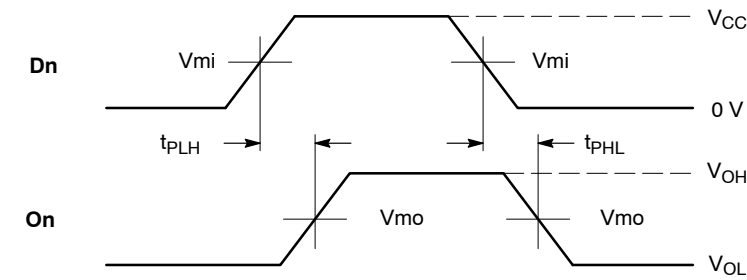
## ORDERING INFORMATION

| Device          | Package                | Shipping <sup>†</sup> |
|-----------------|------------------------|-----------------------|
| MC74LCX125DG    | SOIC-14<br>(Pb-Free)   | 55 Units / Rail       |
| MC74LCX125DR2G  | SOIC-14<br>(Pb-Free)   | 2500 Tape & Reel      |
| MC74LCX125DTG   | TSSOP-14<br>(Pb-Free)  | 96 Units / Rail       |
| MC74LCX125DTR2G | TSSOP-14<br>(Pb-Free)  | 2500 Tape & Reel      |
| NLVLCX125DTR2G  | TSSOP-14*<br>(Pb-Free) | 2500 Tape & Reel      |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

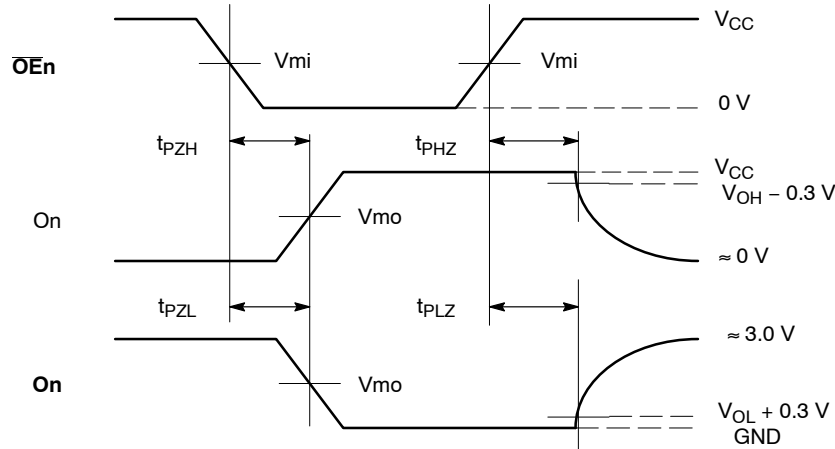
\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified.

## MC74LCX125



**WAVEFORM 1 – PROPAGATION DELAYS**

$t_R = t_F = 2.5 \text{ ns}$ , 10% to 90%;  $f = 1 \text{ MHz}$ ;  $t_W = 500 \text{ ns}$

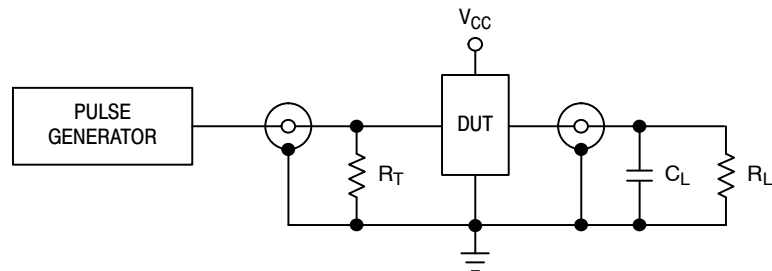


**WAVEFORM 2 – OUTPUT ENABLE AND DISABLE TIMES**

$t_R = t_F = 2.5 \text{ ns}$ , 10% to 90%;  $f = 1 \text{ MHz}$ ;  $t_W = 500 \text{ ns}$

| Symbol | $V_{CC}$                          |                 |                                   |
|--------|-----------------------------------|-----------------|-----------------------------------|
|        | $3.3 \text{ V} \pm 0.3 \text{ V}$ | $2.7 \text{ V}$ | $2.5 \text{ V} \pm 0.2 \text{ V}$ |
| Vmi    | 1.5 V                             | 1.5 V           | $V_{CC}/2$                        |
| Vmo    | 1.5 V                             | 1.5 V           | $V_{CC}/2$                        |

**Figure 3. AC Waveforms**



$C_L = 50 \text{ pF}$  at  $V_{CC} = 3.3 \pm 0.3 \text{ V}$  or equivalent (includes jig and probe capacitance)

$C_L = 30 \text{ pF}$  at  $V_{CC} = 2.5 \pm 0.2 \text{ V}$  or equivalent (includes jig and probe capacitance)

$R_L = R_1 = 500 \Omega$  or equivalent

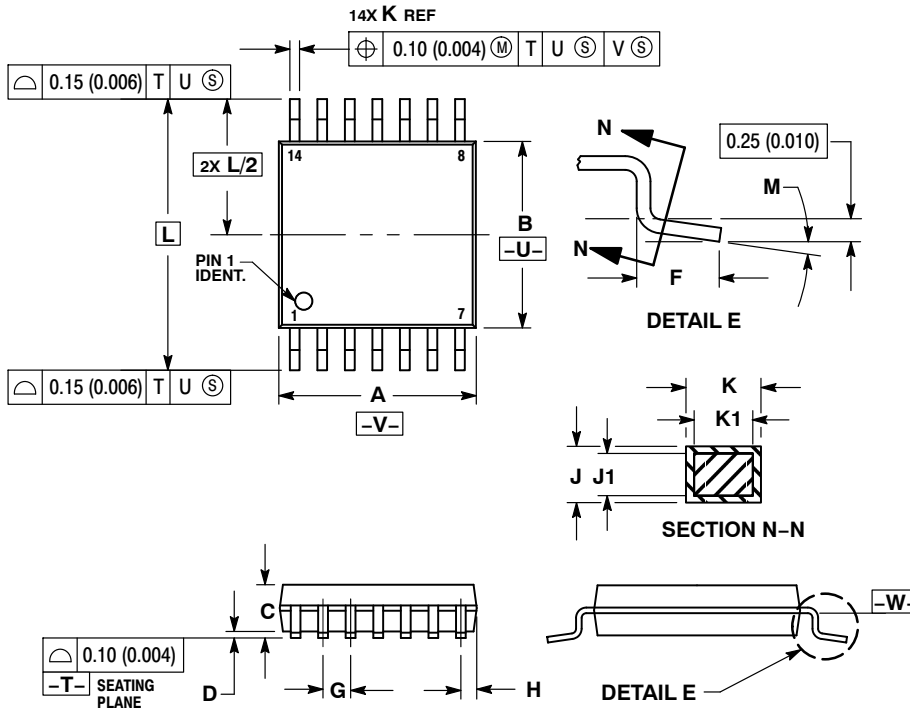
$R_T = Z_{OUT}$  of pulse generator (typically  $50 \Omega$ )

**Figure 4. Test Circuit**

# MC74LCX125

## PACKAGE DIMENSIONS

TSSOP-14  
CASE 948G  
ISSUE B

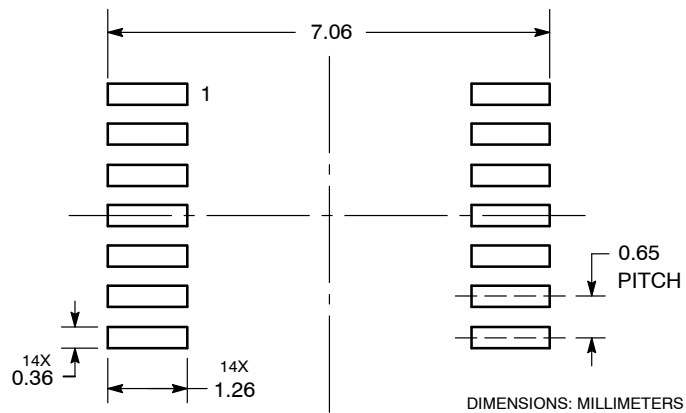


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -V-.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 4.90        | 5.10 | 0.193     | 0.200 |
| B   | 4.30        | 4.50 | 0.169     | 0.177 |
| C   | ---         | 1.20 | ---       | 0.047 |
| D   | 0.05        | 0.15 | 0.002     | 0.006 |
| F   | 0.50        | 0.75 | 0.020     | 0.030 |
| G   | 0.65 BSC    |      | 0.026 BSC |       |
| H   | 0.50        | 0.60 | 0.020     | 0.024 |
| J   | 0.09        | 0.20 | 0.004     | 0.008 |
| J1  | 0.09        | 0.16 | 0.004     | 0.006 |
| K   | 0.19        | 0.30 | 0.007     | 0.012 |
| K1  | 0.19        | 0.25 | 0.007     | 0.010 |
| L   | 6.40 BSC    |      | 0.252 BSC |       |
| M   | 0°          | 8°   | 0°        | 8°    |

### SOLDERING FOOTPRINT\*

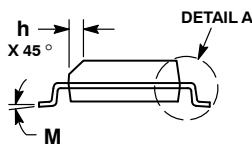
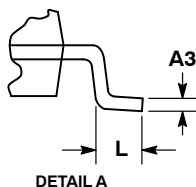
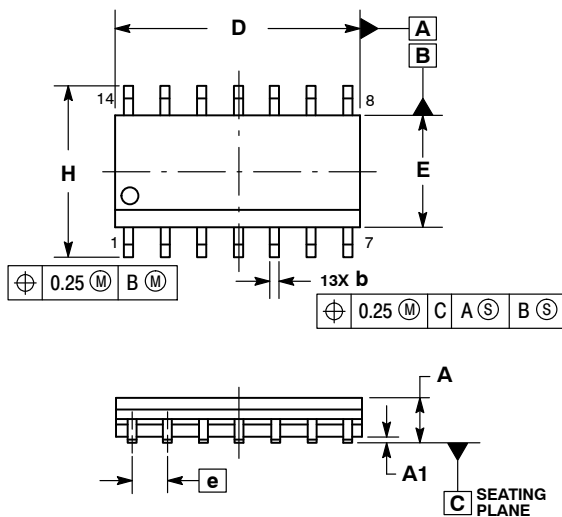


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MC74LCX125

## PACKAGE DIMENSIONS

SOIC-14 NB  
CASE 751A-03  
ISSUE K

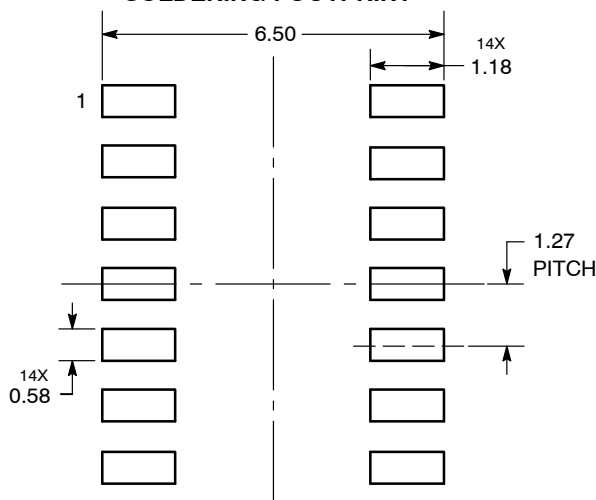


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF AT MAXIMUM MATERIAL CONDITION.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 1.35        | 1.75 | 0.054     | 0.068 |
| A1  | 0.10        | 0.25 | 0.004     | 0.010 |
| A3  | 0.19        | 0.25 | 0.008     | 0.010 |
| b   | 0.35        | 0.49 | 0.014     | 0.019 |
| D   | 8.55        | 8.75 | 0.337     | 0.344 |
| E   | 3.80        | 4.00 | 0.150     | 0.157 |
| e   | 1.27 BSC    |      | 0.050 BSC |       |
| H   | 5.80        | 6.20 | 0.228     | 0.244 |
| h   | 0.25        | 0.50 | 0.010     | 0.019 |
| L   | 0.40        | 1.25 | 0.016     | 0.049 |
| M   | 0°          | 7°   | 0°        | 7°    |

### SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marketing.pdf](http://www.onsemi.com/site/pdf/Patent-Marketing.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

N. American Technical Support: 800-282-9855 Toll Free  
USA/Canada

Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative

MC74LCX125/D

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[ON Semiconductor:](#)

[MC74LCX125DG](#) [MC74LCX125DR2G](#) [MC74LCX125DTG](#) [MC74LCX125DTR2G](#)