Instant GUI 7.0" Touch Screen LCD Kit



uEZGUI-1788-70WVE for the NXP LPC1788



Highlights

Features

- Seiko 7.0" TFT WVGA 800x480 LCD Panel with integrated Touch Screen
- NXP LPC1788 CPU running at 100MHz (with 512KB internal Flash)
- 8MB of SDRAM (opt to 16MB)
- 8MB of NOR FLASH (opt to 16MB)
- 2GB microSD Memory Card
- USB Device Mini-B PC communications
- USB Device Mini-B connector for power
- NV Data Storage via 4kB Internal EEPROM
- Low power, Real-Time Clock with Supercap Backup
- Speaker, 3-axis Accelerometer, Temperature Sensor
- Mini-JTAG Debug Connector
- Optional 128Mbit Serial PCM Memory
- Optional Redpine Signals 802.11n Wi-Fi module
- Fine Pitch I/O Connectors for External Expansion
 - Serial Ports, UART, I2C, SPI, USB Host/Device
 - ➢ RMII interface for Ethernet 10/100

Software

- uEZ® / FreeRTOS Rapid Development Platform
- MicroSD card maps as USB Flash Drive to the PC
- Rowley CrossWorks Compiler and Tool Suite
- Segger J-Link Lite JTAG for programming and debug



















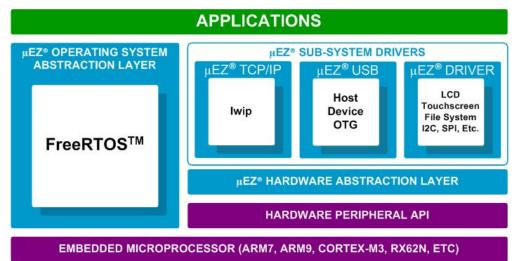
The NXP LPC1788 ARM Cortex-M3 based microcontroller runs the open source uEZ[®] + FreeRTOS software platform. The LPC1788 has 512KB of internal Flash memory, 96KB of internal SRAM, a 10/100 Ethernet Media Access Controller (MAC), a USB full speed device/host/OTG controller, four UARTs, two CAN channels and a collection of serial communications interfaces. The uEZ GUI board also includes 8MB of external SDRAM and 8MB of external NOR Flash.

Software Included

 $\mu EZ^{\text{(Pronounced Muse)}}$ is an open source rapid development platform that supplies application developers with an extensive library of open source software, drivers, and processor support - all under a common framework. $\mu EZ^{\text{(Pronounced Muse)}}$ allows companies to focus on innovation and their value-added applications while minimizing development time and maximizing software reuse.

The diagram below shows a typical embedded application stack. The $\mu EZ^{\mathbb{R}}$ components comprise three primary categories to simplify embedded application development:

- Operating System Abstraction Layer ($\mu EZ^{\mathbb{R}}$ OSAL)
- Sub-system drivers (ex: $\mu EZ^{\mathbb{R}}$ TCP/IP, $\mu EZ^{\mathbb{R}}$ USB, $\mu EZ^{\mathbb{R}}$ Driver)
- Hardware Abstraction Layer (µEZ[®] HAL)



The **uEZGUI-1788-70WVE** is designed to be used as an "off-the-shelf" Graphical User Interface (GUI) or Human Machine Interface (HMI) in a variety of end customer applications. The miniature, self-contained design is well suited to be embedded directly into your product or FDI offers prepackaged versions for stand-alone use. FDI also offers low cost customization services for customer specific hardware, software or packaging applications at volumes of 500 units or more.



Part Number: uEZGUI-1788-70WVE Order Online at: www.teamfdi.com/uezgui

Warranty: 30-day money back guarantee Phone 256-883-1240 Fax 256-883-1241 sales@teamfdi.com www.teamfdi.com Kit Contents:

- uEZ[®] GUI 7.0" Board with LPC1788
- Seiko 7.0" WVGA Touch Screen LCD
- USB Device cables for Power and PC communications
- Mini JTAG Debugger with cables

Download Users Manual, documents, schematics, and software examples at: www.teamfdi.com/uEZGUI



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