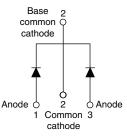


Vishay Semiconductors



Schottky Rectifier, 2 x 7.5 A





PRODUCT SUMMARY						
Package	TO-220AB					
I _{F(AV)}	2 x 7.5 A					
V _R	35 V, 45 V					
V _F at I _F	0.57 V					
I _{RM} max.	15 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	7 mJ					

FEATURES

- 150 °C T_J operation
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- · Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-MBR15...CT... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	15	А			
V _{RRM}		35/45	V			
I _{FSM}	t _p = 5 μs sine	690	А			
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V			
TJ	Range	- 65 to 150	°C			

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-MBR1535CTPbF	VS-MBR1535CT-N3	VS-MBR1545CTPbF	VS-MBR1545CT-N3	UNITS				
Maximum DC reverse voltage	V _R	35	35	45	45	V				
Maximum working peak reverse voltage	V _{RWM}		55	40	44	v				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg		$T_{\rm C}$ = 131 °C, rated V _B		7.5			
forward current	per device	I _{F(AV)}	$T_{\rm C} = 131$ C, rated $V_{\rm R}$		15			
Maximum peak one cycle non-repetitive surge		I _{ESM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied				
		1 OM	Surge applied at rated load condition half wave single phase 60 Hz		150			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 3.5 mH		7	mJ		
Repetitive avalanche current pe	r leg	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxin		2	А		

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1



RoHS

COMPLIANT

HALOGEN

FREE



VS-MBR15...CTPbF Series, VS-MBR15...CT-N3 Series

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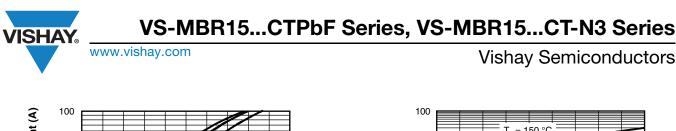
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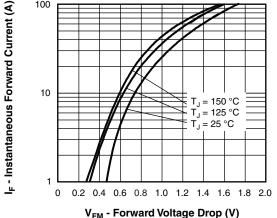
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
		15 A	T _J = 25 °C	0.84				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	T 405.00	0.57	V			
		15 A	T _J = 125 °C	0.72				
Maximum instantaneous reverse current	ı (1)	T _J = 25 °C	Rated DC voltage	0.1	m ^			
	I _{RM} ⁽¹⁾	T _J = 125 °C	Haleu DC Vollage	15	mA			
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		400	pF			
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs			

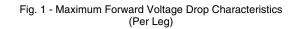
Note

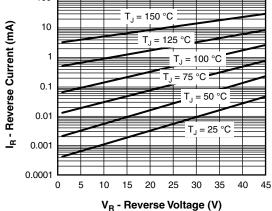
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

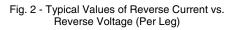
THERMAL - MECHA	THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction temperatu	re range	TJ		- 65 to 150	°C			
Maximum storage temperatu	re range	T _{Stg}		- 65 to 175	C			
Maximum thermal resistance junction to case per leg	3	R _{thJC}	DC operation	3.0				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W			
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	60				
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
Marking davias				MBR1	535CT			
Marking device			Case style TO-220AB		545CT			











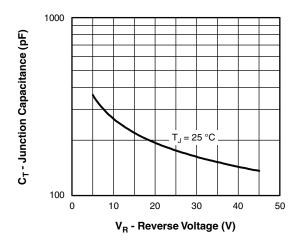
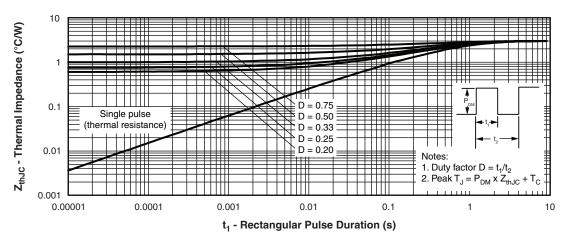
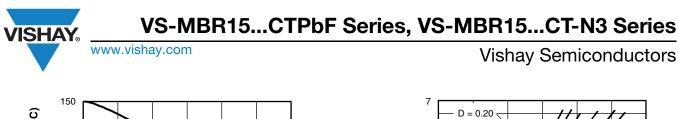


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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Average Power Loss (W)

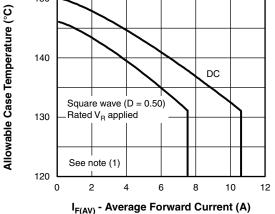


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

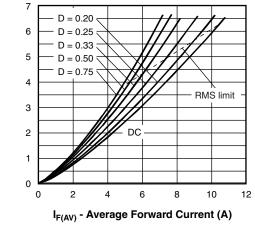


Fig. 6 - Forward Power Loss Characteristics

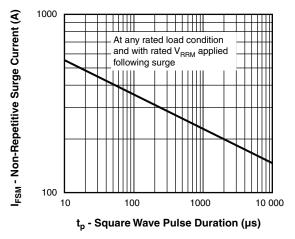


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \, \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \, \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

VS-MBR15...CTPbF Series, VS-MBR15...CT-N3 Series



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ORDERING INFORMATION TABLE

Device code	VS-	MBR	15	45	СТ	PbF												
		2	3	4	5	6												
	1	- Visl	nay Sem	niconduc	ctors pro	oduct												
	2	- Sch	ottky M	BR serie	es													
	3 ·	- Cur	rent rati	ng (15 =	= 15 A)	35	_	3	35	35 \	35 \/	35 V	35 V	35 V	35 V	35 V	35 V	35 V
	4	- Volt	age rati	ngs —								45 V						
	5	- CT	= Esser	ntial part	numbe	r]]]]	
	6 -	- Env	rironmer	ntal digit														
		• F	bF = Le	ad (Pb)	-free an	d RoHS	cc	or	m	mp	mpl	mplia	omplia	omplia	ompliar	mpliar	mplian	mplian

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR1535CTPbF	50	1000	Antistatic plastic tube				
VS-MBR1535CT-N3	50	1000	Antistatic plastic tube				
VS-MBR1545CTPbF	50	1000	Antistatic plastic tube				
VS-MBR1545CT-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions		www.vishay.com/doc?95222			
Dent mentions information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			
SPICE model		www.vishay.com/doc?95294			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIMETERS		METERS INCHES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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